

# MOS FIELD EFFECT TRANSISTOR 2SK3322

# SWITCHING N-CHANNEL POWER MOS FET

#### **DESCRIPTION**

The 2SK3322 is N-Channel DMOS FET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

#### **ORDERING INFORMATION**

L	PART NUMBER	PACKAGE
	2SK3322	TO-220AB (MP-25)
	2SK3322-S	TO-262
	2SK3322-ZJ	TO-263(MP-25ZJ)
	2SK3322-ZK	TO-263(MP-25ZK)

#### **FEATURES**

★ • Low gate charge :

 $Q_G = 15 \text{ nC TYP}$ . ( $V_{DD} = 450 \text{ V}$ ,  $V_{GS} = 10 \text{ V}$ ,  $I_D = 5.5 \text{ A}$ )

- Gate voltage rating: ±30 V
- · Low on-state resistance :

 $R_{DS(on)} = 2.2 \Omega MAX. (V_{GS} = 10 V, I_{D} = 2.8 A)$ 

- · Avalanche capability ratings
- · Surface mount package available

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	600	V
Gate to Source Voltage (Vps = 0 V)	Vgss	±30	V
Drain Current (DC) (Tc = 25°C)	I <sub>D(DC)</sub>	±5.5	Α
Drain Current (pulse) Note1	D(pulse)	±20	Α
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T1</sub>	1.5	W
Total Power Dissipation (Tc = 25°C)	P <sub>T2</sub>	65	W
Channel Temperature	$T_ch$	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note2	IAS	4.0	Α
Single Avalanche Energy Note2	Eas	10.7	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

2. Starting Tch = 25°C, VDD = 150 V, Rg = 25  $\Omega$ , Vgs = 20  $\rightarrow$  0 V

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90%

90%

10%

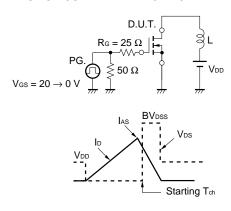


# **★** ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

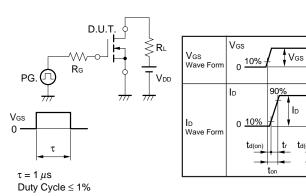
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	Vps = 600 V, Vgs = 0 V			100	μΑ
Gate Leakage Current	Igss	Vgs = ±30 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.5		3.5	٧
Forward Transfer Admittance Note	yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.8 A	1.0			S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, ID = 2.8 A		1.7	2.2	Ω
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V,		550		pF
Output Capacitance	Coss	$V_{GS} = 0 V$ ,		115		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		13		pF
Turn-on Delay Time	td(on)	VDD = 150 V, ID = 2.8 A,		12		Ns
Rise Time	tr	Vgs = 10 V,		10		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		35		ns
Fall Time	tf			12		ns
Total Gate Charge	Q <sub>G</sub>	VDD = 450 V,		15		nC
Gate to Source Charge	Qgs	Vgs = 10 V,		4		nC
Gate to Drain Charge	Q <sub>GD</sub>	lo = 5.5 A		4.4		nC
Body Diode Forward Voltage Note	V <sub>F(S-D)</sub>	IF = 5.5 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 5.5 A, VGS = 0 V,		1.6		μs
Reverse Recovery Charge	Qrr	di/dt = 50 A/μs		5.3		μC

Note Pulsed

#### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**



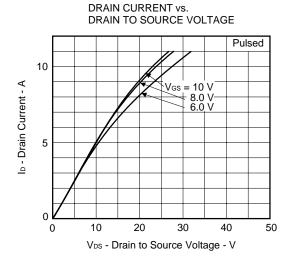
### TEST CIRCUIT 2 SWITCHING TIME

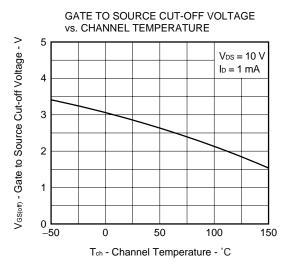


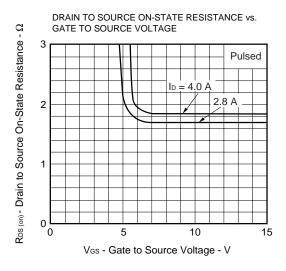
# **TEST CIRCUIT 3 GATE CHARGE**

$$\begin{array}{c|c} D.U.T. \\ \hline \\ I_G = 2 \text{ mA} \\ \hline \\ PG. \\ \hline \\ \end{array} \begin{array}{c} R_L \\ \hline \\ V_{DD} \\ \hline \end{array}$$

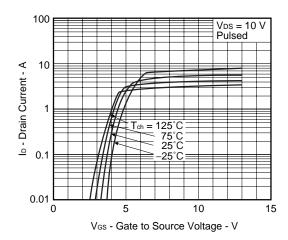
#### TYPICAL CHARACTERISTICS (TA = 25°C)



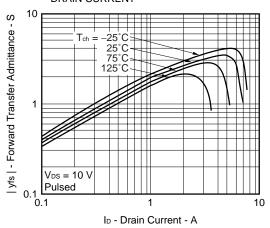


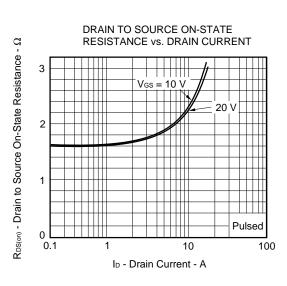


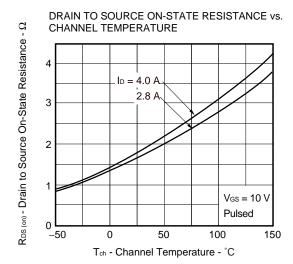
#### FORWARD TRANSFER CHARACTERISTICS

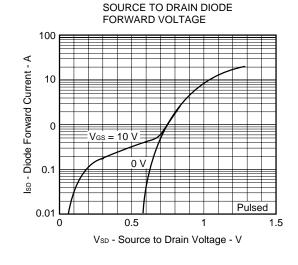


# FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

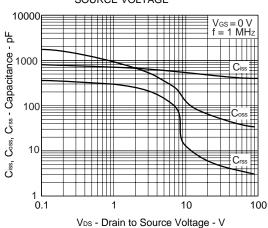




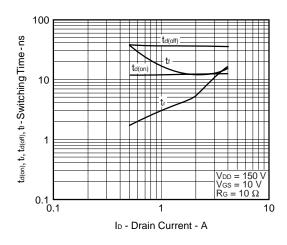




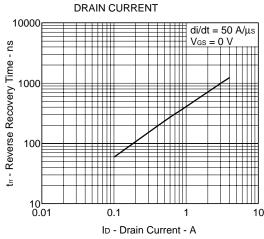
# CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



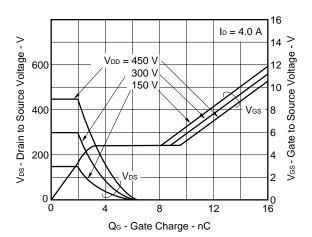
SWITCHING CHARACTERISTICS

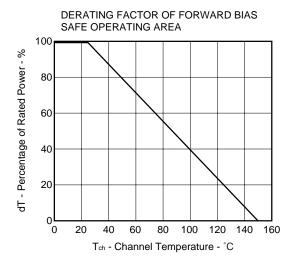


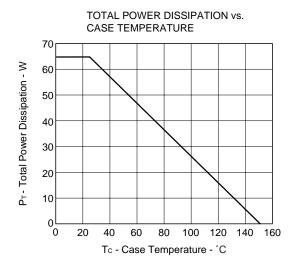
# REVERSE RECOVERY TIME vs.



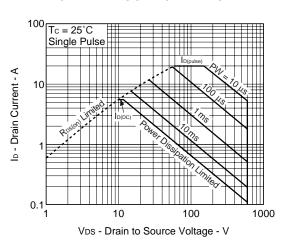
#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS



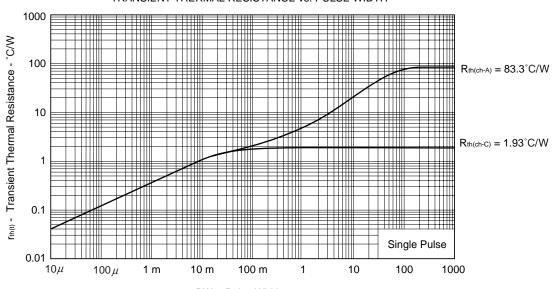




#### FORWARD BIAS SAFE OPERATING AREA

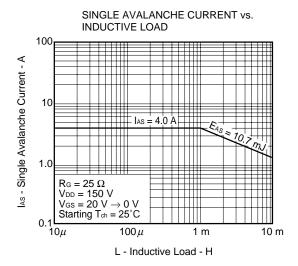


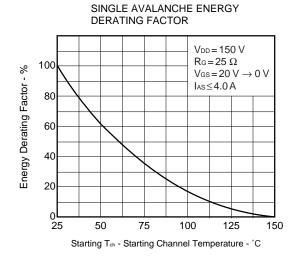
## TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



PW - Pulse Width - sec

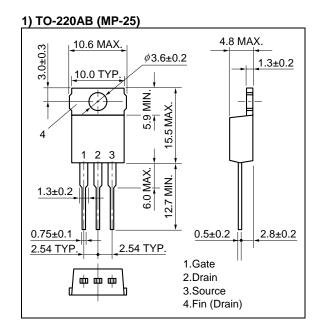
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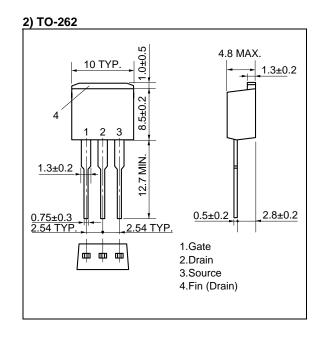


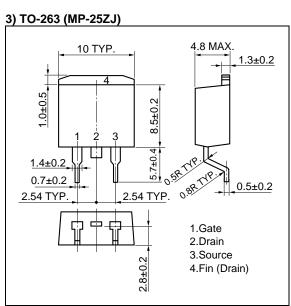


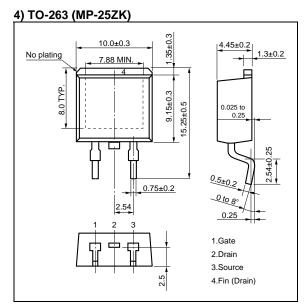


# **★ PACKAGE DRAWINGS (Unit: mm)**

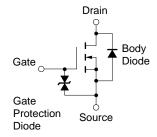








#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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