

Features

- Low-noise Low Drop Out Voltage Regulator
- 2.8V Fixed Output Voltage
- 3V to 5.5V Supply Operation
- 130 mA Maximum Load Current
- Power-down Mode Consumption Less Than 1 μ A
- More Than 70 dB (Typical) PSRR at 1 kHz
- 29 μ V_{RMS} Output Noise
- 0.35 μ m CMOS Technology
- Typical Application: Radio Section Supply in Mobile Terminals

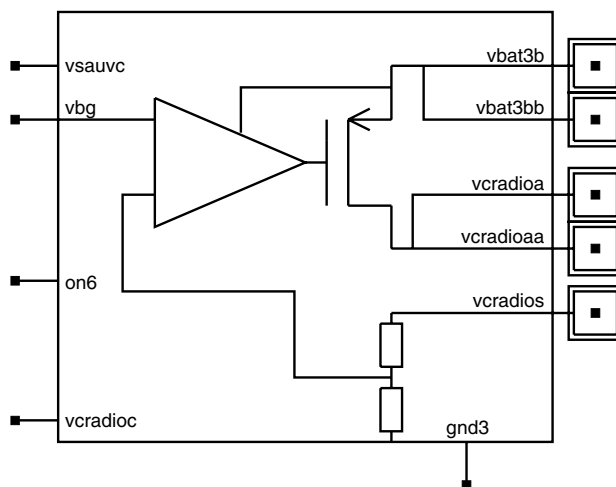
Description

RE027 is a Low Drop Out (LDO) voltage regulator macrocell with a fixed 2.8V output voltage, rated for loads up to 130 mA. It is designed to be integrated with other analog cells, digital logic, microcontrollers, DSP cores and memory blocks into system-on-chip products.

The circuit consists of a PMOS pass device, an error amplifier and a feedback resistive network, sized to achieve the required closed loop gain. These blocks make up the regulating loop. An over-current and short circuit protection circuit has been included to limit the output current delivered by the regulator, thus avoiding destruction in case of a short circuit.

An external reference voltage (bandgap voltage) is necessary for correct functionality. The target reference voltage is 1.231V delivered, for example, by BG019. Double pads on the supply voltage V_{BAT3B}/V_{BAT3BB} and output voltage $V_{CRADIOA}/V_{CRADIOAA}$ are used to reduce the total output resistance. Current reference is generated inside the cell through a circuit supplied by a $2.5V \pm 0.1V$ regulated input voltage on V_{SAUVC} . Remote sense terminal $V_{CRADIOS}$ provides regulation at the load by connecting it to the output terminal near a critical point to improve performance of the regulator (e.g., connecting them at the package pin by double-bonding, thus avoiding the bonding resistance influence). A capacitor of 2.2 μ F connected from $V_{CRADIOA}/V_{CRADIOAA}$ to ground is needed as external compensation.

Figure 1. Symbol⁽¹⁾



Note: 1. Pin names are written as they appear on the user screen when the symbol is opened in the design tool environment.



**Embedded ASIC
Macrocell:
Power
Management for
Mobile
Terminals (PM)**

**RE027
Fixed 2.8V
130 mA
Low-noise LDO
Voltage
Regulator**

Rev. 2701A-01/02



Absolute Maximum Ratings*

V_{IN}	-0.3V to 6.5V
Digital Signals.....	-0.3V to 5.5V
Output Current.....	Internally Limited
Junction Temperature	-40°C to 150°C

*NOTICE: Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Specifications⁽¹⁾

$T_J = -20^{\circ}\text{C}$ to 125°C , $V_{BAT3B}/V_{BAT3BB} = 3\text{V}$ to 5.5V unless otherwise specified, output capacitance = $2.2\ \mu\text{F}$.

Table 1. Electrical Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
V_{BAT3B}/V_{BAT3BB}	Operating Supply Voltage		3		5.5	V
V_{SAUVC}	Auxiliary Operating Supply Voltage		2.4	2.5	2.6	V
T_J	Junction Temperature Range		-20		125	°C
$V_{CRADIOA}/V_{CRADIOAA}$	Output Voltage		2.75	2.8	2.85	V
$I_{CRADIOA}/I_{CRADIOAA}$	Output Current				130	mA
I_{QQ}	Quiescent Current			195	236	μA
ΔV_{DC}	Line Regulation	$V_{BAT3B}/V_{BAT3BB} = 3\text{V}$ to 3.4V ; $I_{CRADIOA}/I_{CRADIOAA} = 130\text{ mA}$		0.05	0.1	mV
ΔV_{TRAN}	Transient Line Regulation	$V_{BAT3B}/V_{BAT3BB} = 3\text{V}$ to 3.4V ; $I_{CRADIOA}/I_{CRADIOAA} = 130\text{ mA}$ rise time = fall time = $5\ \mu\text{s}$		1.5	2.85	mV
ΔV_{DC}	Load Regulation	10% - 90% of max $I_{CRADIOA}/I_{CRADIOAA}$; $V_{BAT3B}/V_{BAT3BB} = 5.5\text{V}$		0.4	0.9	mV
ΔV_{TRAN}	Transient Load Regulation	10% - 90% of max $I_{CRADIOA}/I_{CRADIOAA}$; rise time = fall time = $5\ \mu\text{s}$		1.2	2.4	mV

Table 1. Electrical Characteristics (Continued)

Symbol	Parameter	Condition		Min	Typ	Max	Unit
PSRR	Power Supply Rejection Ratio at Full Load	V _{BAT} = 3V	@ 100 Hz	-78		-68	dB
			@ 1 kHz	-76		-65	dB
			@ 20 kHz	-56		-43	dB
			@ 100 kHz	-43		-29	dB
		V _{BAT} = 4.25V	@ 100 Hz	-90		-73	dB
			@ 1 kHz	-89		-73	dB
			@ 20 kHz	-71		-66	dB
			@ 100 kHz	-59		-53	dB
		V _{BAT} = 5.5V	@ 100 Hz	-86		-69	dB
			@ 1 kHz	-85		-69	dB
			@ 20 kHz	-72		-66	dB
			@ 100 kHz	-61		-53	dB
V _N	Output Noise ⁽²⁾	Bandwidth = 10 Hz to 100 kHz; output current = 130 mA			29	37	μ V _{RMS}
T _R	Rise Time	100% of I _{CRADIOA} /I _{CRADIOAA} ; 10% - 90% of V _{CRADIOA} /V _{CRADIOAA}				50	μs
I _{SD}	Shut Down Current					1	μA
I _{CC}	Short-circuit Current Threshold					460	mA

Notes: 1. Obtained by considering the parasitics of a TFBGA100 Package.
2. Obtained by using BG019 as reference voltage generator.

Control Modes

All digital signals are referred to the supply voltage $V_{BAT3B/BAT3BB}$.

Table 2. Truth Table

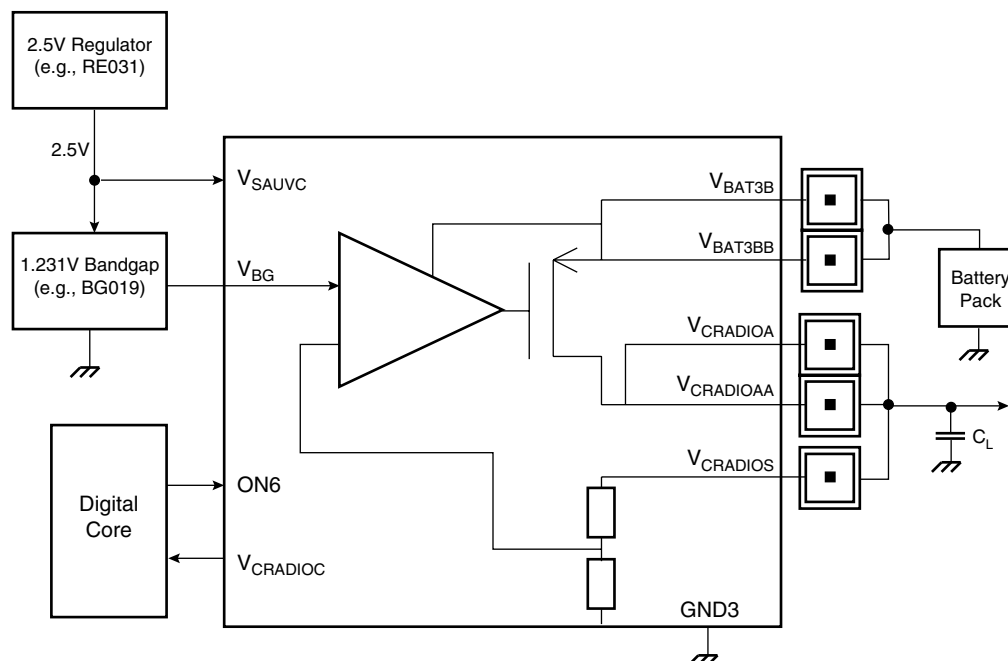
ON6	$V_{CRADIOA}/V_{CRADIOAA}$
0	Power down (High-Z)
1	Power on, $V_{CRADIOA}/V_{CRADIOAA} = 2.8V$

Application Example

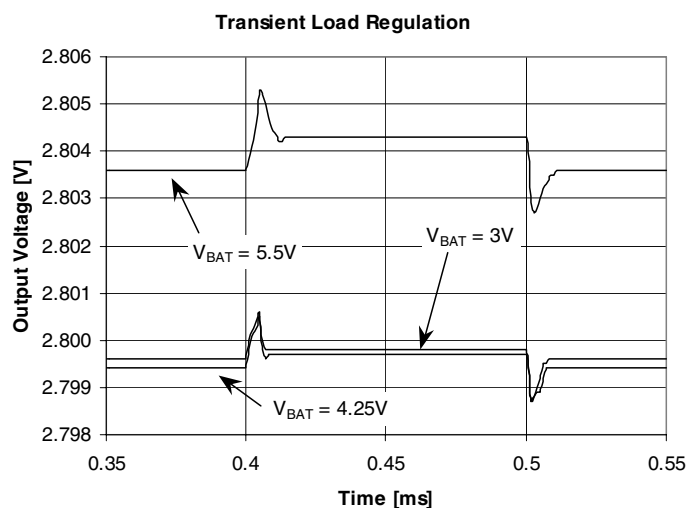
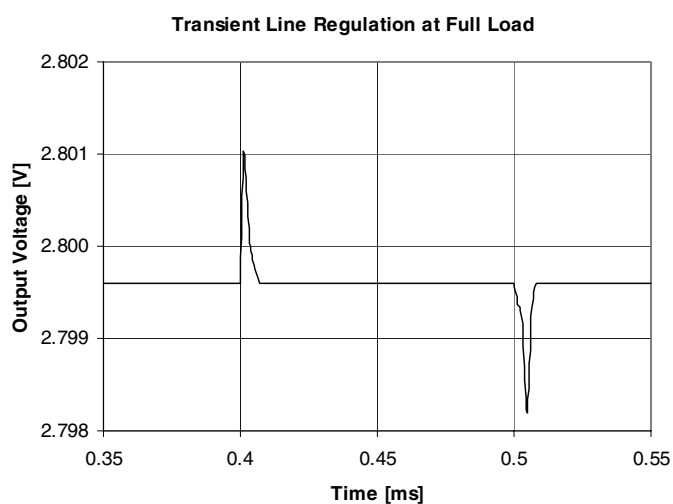
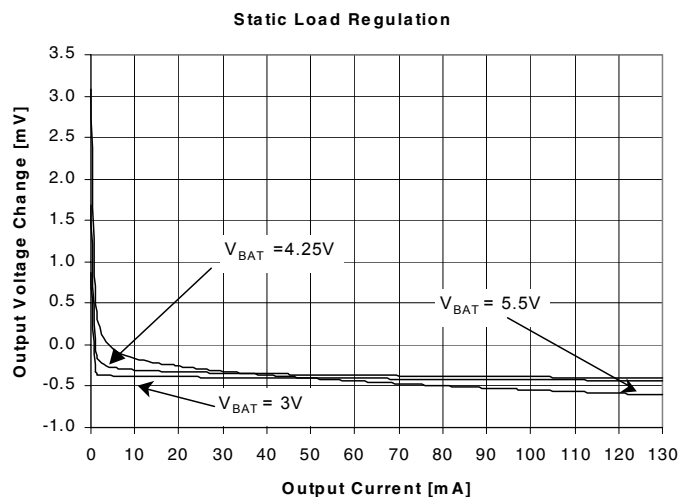
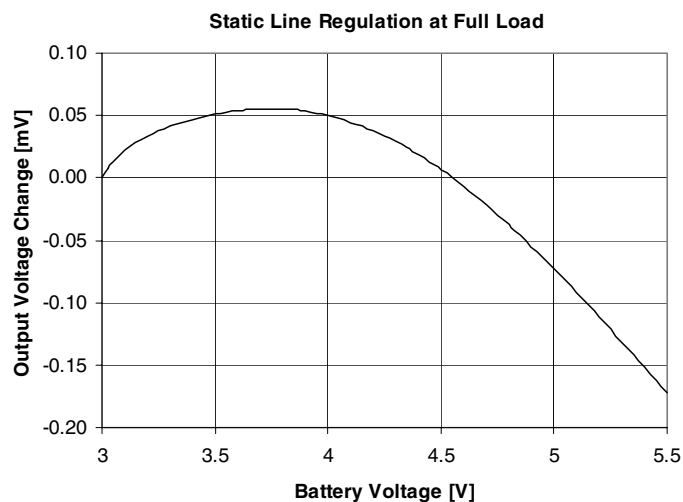
A ceramic capacitor of 2.2 μF with ESR between 20 m Ω and 250 m Ω connected from $V_{CRADIOA}/V_{CRADIOAA}$ to ground is needed for external compensation.

Description	Min	Typ	Max	Units
Capacitor (C_L)	1.8	2.2	2.6	μF

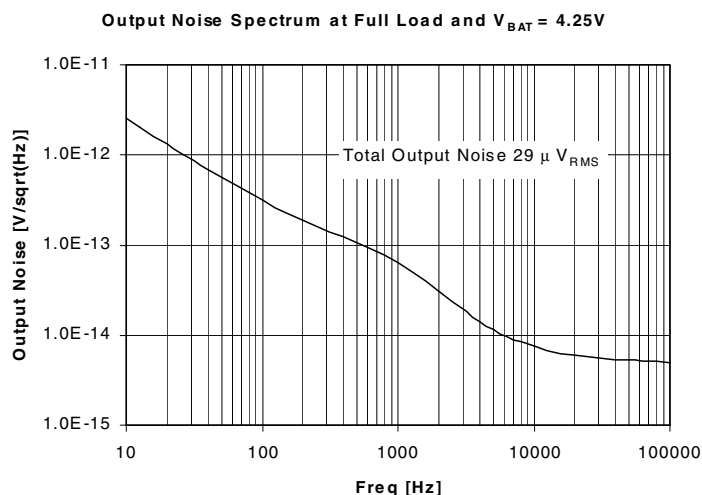
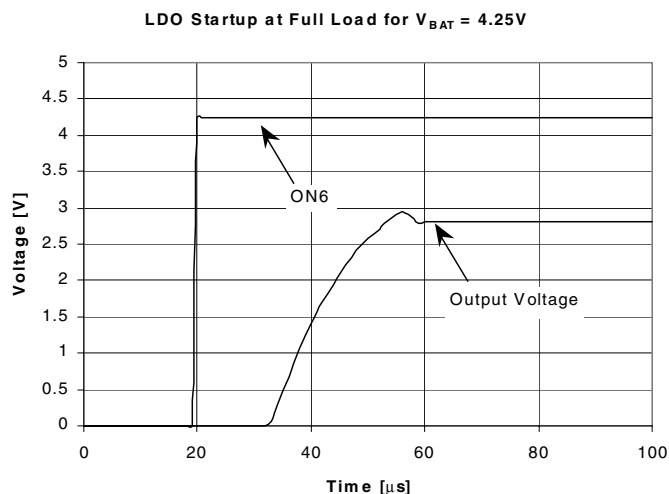
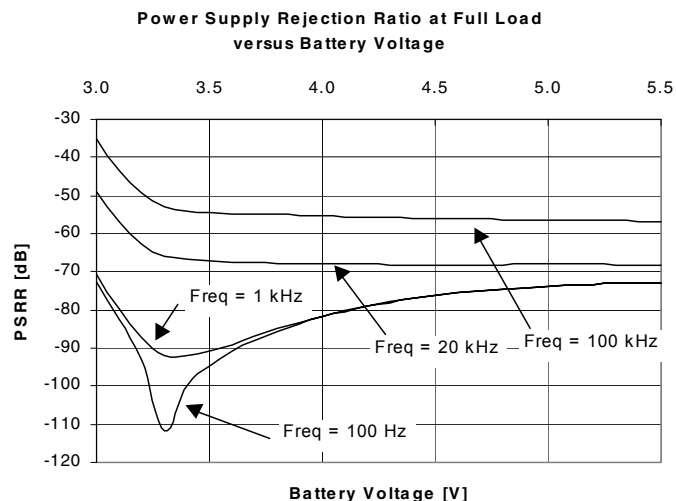
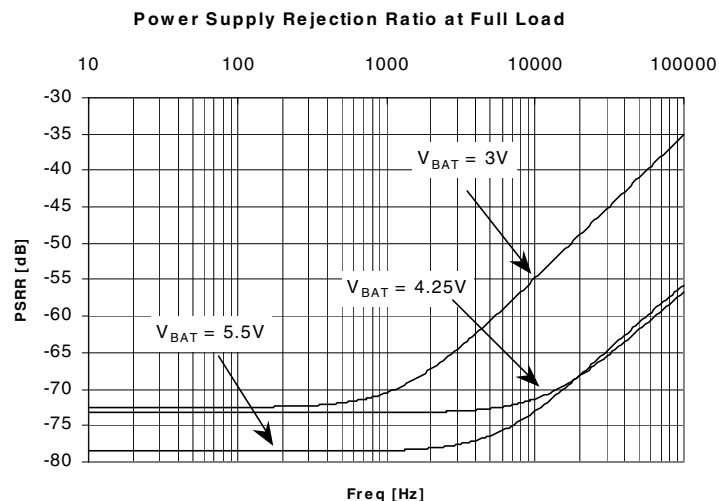
Figure 3. Application Example



Typical Performance Characteristics (Conditions specified on page 8)



Typical Performance Characteristics (Conditions specified on page 8)

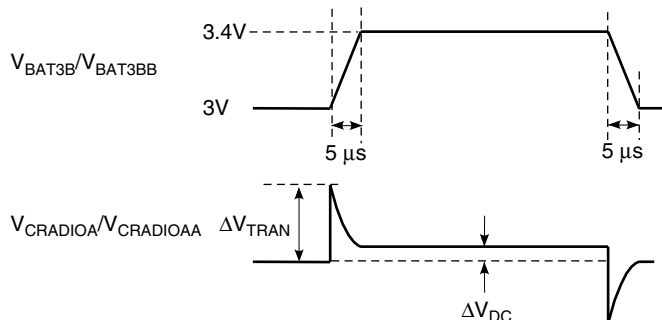


Terminology

Line Regulation

Measures the maximum transient and DC variations of the output voltage of the LDO when the supply changes between two specified values with fixed load current; minimum rise time and fall time is 5 μ s.

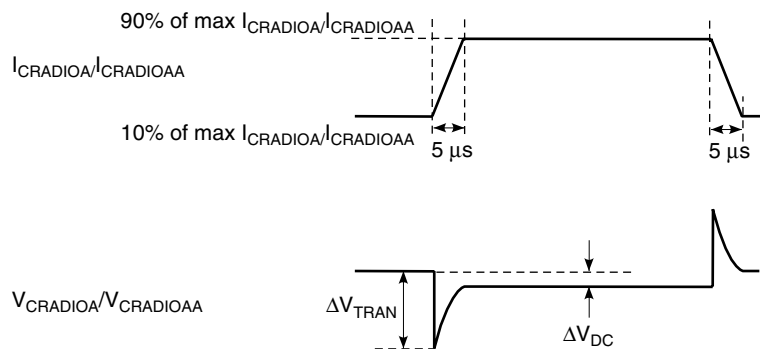
Figure 4. Line Regulation



Load Regulation

Measures the maximum transient and DC variations of the output voltage of the LDO when the load current changes between two specified values with fixed power supply; minimum rise time and fall time is 5 μ s.

Figure 5. Load Regulation





Atmel Headquarters

Corporate Headquarters

2325 Orchard Parkway
San Jose, CA 95131
TEL 1(408) 441-0311
FAX 1(408) 487-2600

Europe

Atmel SarL
Route des Arsenaux 41
Casa Postale 80
CH-1705 Fribourg
Switzerland
TEL (41) 26-426-5555
FAX (41) 26-426-5500

Asia

Atmel Asia, Ltd.
Room 1219
Chinachem Golden Plaza
77 Mody Road Tsimhatsui
East Kowloon
Hong Kong
TEL (852) 2721-9778
FAX (852) 2722-1369

Japan

Atmel Japan K.K.
9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
TEL (81) 3-3523-3551
FAX (81) 3-3523-7581

Atmel Operations

Memory

Atmel Corporate
2325 Orchard Parkway
San Jose, CA 95131
TEL 1(408) 436-4270
FAX 1(408) 436-4314

Microcontrollers

Atmel Corporate
2325 Orchard Parkway
San Jose, CA 95131
TEL 1(408) 436-4270
FAX 1(408) 436-4314

Atmel Nantes
La Chantrerie
BP 70602
44306 Nantes Cedex 3, France
TEL (33) 2-40-18-18-18
FAX (33) 2-40-18-19-60

ASIC/ASSP/Smart Cards

Atmel Rousset
Zone Industrielle
13106 Rousset Cedex, France
TEL (33) 4-42-53-60-00
FAX (33) 4-42-53-60-01

Atmel Colorado Springs
1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906
TEL 1(719) 576-3300
FAX 1(719) 540-1759

Atmel Smart Card ICs
Scottish Enterprise Technology Park
Maxwell Building
East Kilbride G75 0QR, Scotland
TEL (44) 1355-803-000
FAX (44) 1355-242-743

RF/Automotive

Atmel Heilbronn
Theresienstrasse 2
Postfach 3535
74025 Heilbronn, Germany
TEL (49) 71-31-67-0
FAX (49) 71-31-67-2340

Atmel Colorado Springs
1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906
TEL 1(719) 576-3300
FAX 1(719) 540-1759

Biometrics/Imaging/Hi-Rel MPU/ High Speed Converters/RF Datacom

Atmel Grenoble
Avenue de Rochepleine
BP 123
38521 Saint-Egreve Cedex, France
TEL (33) 4-76-58-30-00
FAX (33) 4-76-58-34-80

e-mail

literature@atmel.com

Web Site

<http://www.atmel.com>

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