Advance Information

Thyristor Surge ProtectorsHigh Voltage Bidirectional TSPD

These Thyristor Surge Protective devices (TSPD) prevent overvoltage damage to sensitive circuits by lightning, induction and power line crossings. They are breakover–triggered crowbar protectors. Turn–off occurs when the surge current falls below the holding current value.

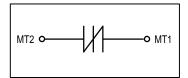
Secondary protection applications for electronic telecom equipment at customer premises.

- Outstanding High Surge Current Capability: 150 Amps 10x1000 μsec Guaranteed at the extended temp range of –20°C to 65°C
- Bidirectional Protection in a Single Device
- · Little Change of Voltage Limit with Transient Amplitude or Rate
- Freedom from Wearout Mechanisms Present in Non–Semiconductor Devices
- Fail—Safe, Shorts When Overstressed, Preventing Continued Unprotected Operation.
- Surface Mount Technology (SMT)
- Complies with GR1089 Second Level Surge Spec at 500 Amps 2x10 μsec waveforms
- Supplied in 12mm Tape and Reel, 2500 units per reel. (T3 suffix)

MMT10B230T3 MMT10B260T3 MMT10B310T3

Motorola preferred devices

BIDIRECTIONAL THYRISTOR SURGE PROTECTOR





DEVICE RATINGS: @ 25°C unless otherwise noted

Parameter		Value	Unit
Off-State Voltage — Maximum MMT10B230T3 MMT10B260T3 MMT10B310T3	V _{DM}	±170 ±200 ±270	Volts
Minimum Impulse Surge Short Circuit Current Non–Repetitive double exponential wave, Notes 1, 2 10 x 1000 μsec (–20°C to +65°C) 2 x 10 μsec 10 x 700 μsec		±150 ±500 ±180	A(pk)
Maximum Non–Repetitive Rate of Change of On–State Current Double Exponential Waveform, R = 2.0, L = 1.5 μ H, C = 1.67 μ F, I _{pk} = 110A	di/dt	±100	A/μs

DEVICE THERMAL RATINGS

Operating Temperature Range Blocking or Conducting State	T _{J1}	-40 to +125	°C
Overload Junction Temperature — Maximum Conducting State Only	T _{J2}	+175	°C
Instantaneous Peak Power Dissipation (I _{pk} = 100A, 10x100 μsec @ 25°C)	PpK	4000	W

This document contains information on a new product. Specifications and information herein are subject to change without notice. **Preferred** devices are Motorola recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristics		Symbol	Min	Тур	Max	Unit
Breakover Voltage (Both polarities) ($dv/dt = 100 \text{ V/}\mu\text{s}$, ISC = 1.0 A, Vdc = 1000 V) (+65°C)	MMT10B230T3 MMT10B260T3 MMT10B310T3	V(BO)	_ _ _	_ _ _	265 320 365	Volts
(+03 C)	MMT10B230T3 MMT10B260T3 MMT10B310T3		_ _ _	_ _ _	290 340 400	
Breakover Voltage (Both polarities) (f = 60 Hz, I _{SC} = 1.0 A(rms), V _{OC} = 1000 V(rms), R _I = 1.0 k Ω , t = 0.5 cycle, Note 2) (+65°C)	MMT10B230T3 MMT10B260T3 MMT10B310T3	V(BO)	_ _ _	_ _ _	265 320 365	Volts
(100 0)	MMT10B230T3 MMT10B260T3 MMT10B310T3		_ _ _	_ _ _	290 340 400	
Breakover Voltage Temperature Coefficient		dV _(BO) /dT _J	_	0.08	_	%/°C
Breakdown Voltage ($I_{(BR)} = 1.0 \text{ mA}$) Both polarities	MMT10B230T3 MMT10B260T3 MMT10B310T3	V(BR)	_ _ _	190 240 280	_ _ _	Volts
Off State Current ($V_{D1} = 50 \text{ V}$) Both polarities ($V_{D2} = V_{DM}$) Both polarities		I _{D1} I _{D2}	_	_	2.0 5.0	μА
On–State Voltage (I _T = 1.0 A) (PW ≤ 300 μs, Duty Cycle ≤ 2%, Note 2)		VT	_	1.53	5.0	Volts
Breakover Current (f = 60 Hz, V _{DM} = 1000 V(rms), Rs Both polarities	$S = 1.0 \text{ k}\Omega$)	I _{BO}	_	260	_	mA
Holding Current (Both polarities)	Note 2 (+65°C)	lн	175 130	270 —	_	mA
Critical Rate of Rise of Off–State Voltage (Linear waveform, V _D = Rated V _{BR} , T _J = 25°C)		dv/dt	2000	_	_	V/µs
Capacitance (f = 1.0 MHz, 50 V, 1.0 V) (f = 1.0 MHz, 2.0 V, 15 mV)		СО		65 160	 200	pF

- 1. Allow cooling before testing second polarity.
- 2. Measured under pulse conditions to reduce heating.

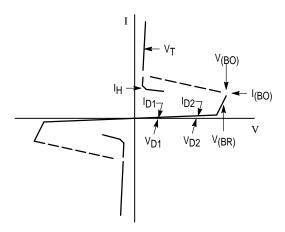


Figure 1. Voltage – Current Characteristics

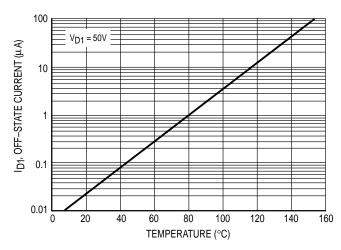


Figure 2. Off-State Current versus Temperature

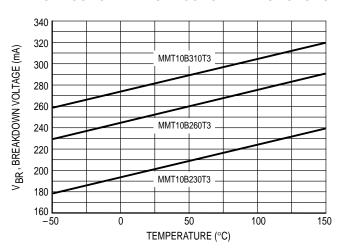


Figure 3. Breakdown Voltage versus Temperature

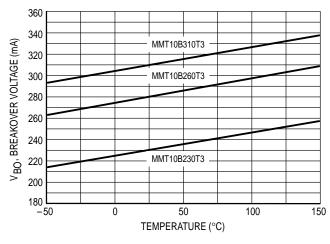


Figure 4. Breakover Voltage versus Temperature

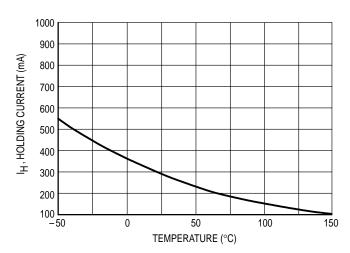


Figure 5. Holding Current versus Temperature

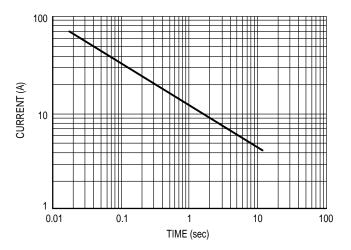
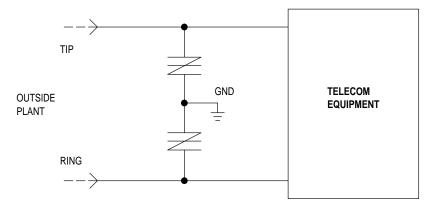
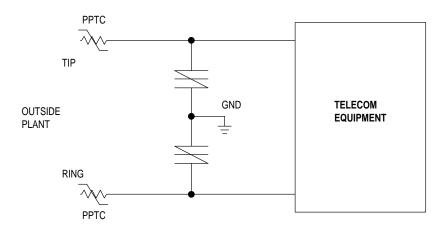
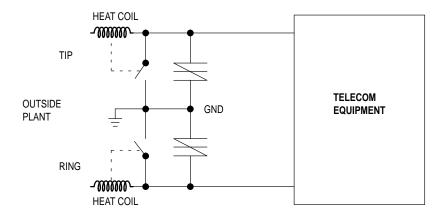


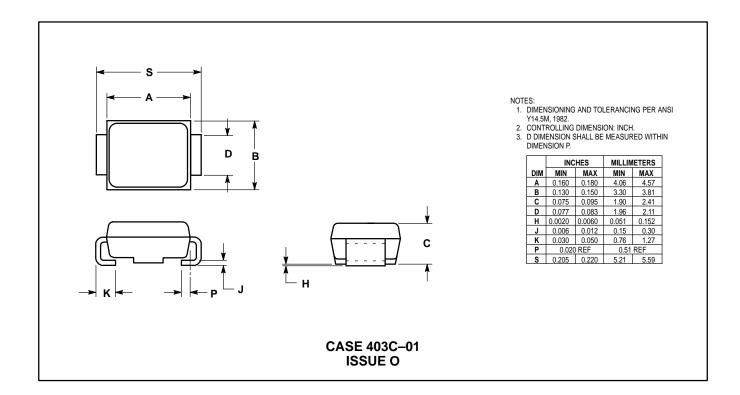
Figure 6. Peak Surge On-State Current versus Surge Current Duration







PACKAGE DIMENSIONS



NOTES

NOTES

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