National Semiconductor

# MILITARY DATA SHEET

## MNLM140-15H REV 0A0

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# THREE TERMINAL POSITIVE REGULATORS

## General Description

The LM140 monolithic 3-terminal positive voltage regulators employ internal current-limiting, thermal shutdown and safe-area compensation, making them essentially indestructible. If adequate heat sinking is provided, they can deliver over 0.5A output current. They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents.

Considerable effort was expended to make the entire series of regulators easy to use and minimize the number of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

## Industry Part Number

LM140

## NS Part Numbers

LM140H-15/883

## Prime Die

LM141

## Processing

MIL-STD-883, Method 5004

## Quality Conformance Inspection

MIL-STD-883, Method 5005

| Subgrp | Description | Temp ($^{O}$C) |
|---|---|---|
| 1 | Static tests at | +25 |
| 2 | Static tests at | +125 |
| 3 | Static tests at | -55 |
| 4 | Dynamic tests at | +25 |
| 5 | Dynamic tests at | +125 |
| 6 | Dynamic tests at | -55 |
| 7 | Functional tests at | +25 |
| 8A | Functional tests at | +125 |
| 8B | Functional tests at | -55 |
| 9 | Switching tests at | +25 |
| 10 | Switching tests at | +125 |
| 11 | Switching tests at | -55 |

## Features

- Output current in excess of 0.5A
- No external components
- Internal thermal overload protection
- Internal short circuit current-limiting
- Output transistor safe-area compensation

## (Absolute Maximum Ratings)

| | |
|---|---|
| DC Input Voltage | 35V |
| Internal Power Dissipation (Note 2) | Internally Limited |
| Maximum Junction Temperature | 150 C |
| Lead Temperature<br>Soldering (10 seconds) | 300 C |
| Thermal Resistance<br>ThetaJA<br>(Still Air)<br>(500 LF/Min Air Flow) | <br><br>232 C/W<br>77 C/W |
| ThetaJC | 15 C/W |
| ESD Susceptibility (Note 3) | 2KV |

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specification might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.
Note 2: The Maximum allowable power dissipation at any ambient temperature is a function of the maximum junction temperature for operation (TjMAX = 150 C), the junction-to-ambient thermal resistance (ThetaJA), and the ambient temperature (TA). PDMAX = (TjMAX - TA)/ThetaJA. If this dissipation is exceeded, the die temperature will rise above TjMAX and the electrical specifications do not apply. If the die temperature rises above 150 C, the device will do into thermal shutdown.
Note 3: Human body model, 100pF discharged through 1.5K Ohms

## Recommended Operating Conditions

(Note 1)

| | |
|---|---|
| Temperature Range (TA) (Note 2) | -55 C to +125 C |

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Note 2: The Maximum allowable power dissipation at any ambient temperature is a function of the maximum junction temperature for operation (TjMAX = 150 C), the junction-to-ambient thermal resistance (ThetaJA), and the ambient temperature (TA). PDMAX = (TjMAX - TA)/ThetaJA. If this dissipation is exceeded, the die temperature will rise above TjMAX and the electrical specifications do not apply. If the die temperature rises above 150 C, the device will go into thermal shutdown.

# Electrical Characteristics

## DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)
DC: Vin = 23V, Il = 350mA

| SYMBOL | PARAMETER | CONDITIONS | NOTES | PIN-NAME | MIN | MAX | UNIT | SUB-GROUPS |
|---|---|---|---|---|---|---|---|---|
| Vout | Output Voltage | Vin = 35V, Il = 5mA | | | 14.25 | 15.75 | V | 1 |
| | | | | | 14.40 | 15.60 | V | 1 |
| | | Vin = 18.5V | | | 14.25 | 15.75 | V | 1, 2, 3 |
| | | Vin = 18.5V, Il = 5mA | | | 14.5 | 15.75 | V | 1, 2, 3 |
| | | Vin = 30V, Il = 5mA | | | 14.25 | 15.75 | V | 1, 2, 3 |
| | | Vin = 30V | | | 14.25 | 15.75 | V | 1, 2, 3 |
| RLINE | Line Regulation | 17.5V <= Vin <= 30V, Il = 200mA | | | -60 | 60 | mV | 1 |
| | | 18.5V <= Vin <= 30V, Il = 200mA | | | -120 | 120 | mV | 2, 3 |
| | | 20V <= Vin <= 30V, Il = 200mA | | | -30 | 30 | mV | 1 |
| | | | | | -60 | 60 | mV | 2, 3 |
| RLOAD | Load Regulation | 5mA <= Il <= 500mA | | | -150 | 150 | mV | 1 |
| | | | | | -300 | 300 | mV | 2, 3 |
| | | 5mA <= Il <= 200mA | | | -75 | 75 | mV | 1 |
| | | | | | -150 | 150 | mV | 2, 3 |
| IQ | Quiescent Current | | | | | 7 | mA | 1, 2, 3 |
| Delta IQ | Quiescent Current Change | 17.5V <= Vin <= 30V, Il = 200mA | | | -0.8 | 0.8 | mA | 1, 2, 3 |
| | | 5mA <= Il <= 350mA | | | -0.5 | 0.5 | mA | 1, 2, 3 |
| Ipk | Peak Current | Vin - Vout = 7V | 1 | | 0.4 | 2.0 | A | 1, 2, 3 |
| Vdo | Dropout Voltage | | 2 | | | 2.5 | V | 1 |
| Ios | Short Circuit Current | Vin = 35V | | | | 1.0 | A | 1, 2, 3 |

## AC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)
AC: Vin = 23V, Il = 350mA

| | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| Rr | Ripple Rejection | Vin=20V, Il=125mA, ei=1Vrms, f=2.4KHz | | | 54 | | dB | 4, 5, 6 |

Note 1: Vout is set to 90 % Reference Voltage.
Note 2: Vdo = Vin - Vout when Vo is 95 % of Reference Voltage.

## Graphics and Diagrams

| GRAPHICS# | DESCRIPTION |
|---|---|
| H03ARC | (blank) |

**See attached graphics following this page.**