TOSHIBA SSM6K07FU

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE

S S M 6 K 0 7 F U

DC-DC CONVERTERS

HIGH SPEED SWITCHING APPLICATIONS

- Small Package
- Low on Resistance

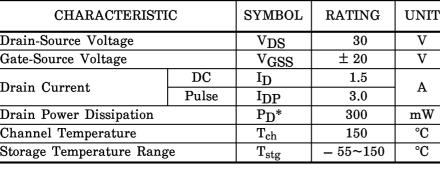
: $R_{\mbox{on}}=130\,\mbox{m}\Omega$ max. (@VGS = 10 V) : $R_{\mbox{on}}=220\,\mbox{m}\Omega$ max. (@VGS = 4 V)

Low Input Capacitance

: $C_{ISS} = 102 pF Typ$. : $C_{rSS} = 22 pF Typ$.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Drain-Source Voltage		$v_{ m DS}$	30	V	
Gate-Source Voltage		v_{GSS}	± 20	V	
Drain Current	DC	$I_{\mathbf{D}}$	1.5	A	
	Pulse	I_{DP}	3.0		
Drain Power Dissipation		PD*	300	mW	
Channel Temperature		$\mathrm{T_{ch}}$	150	°C	
Storage Temperature Range		$\mathrm{T_{stg}}$	- 55~150	°C	

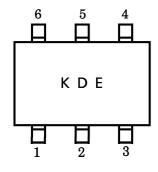


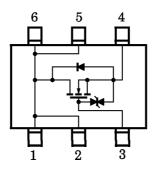
* Mounted on FR4 Board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad} : 0.32 \text{ mm}^2 \times 6)$

MARKING

EQUIVALENT CIRCUIT (TOP VIEW)





HANDLING PRECAUTION

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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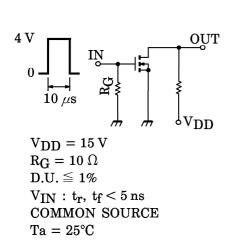
Unit in mm

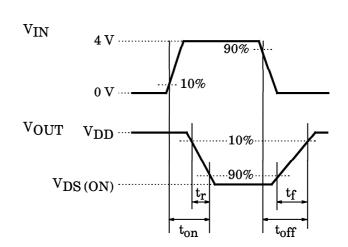
ELECTRICAL	CHARACTERISTICS	(Ta =	25°C)
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CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage	Current	$I_{ m GSS}$	$V_{GS} = \pm 16 V, V_{DS} = 0$	_	_	± 1	μ A
Drain-Source Breakdown Voltage		V (BR) DSS	$I_{\mathrm{D}}=1\mathrm{mA},~\mathrm{V}_{\mathrm{GS}}=0$	30	_	_	V
Drain Cut-off	Current	${ m I}_{ m DSS}$	$V_{DS} = 30 \text{ V}, \ V_{GS} = 0$	_		1	μ A
Gate Threshol	d Voltage	$ m V_{th}$	$V_{DS} = 5 \text{ V}, I_{D} = 0.1 \text{ mA}$	1.1	_	1.8	V
Forward Trans	sfer Admittance	$ Y_{fs} $	$V_{DS} = 5 \text{ V}, I_{D} = 0.75 \text{ A}$ (Note)	1.0	_	_	S
Drain-Source ON Resistance		R _{DS} (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 10 \text{ V}$ (Note)	_	105	130	$\mathbf{m}\Omega$
		R _{DS} (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 4 \text{ V}$ (Note)	_	170	220	$\mathbf{m}\Omega$
		R _{DS} (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 3.3 \text{ V} \text{ (Note)}$	_	230	500	$\mathbf{m}\Omega$
Input Capacita	ance	$\mathrm{c}_{\mathrm{iss}}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	102	_	pF
Reverse Trans	fer Capacitance	$\mathrm{C}_{\mathrm{rss}}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	22	_	pF
Output Capacitance Coss		C_{oss}	$V_{DS} = 15 \text{ V}, \ V_{GS} = 0, \ f = 1 \text{ MHz}$		57		рF
Switching	Turn-on Time	t_{on}	$V_{DD} = 15 \text{ V}, I_D = 0.75 \text{ A},$	_	46	_	ns
Time	Turn-off Time	$t_{ m off}$	$V_{GS} = 0 \sim 4 \text{ V}, R_G = 10 \Omega$	_	65	_	ns

(Note): Pulse test

SWITCHING TIME TEST CIRCUIT





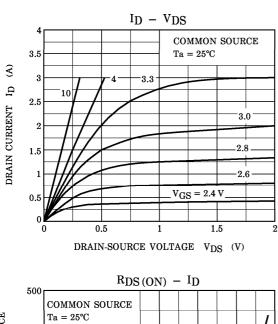
PRECAUTION

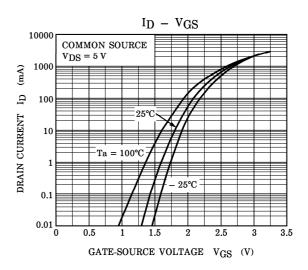
 V_{th} can be expressed as voltage between gate and source when low operating current value is $I_D=100~\mu A$ for this product. For normal switching operation, $V_{GS\,(on)}$ requires higher voltage than V_{th} and $V_{GS\,(off)}$ requires lower voltage than V_{th} .

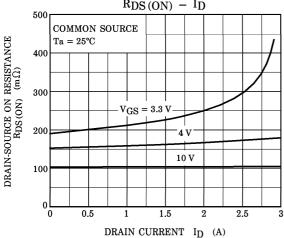
(Relationship can be established as follows: $V_{GS(off)} < V_{th} < V_{GS(on)}$)

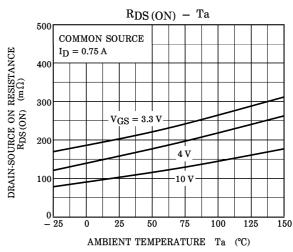
Please take this into consideration for using the device

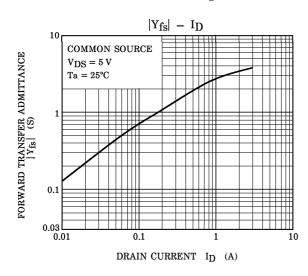
VGS recommended voltage of 4 V or higher to turn on this product.

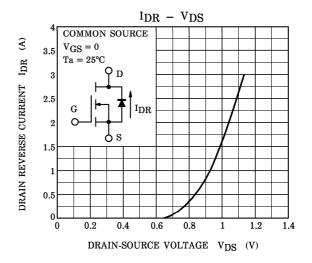


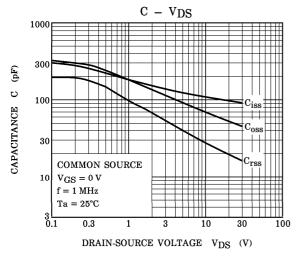


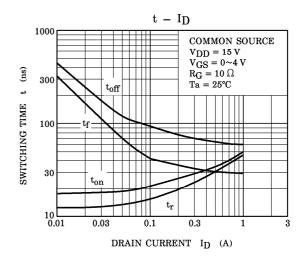


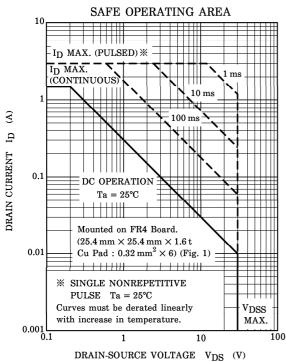












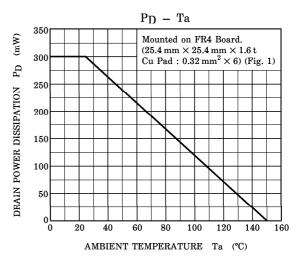


Fig.1 : 25.4 mm \times 25.4 mm \times 1.6 t, Cu Pad : 0.32 mm² \times 6

