TOSHIBA MT6L50AT

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# M T 6 L 5 0 A T

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

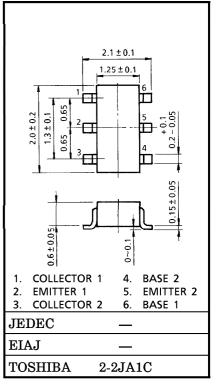
TWO devices are built in to the super-thin and ultra super mini (6 pins) package: TU6

#### MOUNTED DEVICES

	Q1 : SSM (TESM)	Q2 : SSM (TESM)
Three-pins (SSM/TESM) mold	2SC5256	MT3S04AS
products are corresponded.	(5256FT)	(MT3S04AT)

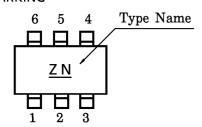
#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	Q1	Q2	UNIT
Collector-Base Voltage	$V_{\mathrm{CBO}}$	15	10	V
Collector-Emitter Voltage	$v_{CEO}$	7	5	V
Emitter-Base Voltage	$v_{\mathrm{EBO}}$	1.5	2	V
Collector Current	$I_{\mathbf{C}}$	40	40	mA
Base Current	$I_{\mathbf{B}}$	20	10	mA
Collector Power Dissipation	$P_{\mathbf{C}}$	200		mW
Junction Temperature	$T_{j}$	125		$^{\circ}\mathrm{C}$
Storage Temperature Range	$ m T_{stg}$	-55~125		°C

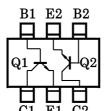


Unit in mm

#### **MARKING**



## PIN ASSIGNMENT (TOP VIEW)



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# ELECTRICAL CHARACTERISTICS Q1 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	$I_{\mathrm{CBO}}$	$V_{CB} = 10 \text{ V}, I_{E} = 0$	_	_	1	$\mu$ A
Emitter Cut-off Current	$I_{ m EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	$\mu$ A
DC Current Gain	$_{ m h_{FE}}$	$V_{\mathrm{CE}} = 5  \mathrm{V},  \mathrm{I_{\mathrm{C}}} = 20  \mathrm{mA}$	50	_	160	_
Transition Frequency	${ m f_T}$	$ m V_{CE} = 5~V,~I_{C} = 20~mA$	10	12	_	GHz
Insertion Gain	$ \mathrm{S}_{21\mathrm{e}} ^2$	$V_{ m CE} = 5   m V,  I_{ m C} = 20   m mA, \ f = 2000   m MHz$	5	7.8	_	dB
Noise Figure	NF	$V_{CE} = 5 \text{ V}, I_{C} = 5 \text{ mA},$ f = 2000  MHz	_	1.5	3	dB
Reverse Transfer Capacitance	$\mathrm{C_{re}}$	$V_{CB} = 5 \text{ V}, I_{E} = 0,$ f = 1  MHz (Note)		0.5	0.95	рF

## ELECTRICAL CHARACTERISTICS Q2 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	$I_{\mathrm{CBO}}$	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	$\mu$ A
Emitter Cut-off Current	${ m I}_{ m EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	$\mu$ A
DC Current Gain	${ m h_{FE}}$	$V_{ m CE}=1 m V,I_{ m C}=5 m mA$	80	_	160	_
Transition Frequency	f <sub>T</sub> (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$	2	4.5	_	GHz
	f <sub>T</sub> (2)	$V_{CE} = 3 \text{ V}, I_{C} = 7 \text{ mA}$	5	7	_	GHz
Incertion (+ain -	$ S_{21e} ^2$ (1)	$V_{\mathrm{CE}} = 1  \mathrm{V},  \mathrm{I}_{\mathrm{C}} = 5  \mathrm{mA},  \mathrm{f} = 1  \mathrm{GHz}$	_	8.5	_	dB
	$ S_{21e} ^2$ (2)	$V_{CE} = 3 V$ , $I_{C} = 20 \text{ mA}$ , $f = 1 \text{ GHz}$	7.5	11	_	dB
Noise Figure	NF (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 1 \text{ GHz}$	_	1.3	2.2	dB
	NF (2)	$V_{\mathrm{CE}} = 3  \mathrm{V},  \mathrm{I_{\mathrm{C}}} = 7  \mathrm{mA},  \mathrm{f} = 1  \mathrm{GHz}$	_	1.2	2	dB
Reverse Transfer Capacitance	$\mathrm{c}_{\mathrm{re}}$	$V_{\mathrm{CB}} = 1  \mathrm{V},  \mathrm{I_E} = 0,$ $\mathrm{f} = 1  \mathrm{MHz}  (\mathrm{Note})$	_	0.9	1.25	pF

(Note):  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

## HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.