



PRELIMINARY

## LOW-VOLTAGE OPERATION TINY C-MOS SINGLE OPERATIONAL AMPLIFIER

### ■ GENERAL DESCRIPTION

The NJU7011, 12 and 13 are single C-MOS operational amplifier operated on a single-power-supply, low voltage and low operating current.

The minimum operating voltage is 1V and the output stage permits output signals to swing between both of the supply rails.

The input bias current is as low as less than 1pA, consequently the very small signal around the ground level can be amplified.

Furthermore, this series is packaged with very small MTP-5, therefore it can be especially applied to portable items.

### ■ PACKAGE OUTLINE



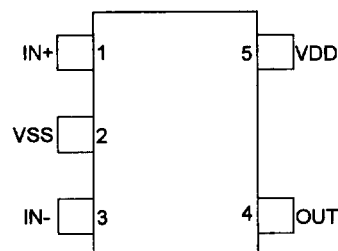
NJU701XF

### ■ FEATURES

- Single-Power-Supply
- Wide Operating Voltage ( $V_{DD}=1\sim 5.5V$ )
- Wide Output Swing Range ( $V_{OM}\geq 2.9V$  min. at  $V_{DD}=3.0V$ )
- Low Operating Current
- Low Bias Current ( $I_{IB}=1pA$  typ.)
- Compensation Capacitor Incorporated
- Package Outline MTP-5
- C-MOS Technology

### ■ PIN CONFIGURATION

(Top View)



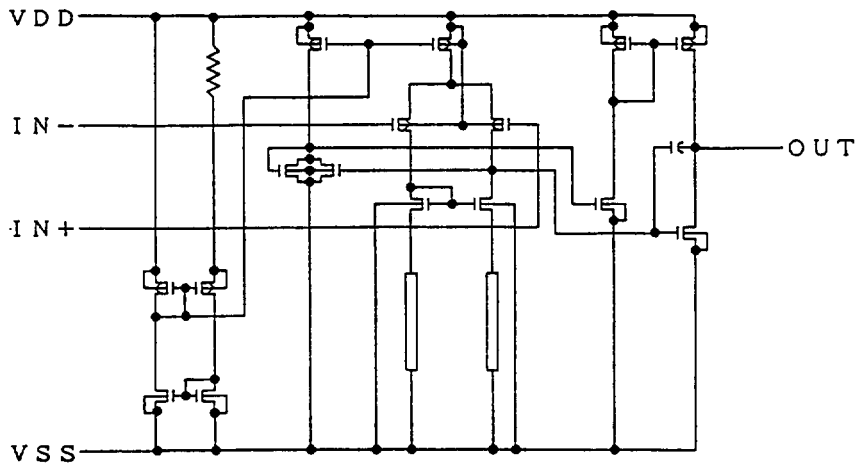
### ■ LINE-UP

(V<sub>DD</sub>=3.0V)

|                      | NJU7011 | NJU7012 | NJU7013 | UNIT              |
|----------------------|---------|---------|---------|-------------------|
| Operating Current    | 15      | 80      | 200     | $\mu A$ (typ.)    |
| Slew Rate            | 0.1     | 1.0     | 2.4     | V/ $\mu s$ (typ.) |
| Unity Gain Bandwidth | 0.2     | 1.0     | 1.0     | MHz (typ.)        |



■ EQUIVALENT CIRCUIT





■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

| P A R A M E T E R          | SYMBOL           | RATINGS    | UNIT |
|----------------------------|------------------|------------|------|
| Supply Voltage             | V <sub>DD</sub>  | 6.5        | V    |
| Differential Input Voltage | V <sub>ID</sub>  | ±6.5 Note1 | V    |
| Common Mode Input Voltage  | V <sub>IC</sub>  | - 0.3~6.5  | V    |
| Power Dissipation          | P <sub>D</sub>   | 200        | mW   |
| Operating Temperature      | T <sub>opr</sub> | - 40~+ 85  | °C   |
| Storage Temperature        | T <sub>stg</sub> | - 55~+125  | °C   |

Note1) If the supply voltage (V<sub>DD</sub>) is less than 6.5V, the input voltage must not over the V<sub>DD</sub> level though 6.5V is limit specified.

Note2) Decoupling capacitor should be connected between V<sub>DD</sub> and V<sub>SS</sub> due to the stabilized operation for the circuit.

■ ELECTRICAL CHARACTERISTICS

NJU7011

(Ta=25°C, V<sub>DD</sub>=3.0V, R<sub>L</sub>=∞)

| P A R A M E T E R               | SYMBOL           | CONDITIONS                                   | MIN                  | TYP | MAX                  | UNIT |
|---------------------------------|------------------|----------------------------------------------|----------------------|-----|----------------------|------|
| Input Offset Voltage            | V <sub>IO</sub>  | V <sub>IN</sub> =1/2V <sub>DD</sub>          |                      |     | 10                   | mV   |
| Input Offset Current            | I <sub>IO</sub>  |                                              |                      | 1   |                      | pA   |
| Input Bias Current              | I <sub>IB</sub>  |                                              |                      | 1   |                      | pA   |
| Input Impedance                 | R <sub>IN</sub>  |                                              |                      | 1   |                      | TΩ   |
| Large Signal Voltage Gain       | A <sub>v</sub>   |                                              | 60                   | 70  |                      | dB   |
| Input Common Mode Voltage Range | V <sub>ICM</sub> |                                              | 0~2.5                |     |                      | V    |
| Maximum Output Swing Voltage    | V <sub>OM1</sub> | R <sub>L</sub> =1MΩ                          | V <sub>DD</sub> -0.1 |     |                      | V    |
|                                 | V <sub>OM2</sub> | R <sub>L</sub> =1MΩ                          |                      |     | V <sub>SS</sub> +0.1 |      |
| Common Mode Rejection Ratio     | CMR              | V <sub>IN</sub> =0~2.5V                      | 55                   | 65  |                      | dB   |
| Supply Voltage Rejection Ratio  | SVR              | V <sub>DD</sub> =1.5~5.5V                    | 60                   | 70  |                      | dB   |
| Operating Current               | I <sub>DD</sub>  |                                              |                      | 15  | 25                   | μA   |
| Slew Rate                       | SR               |                                              |                      | 0.1 |                      | V/μs |
| Unity Gain Bandwidth            | F <sub>t</sub>   | A <sub>v</sub> =40dB<br>C <sub>L</sub> =10pF |                      | 0.2 |                      | MHz  |

NOTE3) The source current is less than 2.9μA(at V<sub>DD</sub>-0.1V).



NJU7012

 $(T_a=25^{\circ}\text{C}, V_{DD}=3.0\text{V}, R_L=\infty)$ 

| P A R A M E T E R               | SYMBOL    | CONDITIONS                             | MIN          | TYP | MAX     | UNIT             |
|---------------------------------|-----------|----------------------------------------|--------------|-----|---------|------------------|
| Input Offset Voltage            | $V_{IO}$  | $V_{IN}=1/2V_{DD}$                     |              |     | 10      | mV               |
| Input Offset Current            | $I_{IO}$  |                                        |              | 1   |         | pA               |
| Input Bias Current              | $I_{IB}$  |                                        |              | 1   |         | pA               |
| Input Impedance                 | $R_{IN}$  |                                        |              | 1   |         | TΩ               |
| Large Signal Voltage Gain       | $A_v$     |                                        | 60           | 70  |         | dB               |
| Input Common Mode Voltage Range | $V_{ICM}$ |                                        | 0~2.5        |     |         | V                |
| Maximum Output Swing Voltage    | $V_{OM1}$ | $R_L=100\text{k}\Omega$                | $V_{DD}-0.1$ |     |         | V                |
|                                 | $V_{OM2}$ | $R_L=100\text{k}\Omega$                |              |     | VSS+0.1 |                  |
| Common Mode Rejection Ratio     | CMR       | $V_{IN}=0\sim 2.5\text{V}$             | 55           | 65  |         | dB               |
| Supply Voltage Rejection Ratio  | SVR       | $V_{DD}=1.5\sim 5.5\text{V}$           | 60           | 70  |         | dB               |
| Operating Current               | $I_{DD}$  |                                        |              | 80  | 160     | $\mu\text{A}$    |
| Slew Rate                       | SR        |                                        |              | 1.0 |         | V/ $\mu\text{s}$ |
| Unity Gain Bandwidth            | $F_t$     | $A_v=40\text{dB}$<br>$C_L=10\text{pF}$ |              | 1.0 |         | MHz              |

NOTE4) The source current is less than  $29\mu\text{A}$ . (at  $V_{DD}=0.1\text{V}$ )

4

NJU7013

 $(T_a=25^{\circ}\text{C}, V_{DD}=3.0\text{V}, R_L=\infty)$ 

| P A R A M E T E R               | SYMBOL    | CONDITIONS                             | MIN          | TYP | MAX     | UNIT             |
|---------------------------------|-----------|----------------------------------------|--------------|-----|---------|------------------|
| Input Offset Voltage            | $V_{IO}$  | $V_{IN}=1/2V_{DD}$                     |              |     | 10      | mV               |
| Input Offset Current            | $I_{IO}$  |                                        |              | 1   |         | pA               |
| Input Bias Current              | $I_{IB}$  |                                        |              | 1   |         | pA               |
| Input Impedance                 | $R_{IN}$  |                                        |              | 1   |         | TΩ               |
| Large Signal Voltage Gain       | $A_v$     |                                        | 60           | 70  |         | dB               |
| Input Common Mode Voltage Range | $V_{ICM}$ |                                        | 0~2.5        |     |         | V                |
| Maximum Output Swing Voltage    | $V_{OM1}$ | $R_L=50\text{k}\Omega$                 | $V_{DD}-0.1$ |     |         | V                |
|                                 | $V_{OM2}$ | $R_L=50\text{k}\Omega$                 |              |     | VSS+0.1 |                  |
| Common Mode Rejection Ratio     | CMR       | $V_{IN}=0\sim 2.5\text{V}$             | 55           | 65  |         | dB               |
| Supply Voltage Rejection Ratio  | SVR       | $V_{DD}=1.5\sim 5.5\text{V}$           | 60           | 70  |         | dB               |
| Operating Current               | $I_{DD}$  |                                        |              | 200 | 400     | $\mu\text{A}$    |
| Slew Rate                       | SR        |                                        |              | 2.4 |         | V/ $\mu\text{s}$ |
| Unity Gain Bandwidth            | $F_t$     | $A_v=40\text{dB}$<br>$C_L=10\text{pF}$ |              | 1.0 |         | MHz              |

NOTE5) The source current is less than  $58\mu\text{A}$ . (at  $V_{DD}=0.1\text{V}$ )