*Warp2*ISR™ VHDL ISR™ Design Kit for CPLDs

Features

- **Complete design and programming kit for In-System Reprogrammable™ (ISR™) CPLDs**
- **Industry-leading *Warp2*® design software for VHDL**
- **Easy-to-use ISR PC programmer for on-board programming**
- **37000 UltraISR programming cable supports all Ultra37000™ CPLDs (For ISR programming of FLASH370i™ devices, please refer to the CY3600i data sheet.)**
- **Standard Windows® GUI**
- **Ultra37000 Prototype Board**
- **Year 2000 Compliant**

Functional Description

*Warp2*ISR™ VHDL is a complete ISR CPLD design and programming solution. *Warp2* VHDL and the Ultra37000 ISR Programming Kit are included in *Warp2*ISR VHDL. *Warp2* VHDL is a state-of-the-art HDL compiler for designing with Cypress's Complex Programmable Logic Devices (CPLDs) including the new Ultra37000 family of devices. For a complete description of *Warp2* VHDL, please see the CY3120 data sheet. For a complete description of the ISR PC programmer, please see the CY3700i Ultra37000 ISR Programming Kit data sheet.

Figure 1 shows the *Warp2*ISR VHDL kit in action with *Warp2* VHDL being used for design and the 37000 UltraISR™ cable

for programming. The ISR programmer connects the PC parallel port directly to your board, allowing you to easily reprogram devices when you make design changes. Multiple devices can be programmed in series, and ISR devices are cascadable with other IEEE 1149.1 compliant devices for convenient programming.
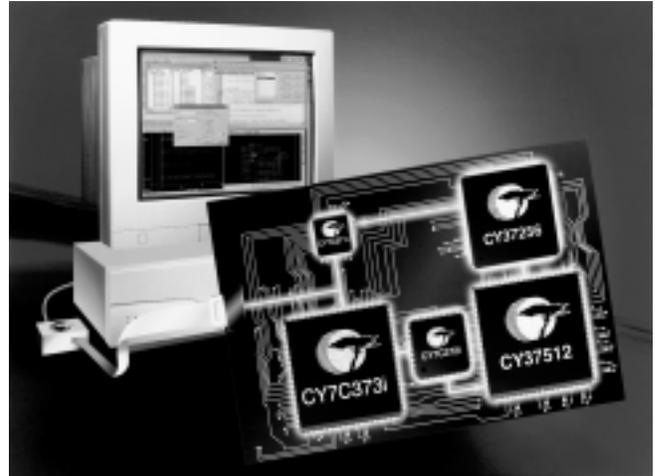
Figure 1. Design and Program with *Warp2*ISR

Ultra37000 Prototype Board
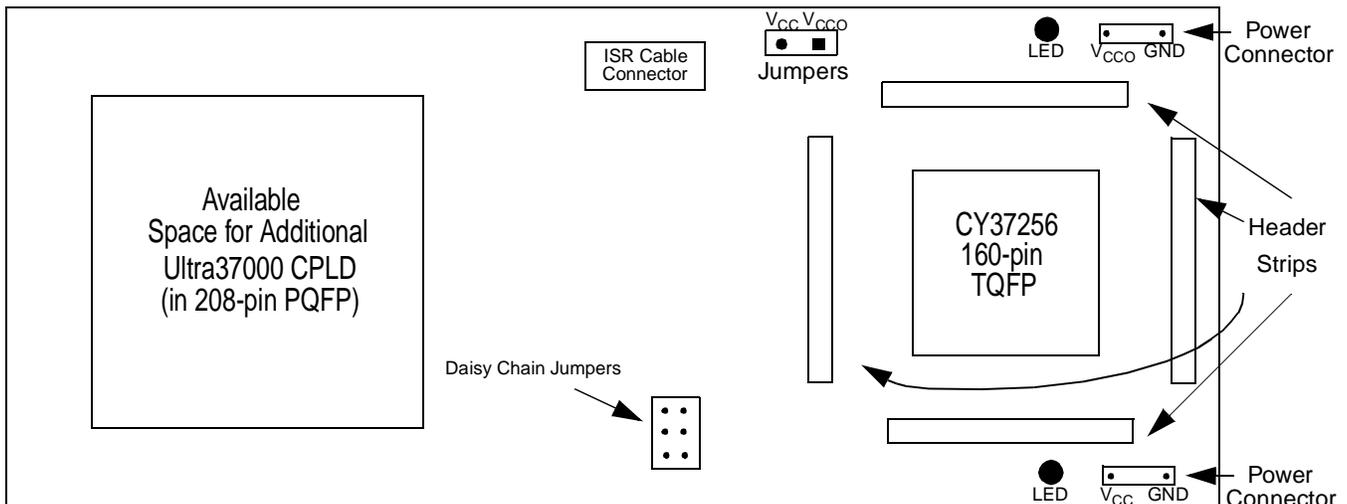
Figure 2. Ultra37000 Prototype Board

To facilitate easy and quick prototyping of designs, an Ultra37000 Prototype Board has been included with *Warp2*ISR VHDL (see *Figure 2* for the basic layout). The prototype comes with a CY37256 160-pin TQFP device already premounted and header strips that facilitate signal testing. Detailed information on the board layout and proper usage may be found in the Cypress application note, "Using the Ultra37000 ISR Prototype Board."

System Requirements

- IBM PC or equivalent (Pentium® class recommended)
- 32 Mbytes of RAM (64 Mbytes recommended)
- 110 Mbytes Disk Space
- CD-ROM drive
- Windows (including Japanese) 95, Windows 98, or Windows NT 4.0

The Ultra37000 ISR Programming Kit requires an additional 3 Mbytes of disk space and one free parallel port.

Ordering Information

Product Code	Description
CY3620R52	*Warp2*ISR VHDL ISR Design Kit for CPLDs
CY3620JR52	*Warp2*ISR VHDL ISR Design Kit for CPLDs (Japanese)

_Warp2_ISR VHDL includes:

- CD-ROM with *Warp2* and on-line documentation
- 37000 UltraISR programming cable
- CD-ROM with ISR Programming Software Release 2.2
- ISR Application Notes
- *Warp* Release Notes
- Prototype board with a CY37256 160-pin TQFP device
- VHDL for Programmable Logic Textbook
- Registration Card
- Release Notes

Document #: 38–00592–F

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