2SD2321

Silicon NPN epitaxial planer type

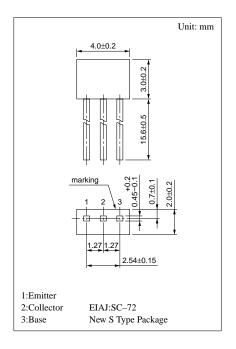
For low-frequency power amplification

Features

- ullet Low collector to emitter saturation voltage $V_{\text{CE}(sat)}$.
- Satisfactory operation performances at high efficiency with the low-voltage power supply.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	40	V
Collector to emitter voltage	V _{CEO}	20	V
Emitter to base voltage	V _{EBO}	7	V
Collector current	I_{C}	5	A
Peak collector current	I_{CP}	8	A
Collector power dissipation	P_{C}	400	mW
Junction temperature	T _j	150	°C
Storage temperature	T_{stg}	−55 ~ +150	°C



Electrical Characteristics (Ta=25°C)

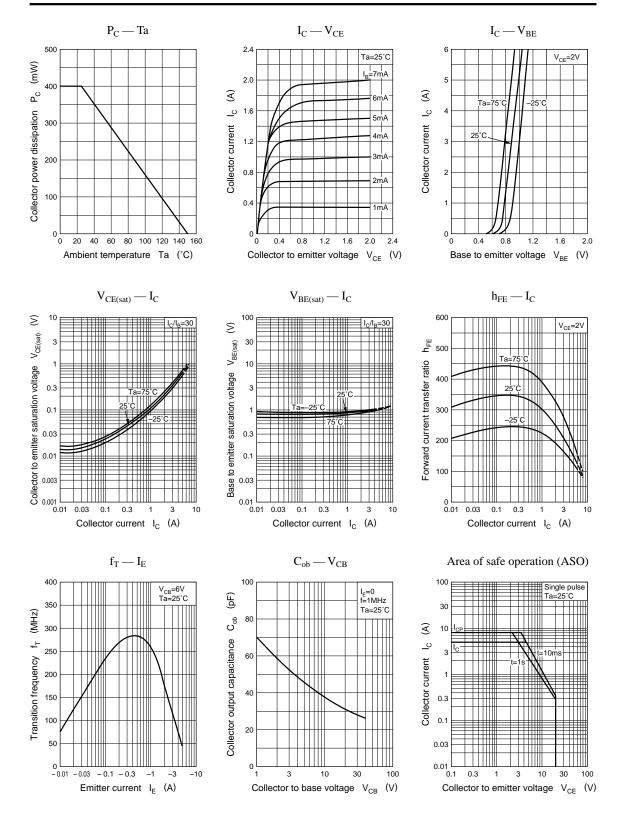
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 10V, I_{E} = 0$			0.1	μΑ
	I _{CEO}	$V_{CE} = 10V, I_{B} = 0$			1.0	μА
Emitter cutoff current	I_{EBO}	$V_{EB} = 7V, I_{C} = 0$			0.1	μА
Collector to emitter voltage	V _{CEO}	$I_{C} = 1 \text{mA}, I_{B} = 0$	20			V
Emitter to base voltage	V _{EBO}	$I_E = 10 \mu A, I_C = 0$	7			V
	h _{FE1} *1	$V_{CE} = 2V, I_C = 0.5A^{*2}$	230		600	
Forward current transfer ratio	h _{FE2}	$V_{CE} = 2V, I_C = 2A^{*2}$	150			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = 3A, I_B = 0.1A^{*2}$		0.28	1.0	V
Transition frequency	f_T	$V_{CB} = 6V, I_E = -50mA, f = 200MHz$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 20V, I_{E} = 0, f = 1MHz$		26	50	pF

^{*2} Pulse measurement

^{*1}hFE1 Rank classification

Rank	Q	R
h _{FE1}	230 ~ 380	340 ~ 600

Transistor 2SD2321



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