

## REPETITIVE AVALANCHE AND dv/dt RATED HEXFET® TRANSISTOR

## IRHF7310SE

# N-CHANNEL

## SINGLE EVENT EFFECT (SEE) RAD HARD

## 400 Volt, 4.5Ω, (SEE) RAD HARD HEXFET

International Rectifier's (SEE) RAD HARD technology HEXFETs demonstrate virtual immunity to SEE failure. Additionally, under identical pre- and post-radiation test conditions. International Rectifier's RAD HARD HEXFETs retain **identical** electrical specifications up to 1 x 10<sup>5</sup> Rads (Si) total dose. No compensation in gate drive circuitry is required. These devices are also capable of surviving transient ionization pulses as high as 1 x 1012 Rads (Si)/Sec, and return to normal operation within a few microseconds. Since the SEE process utilizes International Rectifier's patented HEXFET technology, the user can expect the highest quality and reliability in the industry.

RAD HARD HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits in space and weapons environments.

## **Product Summary**

Part Number	lumber BVpss		ΙD
IRHF7310SE	400V	$4.5\Omega$	1.15A

#### Features:

- Radiation Hardened up to 1 x 10<sup>5</sup> Rads (Si)
- Single Event Burnout (SEB) Hardened
- Single Event Gate Rupture (SEGR) Hardened
- Gamma Dot (Flash X-Ray) Hardened
- **Neutron Tolerant**
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed

## **Absolute Maximum Ratings**

	Parameter	IRHF7310SE	Units
ID @ VGS = 12V, TC = 25°C	Continuous Drain Current	1.15	
ID @ VGS = 12V, TC = 100°C	Continuous Drain Current	0.70	Α
IDM			]
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Max. Power Dissipation	15	W
	Linear Derating Factor	2.0	W/K ®
VGS Gate-to-Source Voltage		±20	V
EAS	Single Pulse Avalanche Energy ② 75		mJ
dv/dt	dv/dt Peak Diode Recovery dv/dt ®		V/ns
TJ	Operating Junction	-55 to 150	
TSTG	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from	°C
		case for 10 sec.)	1.0
	Weight	0.98 (typical)	g

## Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min.	Тур.	Max.	Units	Test Conditions		
BVDSS	Drain-to-Source Breakdown Voltage	400	_	_	V	VGS = 0V, ID = 1.0 mA		
ΔBVDSS/ΔTJ	Temperature Coefficient of Breakdown Voltage	_	0.37	_	V/°C	Reference to 25°C, I <sub>D</sub> = 1.0 mA		
RDS(on)	Static Drain-to-Source	_	_	4.5		VGS = 12V, ID = 0.7A VGS = 12V, ID = 1.15A		
, ,	On-State Resistance	_	_	5.2	Ω			
VGS(th)	Gate Threshold Voltage	2.5	_	4.5	V	VDS = VGS, ID = 1.0 mA		
gfs	Forward Transconductance	0.2	_	_	S (U)	VDS > 15V, IDS = 0.7A 4		
IDSS	Zero Gate Voltage Drain Current	_	_	50	_	$V_{DS} = 0.8 \times Max Rating, V_{GS} = 0V$		
		_	_	250	μΑ	V <sub>DS</sub> = 0.8 x Max Rating		
						VGS = 0V, TJ = 125°C		
IGSS	Gate-to-Source Leakage Forward			100	nA	VGS = 20V		
IGSS	Gate-to-Source Leakage Reverse	_	_	-100	1171	VGS = -20V		
Qg	Total Gate Charge	_	_	10		VGS =12V, ID = 1.15A		
Qgs	Gate-to-Source Charge	_	_	3	nC	V <sub>DS</sub> = Max. Rating x 0.5		
Qgd	Gate-to-Drain ("Miller") Charge	_	_	6				
td(on)	Turn-On Delay Time	_	_	15		VDD = 200V, ID = 1.15A,		
tr	Rise Time	_	_	20	ns	$RG = 7.5\Omega$		
td(off)	Turn-Off Delay Time	_	_	35	115			
tf	Fall Time	_	_	30				
LD	Internal Drain Inductance	_	5.0	_	nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die.  Modified MOSFET symbol showing the internal inductances.		
LS	Internal Source Inductance	_	15	_	1111	Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.		
C <sub>iss</sub>	Input Capacitance	_	215	_		VGS = 0V, VDS = 25V		
Coss	Output Capacitance	_	65	_	pF	f = 1.0 MHz		
C <sub>rss</sub>	Reverse Transfer Capacitance	_	_	_				

## **Source-Drain Diode Ratings and Characteristics**

	Parameter	ľ	Min.	Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current (Body Die	ode)	_	_	1.15	. A	Modified MOSFET symbol showing the
ISM	Pulse Source Current (Body Diode) ①			_	4.6	, · ·	integral reverse p-n junction rectifier.
VSD	Diode Forward Voltage Reverse Recovery Time			_	1.4	٧	Tj = 25°C, IS = 1.15A, VGS = 0V ④
t <sub>rr</sub>				_	540	ns	Tj = 25°C, IF = 1.15A, di/dt ≤ 100A/μs
QRR	Reverse Recovery Charge		-	_	4.5	μC	V <sub>DD</sub> ≤ 50V ④
ton	Forward Turn-On Time Intrins	sic turn-on	time	is negli	gible. Tu	urn-on sp	peed is substantially controlled by Ls + Lp.

## **Thermal Resistance**

	Parameter	Min.	Тур.	Max.	Units	Test Conditions
R <sub>th</sub> JC	Junction-to-Case	_	_	8.3	K/W®	
R <sub>th</sub> JA	Junction-to-Ambient	_	_	175	10.00	

#### Radiation Performance of Rad Hard HEXFETs

International Rectifier Radiation Hardened HEX-FETs are tested to verify their hardness capability. The hardness assurance program at International Rectifier uses two radiation environments.

Every manufacturing lot is tested in a low dose rate (total dose) environment per MIL-STD-750, test method 1019. International Rectifier has imposed a standard gate voltage of 12 volts per note 6 and a V<sub>DSS</sub> bias condition equal to 80% of the device rated voltage per note 7. Pre- and post-radiation limits of the devices irradiated to 1 x 10<sup>5</sup> Rads (Si) are identical and are presented in Table 1. The values in Table 1 will be met for either of the two low dose rate test circuits that are used.

Both pre- and post-radiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison. It should be noted that at a radiation level of 1 x 10<sup>5</sup> Rads (Si), no change in limits are specified in DC parameters.

High dose rate testing may be done on a special request basis, using a dose rate up to 1 x 10<sup>12</sup> Rads (Si)/Sec.

International Rectifier radiation hardened HEXFETs have been characterized in neutron and heavy ion Single Event Effects (SEE) environments. Single Event Effects characterization is shown in Table 3.

Table 1. Low Dose Rate 6 7 IRHF7310SE

		1	0.00_		
	Parameter		Rads (Si)	Units	Test Conditions ®
		min.	max.		
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	400	_	V	$V_{GS} = 0V, I_D = 1.0 \text{ mA}$
V <sub>GS(th)</sub>	Gate Threshold Voltage 4	2.0	4.5		$VGS = V_{DS}$ , $I_D = 1.0 \text{ mA}$
IGSS	Gate-to-Source Leakage Forward	_	100	nA	$V_{GS} = 20V$
I <sub>GSS</sub>	Gate-to-Source Leakage Reverse	_	-100		$V_{GS} = -20V$
IDSS	Zero Gate Voltage Drain Current	_	50	μА	$V_{DS} = 0.8 \text{ x Max Rating}, V_{GS} = 0V$
R <sub>DS(on)1</sub>	Static Drain-to-Source ④	-	6.5	Ω	$VGS = 12V, I_D = 0.6A$
	On-State Resistance One				
V <sub>SD</sub>	Diode Forward Voltage 4	_	1.4	V	$T_C = 25$ °C, $I_S = 1.0$ A, $V_{GS} = 0$ V

Table 2. High Dose Rate ®

		10 <sup>11</sup> F	Rads (	Si)/sec	10 <sup>12</sup> F	1012 Rads (Si)/sec			
	Parameter	Min.	Тур	Max.	Min.	Тур.	Max.	Units	Test Conditions
VDSS	Drain-to-Source Voltage	_	_	320	_	_	320	V	Applied drain-to-source voltage
									during gamma-dot
IPP		_	_	_	_	_	_	Α	Peak radiation induced photo-current
di/dt		_	_	_	_	_	_	A/µsec	Rate of rise of photo-current
L <sub>1</sub>		20	_	_	137			μH	Circuit inductance required to limit di/dt

Table 3. Single Event Effects 9

	Parameter	Тур.	Units	Ion	LET (Si) (MeV/mg/cm²)	Fluence (ions/cm²)	Range (μm)	V <sub>DS</sub> Bias (V)	V <sub>GS</sub> Bias (V)
İ	BVDSS	400	V	Ni	28	1 x 10 <sup>5</sup>	~35	320	-5

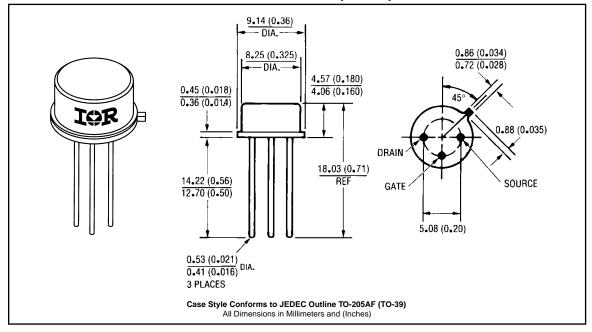
#### **IRHF7310SE Device**

### **Radiation Characteristics**

- Repetitive Rating; Pulse width limited by maximum junction temperature.
   Refer to current HEXFET reliability report.
- ② @  $V_{DD} = 50V$ , Starting  $T_{J} = 25^{\circ}C$ ,  $E_{AS} = [0.5 * L * (I_{L}^{2}) * [BV_{DSS}/(BV_{DSS}-V_{DD})]$ Peak  $I_{L} = 1.15A$ ,  $V_{GS} = 12V$ ,  $25 \le R_{G} \le 200\Omega$
- ③ I<sub>SD</sub> ≤ 1.15A, di/dt ≤ 40 A/ $\mu$ s, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, T<sub>J</sub> ≤ 150°C Suggested RG = 2.35Ω
- ④ Pulse width ≤ 300  $\mu$ s; Duty Cycle ≤ 2%
- ⑤ K/W = °C/W W/K = W/°C

- ® Total Dose Irradiation with V<sub>G</sub>S Bias. 12 volt V<sub>G</sub>S applied and V<sub>D</sub>S = 0 during irradiation per MIL-STD-750, method 1019.
- Total Dose Irradiation with Vps Bias.
  Vps = 0.8 rated BVpss (pre-radiation)
  applied and Vgs = 0 during irradiation per MIL-STD-750, method 1019.
- ® This test is performed using a flash x-ray source operated in the e-beam mode (energy ~2.5 MeV), 30 nsec pulse.
- 9 Process characterized by independent laboratory.
- All Pre-Radiation and Post-Radiation test conditions are identical to facilitate direct comparison for circuit applications.

## Case Outline and Dimensions — TO-204AF (TO-39)



# International TOR Rectifier

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