2SJ0364 (2SJ364)

Silicon P-Channel Junction FET

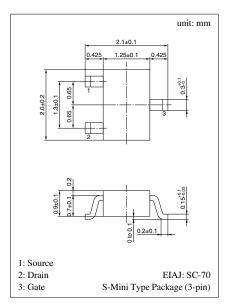
For analog switch

■ Features

- Low ON-resistance
- Low-noise characteristics

■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit	
Gate to Drain voltage	V _{GDS}	65	V	
Drain current	I_{D}	-20	mA	
Gate current	I_{G}	-10	mA	
Allowable power dissipation	P_{D}	150	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



Marking Symbol (Example): 4M

■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS} *	$V_{DS} = -10V, V_{GS} = 0$	- 0.2		-6	mA
Gate to Source leakage current	I_{GSS}	$V_{GS} = 30V, V_{DS} = 0$			10	nA
Gate to Drain voltage	V _{GDS}	$I_{G} = 10\mu A, V_{DS} = 0$	65			V
Gate to Source cut-off voltage	V _{GSC}	$V_{DS} = -10V, I_{D} = -10\mu A$		1.5	3.5	V
Forward transfer admittance	Y _{fs}	$V_{DS} = -10V, I_{D} = -1mA, f = 1kHz$	1.8	2.5		mS
Drain to Source ON-resistance	R _{DS(on)}	$V_{DS} = -10 \text{mV}, V_{GS} = 0$		300		Ω
Input capacitance (Common Source)	C _{iss}	$V_{DS} = -10V, V_{GS} = 0, f = 1MHz$		12		pF
Reverse transfer capacitance (Common Source)	C _{rss}	$\mathbf{v}_{\mathrm{DS}} = -10\mathbf{v}, \ \mathbf{v}_{\mathrm{GS}} = 0, 1 = 1101112$		4		pF

 $^{^{*}}$ I_{DSS} rank classification

Runk	0	P	Q	R
I _{DSS} (mA)	− 0.2 to −1	- 0.6 to -1.5	−1 to −3	−2.5 to −6
Marking Symbol	4MO	4MP	4MQ	4MR

Note) The part number in the parenthesis shows conventional part number.

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1.5 1.0 0.5

1.5

1.0

Gate to source voltage $\ V_{GS} \ (V)$

0.5

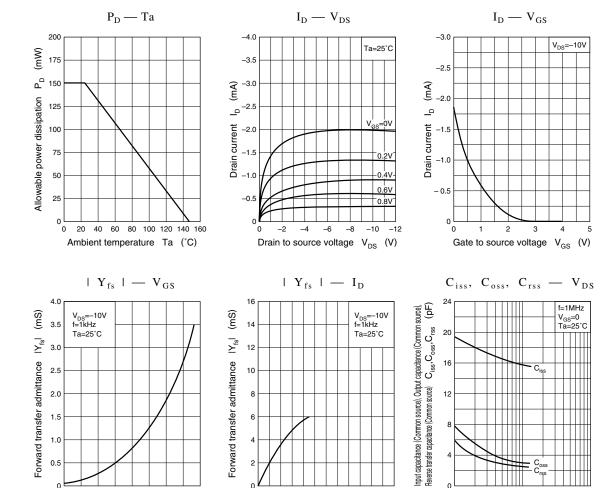
-3

-30

-100

-10

Drain to source voltage $\,V_{\rm DS}\,\,$ (V)



-6 -8

Drain current I_D (mA)

2 0 1

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