# 2SD2067 (Tentative)

### Silicon NPN epitaxial planer type

For low-frequency output amplification

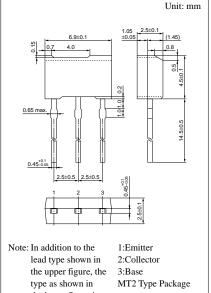
#### Features

- Darlington connection.
- High foward current transfer ratio h<sub>FE</sub>.
- Large peak collector current I<sub>CP</sub>.
- High collector to emitter voltage V<sub>CEO</sub>.
- Allowing supply with the radial taping.

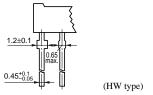
#### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	$V_{CBO}$	120	V
Collector to emitter voltage	$V_{CEO}$	100	V
Emitter to base voltage	$V_{EBO}$	5	V
Peak collector current	$I_{CP}$	3	A
Collector current	$I_{C}$	2	A
Collector power dissipation	${P_C}^*$	1	W
Junction temperature	$T_{j}$	150	°C
Storage temperature	$T_{stg}$	<b>−55 ~ +150</b>	°C

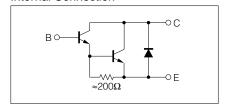
Printed circuit board: Copper foil area of 1cm<sup>2</sup> or more, and the board thickness of 1.7mm for the collector portion



the lower figure is also available



#### Internal Connection



#### Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = 25V, I_E = 0$			0.1	μΑ
Emitter cutoff current	$I_{EBO}$	$V_{EB} = 4V$ , $I_C = 0$			1	μΑ
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = 100 \mu {\rm A},  I_{\rm E} = 0$	120			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_C = 1 \text{mA}, I_B = 0$	100			V
Emitter to base voltage	V <sub>EBO</sub>	$I_E = 100 \mu A, I_C = 0$	5			V
Forward current transfer ratio	h <sub>FE</sub> *1	$V_{CE} = 10V, I_C = 1A^{*2}$	4000		40000	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 1A, I_B = 1mA^{*2}$			1.5	V
Base to emitter saturation voltage	V <sub>BE(sat)</sub>	$I_C = 1A, I_B = 1mA^{*2}$			2	V

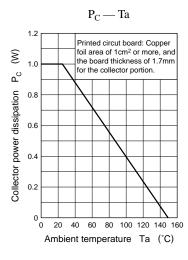
<sup>\*1</sup>hFE Rank classification

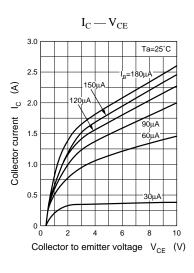
Rank	Q	R	S
$h_{FE}$	4000 ~ 10000	8000 ~ 20000	16000 ~ 40000

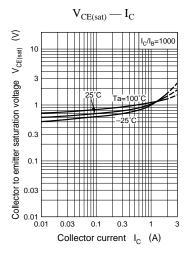
\*2 Pulse measurement

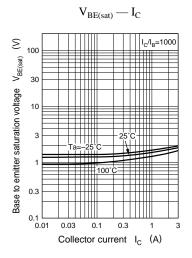
**Panasonic** 

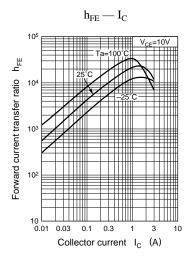
Transistor 2SD2067

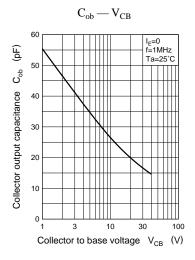




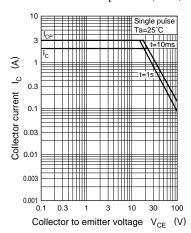








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