

KLM - 128MA - 16A

This module designed for light emitting display device. Organize with 16 x 16 matrix combination with 256 of each Red, Green LED Chips, for indoor use.

FEATURES

- Active display size : 128mm x128mm
- Dot size : ϕ 6.5
- Dot pitch : 7.5mm
- Display color : RED, GREEN, AMBER(Mixed color)
- Duty rate : 1/16
- Dot matrix : 256(16 x16)
- Weight : 270g(Typ.)
- With a simple serial - inter face.

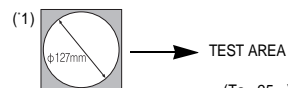
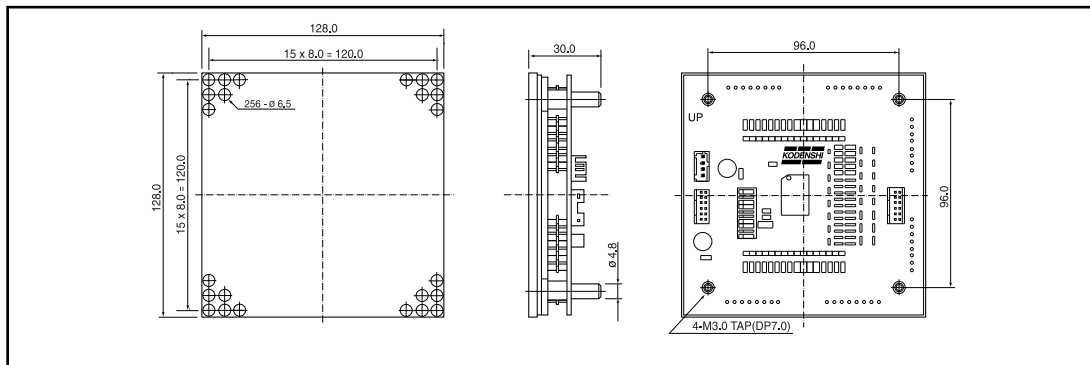
MAXIMUM RATINGS

(Ta=25)

ITEM	SYMBOL	RATINGS	UNIT	COND.
Power dissipation	P _o	18	W	
Supply voltage(DRIVE)	V _{DD}	6	V	
Supply voltage(LED)	V _{LED}	6	V	
Logic input power	V _{in}	- 0.5 ~ V _{DD}	V	
Junction Temp.	T _J	115		
Operating Temp.	T _{opr}	- 20 ~ +65		
Storage Temp.	T _{stg}	- 20 ~ +70		

DIMENSIONS

(Unit : mm)



OPTICAL CHARACTERISTICS

(Ta=25)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT.
Bright - ness(16x 16)	GREEN	V _{DD} = 5V	-	70	-	cd/m ² · 1
	RED		-	50	-	
	AMBER		-	100	-	
Dot - Balance	GREEN	V _{LED} = 5V	-	-	2	-
	RED		-	-	2	
	AMBER		-	-	2	
Emission Wavelen - ght	GREEN		-	565	-	nm
	RED		-	630	-	
Spectrum half - band	GREEN		-	25	-	nm
	RED		-	40	-	

LED Dot Matrix

KLM - 128MA - 16A

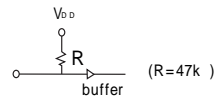
ELECTRICAL CHARACTERISTICS(VOLTAGE CURRENT CHARACTERISTICS)

(Ta=25)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT.	
Supply voltage(LOGIC)	V _{DD}	-	4.75	5	5.25	V	
Supply voltage(LED)	V _{LED}	-	4.50	5	5.50	V	
Supply current(LOGIC)	I _{DD}	V _{DD} =5V	-	50.0	70.0	mA	
Supply current(LED)	GREEN	I _{LEDg}	Lighting all 1 ²	-	-	1.4	A
	RED	I _{LEDr}		-	-	1.4	
	AMBER	I _{LEDo}		-	-	2.5	
Logic - input vol tage LOW	V _L	V _{LED} =5V	-	-	1.5	V	
Logic - input vol tage HIGH	V _{HH}	V _{DD} =5V	3.5	-	-	V	

*2. V₀=5V, V_{LED}=5V *Temperature condition of guarentee range for operating : - 20 - +65

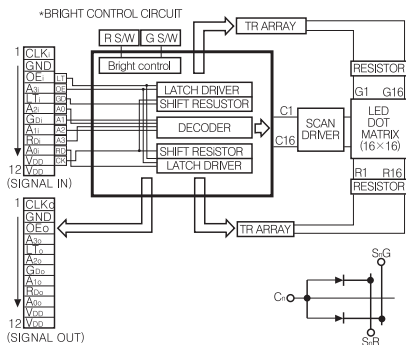
*PULL UP



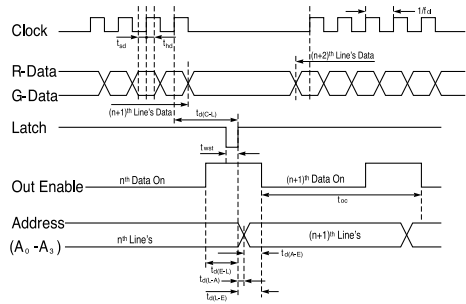
ELECTRICAL CHARACTERISTICS (CONNECTOR SPECIFICATION)

CLASSIFICATION	NAME	STANDARD	LENGH	COMPANY	REMARK	
POWER CONN.	WAFER	B 4B - XH - A	-	KST	JST C/N	
INPUT SIGNAL CONN.	WAFER	B 12B - PHDSS	-	KST	JST C/N	
OUTPUT SIGNAL CONN.	WAFER	B 12B - PHDSS	-	KST	JST C/N	
ASS Y	POWER	HARNESS	XHP - 4P - 000(Ring type)	Option	KST	JST C/N
		HARNESS	XHP - 4P - 000(Solder type)	Option	KST	JST C/N
	SIGNAL	HARNESS	HPDR - 12VS - 000	Option	KST	JST C/N

BLOCK DIAGRAM



TIMING CHART



RECOMMENDED TIMING CONDITION

NO	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT.
1	CLOCK FREQUENCY	f _{cl}	-	-	40	MHz
2	CLOCK CYCLE	1/f _{cl}	25	-	-	ns
3	CLOCK - LATCH TIME	t _{dl(C-L)}	25	-	-	ns
4	LATCH PULSE WIDTH	t _{wst}	25	-	-	ns
5	ENABLE - LATCH TIME	t _{dl(E-L)}	0	-	-	ns
6	DATA SETUPTIME	t _{sd}	6	-	-	ns
7	DATA HOLD TIME	t _{hd}	6	-	-	ns
8	ADDRESS - ENABLE TIME	t _{dl(A-E)}	25	-	-	ns
9	LATCH - ADDRESS TIME	t _{dl(L-A)}	0	-	-	ns
10	LATCH - ENABLE TIME	t _{dl(L-E)}	0	-	-	ns
11	ENABLE CYCLE	t _{ec}	-	-	1	ms