TOSHIBA Transistor Silicon NPN Epitaxial Type

2SC5714

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: $h_{FE} = 400$ to 1000 ($I_{C} = 0.5$ A)
- Low collector-emitter saturation voltage: $V_{CE (sat)} = 0.15 \text{ V (max)}$
- High-speed switching: tf = 90 ns (typ.)

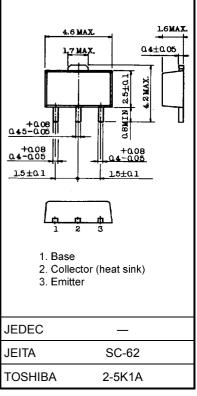
Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	40	V	
Collector-emitter voltage		V _{CEX}	30	V	
Collector-emitter voltage		V _{CEO}	20	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ic	4	Α	
	Pulse	I _{CP}	7		
Base current		ΙΒ	400	mA	
Collector power dissipation	DC	PC	1.0	W	
	t = 10 s	(Note)	2.5		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Note: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Industrial Applications

Unit: mm



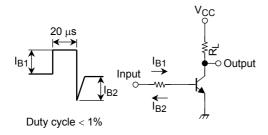
Weight: 0.05 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	$V_{CB} = 40 \text{ V}, I_{E} = 0$	_	_	100	nA	
Emitter cut-off current		I _{EBO}	$V_{EB} = 7 \text{ V, } I_{C} = 0$	_	_	100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	20	_	_	V	
DC current gain		h _{FE} (1)	$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}$	400	_	1000		
		h _{FE} (2)	V _{CE} = 2 V, I _C = 1.6 A	200	_	_		
Collector-emitter saturation voltage		V _{CE} (sat)	$I_C = 1.6 \text{ A}, I_B = 32 \text{ mA}$	_	_	0.15	V	
Base-emitter saturation voltage		V _{BE (sat)}	$I_C = 1.6 \text{ A}, I_B = 32 \text{ mA}$	_	_	1.10	V	
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	18	_	pF	
Switching time	Rise time	t _r	See Figure 1 circuit diagram.	_	100	_	ns	
	Storage time	t _{stg}	$V_{CC} \simeq 12 \text{ V}, R_L = 7.5 \Omega$	_	350	_		
	Fall time	t _f	$I_{B1} = -I_{B2} = 53.3 \text{ mA}$		90	_		

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Marking



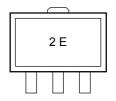
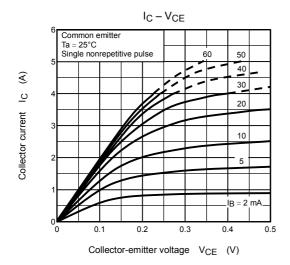
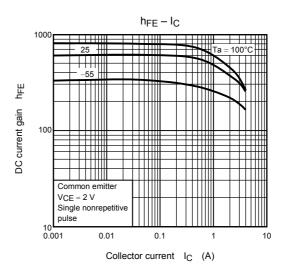
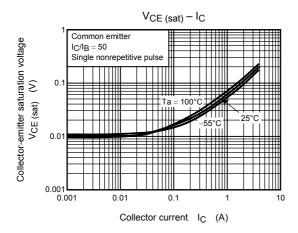
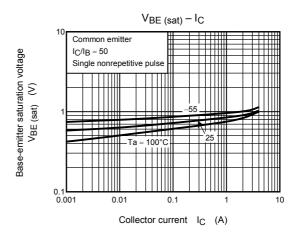


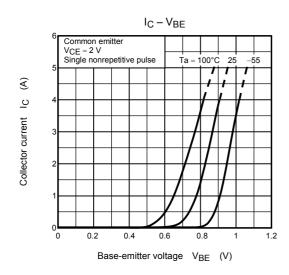
Figure 1 Switching Time Test Circuit & Timing Chart



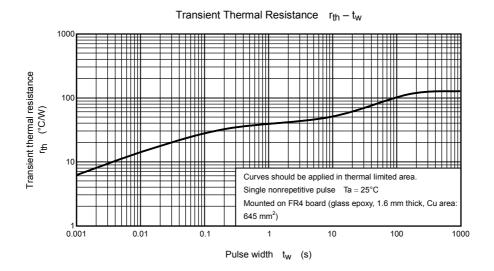


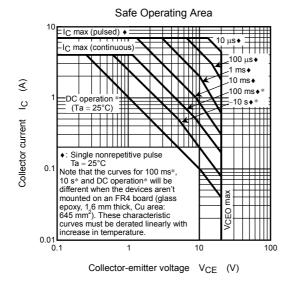






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