LEM

Current Transducer LTS 15-NP

For the electronic measurement of currents : DC, AC, pulsed, mixed, with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).





Electrical data

I _{PN}	Primary nominal r.m.s. current		15	At
I _P	Primary current, measuring range		0±48	At
Î _{PDC}	Overload capability	250	At	
V _{OUT}	Analog output voltage @ I		2.5 ± (0.62	25 ·I_P/I_{PN}) V
001	$I_{P} = 0$		2.5 ¹⁾	V
Ns	Number of secondary turns (± 0.1 %)		2000	
R	Load resistance		≥2	kΩ
R _M	Internal measuring resistance (± 0.5 %)		83.33	Ω
	Thermal drift of R _M		< 50	ppm/K
V _c	Supply voltage (± 5 %)		5	V
I _c	Current consumption @ $V_c = 5 V$	Тур	$28 + I_{s}^{2} + (V_{c})$	_{வா} / R _)mA

Ac	curacy - Dynamic performance data		
х	Accuracy @ I_{PN} , $T_{A} = 25^{\circ}C$	± 0.2	%
	Accuracy with $\mathbf{R}_{M} @ \mathbf{I}_{PN}$, $\mathbf{T}_{A} = 25^{\circ}C$	± 0.7	%
$\epsilon_{\scriptscriptstyle L}$	Linearity error	< 0.1	%
		Тур	Max
TCV	Thermal drift of $V_{OUT} @ I_{P} = 0$ - 10°C + 85°C - 40°C 10°C	65	120 ppm/K 170 ppm/K
TCE _G	Thermal drift of the gain - 40°C + 85°C		50 ³⁾ ppm/K
V _{ом} [°]	Residual voltage @ $I_p = 0$, after an overload of 3 x I_{PN}		±0.5 mV
	5 x I _{PN}		±2.0 mV
	10 x I _{PN}		± 2.0 mV
t _{ra}	Reaction time @ 10 % of I _{PN}	< 100	ns
t	Response time @ 90 % of I _{PN}	< 400	ns
di/dt	di/dt accurately followed	> 35	A/µs
f	Frequency bandwidth (0 0.5 dB)	DC 1	00 kHz
	(- 0.5 1 dB)	DC 2	200 kHz
Ge	eneral data		
T _A	Ambient operating temperature	- 40	+ 85 °C
T _s	Ambient storage temperature	- 40	+100 °C
m	Mass	10	g
	Standards	EN 5017	78 : 1997
		IEC 60950-1 : 2001	

$I_{PN} = 15$ At

Features

- Closed loop (compensated) multirange current transducer using the Hall effect
- Unipolar voltage supply
- Compact design for PCB mounting
- Insulated plastic case recognized according to UL 94-V0
- Incorporated measuring resistance
- Extended measuring range.

Advantages

- Excellent accuracy
- Very good linearity
- Very low temperature drift
- Optimized response time
- Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability.

Applications

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies
 (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

Application domain

2001 • Industrial.

<u>Notes</u> : ¹⁾ Absolute value @ $T_A = 25^{\circ}C$, 2.475 < $V_{OUT} < 2.525$

- ²⁾ Please see the operation principle on the other side
 - ³⁾ Only due to **TCR**.

LEM Components

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LEM reserves the right to carry out modifications on its transducers, in order to improve them, without previous notice.

Current Transducer LTS 15-NP

Iso	lation	chara	cteristics	

\hat{V}_{d}	R.m.s. voltage for AC isolation test, 50/60 Hz, 1 mn Impulse withstand voltage 1.2/50 μs	3 >8	kV kV
V _e	R.m.s. voltage for partial discharge extinction @ 10pC	Min >1.5	kV
dCp dCl CTI	Creepage distance ⁴⁾ Clearance distance ⁵⁾ Comparative Tracking Index (Group III a)	Min 15.5 6.35 175	mm mm

Application examples

According to EN 50178 and IEC 61010-1 standards and following conditions :

- Over voltage category OV 3
- Pollution degree PD2
- Non-uniform field

	EN 50178	IEC 61010-1	
dCp, dCl, Ŷ _w	Rated isolation voltage	Nominal voltage	
Single isolation	600 V	600 V	
Reinforced isolation	300 V	300 V	

Notes : ⁴⁾ On housing

Safety



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the following manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

Ignoring this warning can lead to injury and/or cause serious damage. This transducer is a built-in device, whose conducting parts must be inaccessible

after installation.

A protective housing or additional shield could be used.

Main supply must be able to be disconnected.

⁵⁾ On PCB with soldering pattern UTEC93-703.



		Operation principle
Bottom view	Model + SP number Date code	N N N N N N N N N N N N N N N N N N N
R3.2 1 R9.2 a3.2 a3.2 a3.2 a3.2 <th>2.54 3.81</th> <th>A B Front view</th>	2.54 3.81	A B Front view
·····=-	12.7	Rep. Clearance Creepage
Back view	Right view	A-B 6.35mm 15,5mm

Dimensions LTS 15-NP (in mm. 1 mm = 0.0394 inch)

Number of primary turns	Primary nominal r.m.s. current I _{PN} [A]	Nominal output voltage V _{our} [V]	Primary resistance R _P [mΩ]	Primary insertion inductance L _P [µH]	Recommended connections
1	± 15	2.5 ± 0.625	0.18	0.013	6 5 4 OUT 0 0 0 0 IN 1 2 3
2	± 7.5	2.5 ± 0.625	0.81	0.05	6 5 4 OUT 0 0 0 IN 1 2 3
3	± 5	2.5 ± 0.625	1.62	0.12	6 5 4 OUT 0 0 0 IN 1 2 3

Mechanical characteristics

- General tolerance
- Fastening & connection of primary Recommended PCB hole
- 6 pins 0.8 x 0.8 mm 1.3 mm 3 pins 0.5 x 0.35 mm
- Fastening & connection of secondary Recommended PCB hole
 Additional primary through hole
- 0.8 mm Ø 3.2 mm

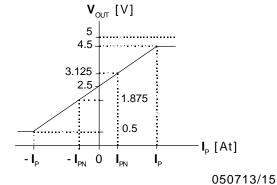
± 0.2 mm

• Additional primary through-hole

Remarks

- V_{OUT} is positive when I_{p} flows from terminals 1, 2, 3 to terminals 6, 5, 4.
- Temperature of the primary jumper should not exceed 100°C.

Output Voltage - Primary Current



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