## TOSHIBA CMOS INTEGRATED CIRCIUTS SILICON MONOLITHIC TCA62735AFLG

## Charge Pump type DC/DC Converter for White LED Driver

The TCA62735AFLG is a charge pump type DC/DC Converter specially designed for constant current driving of White LED.
This IC can outputs LED current 120 mA or more to $2.8-4.2 \mathrm{~V}$ input. This IC observes the power-supply voltage and the output voltage, and does an automatic change to the best of step up mode 1, 1.5 or 2 times. It is possible to prolong the battery longevity to its maximum.
This IC is especially for driving back light white LEDs in LCD of PDA, Cellular Phone, or Handy Terminal Equipment.


Weight: 0.016 g (Typ.)

## Characteristics

- Fabricating with CMOS Process
- Package : QFN16 ( $4 \mathrm{~mm} \times 4 \mathrm{~mm} \times 0.8 \mathrm{~mm}$ )
- Input Voltage : 2.8 V (Min)
- Output Voltage : 4.2V (Min)
- Switching Frequency: 1MHz(Typ.)
- Output Drive Current Capability : Greater than 120mA
- 4 Channels Built in Constant Sink Current Drivers
- Sink Current Adjustment by External Resistance
- Soft Start Function
- Output Open Detection Function
- Integrated protection circuit TSD (Thermal Shut Down)


## Pin Assignment (top view)



## Block Diagram



## Explanation of Terminals

| No | Symbol | Function |
| :---: | :---: | :---: |
| 1 | EN | Logic input terminal. (input a chip enable signal) <br> EN = "H" $\rightarrow$ Operation mode, EN = "L" $\rightarrow$ Shutdown mode |
| 2 | CTLO | Logic input terminal. (Selection of an output number) <br> Please refer to the truth table on page 10. |
| 3 | CTL1 |  |
| 4 | CTL2 |  |
| 5 | ISET | Resistance connection terminal for setting up output current. |
| 6 | VOUT | Output terminal. |
| 7 | VIN | Power supply terminal. |
| 8 | C1+ | Capacitance connection terminal for charge pump. |
| 9 | C1- |  |
| 10 | C2- |  |
| 11 | C2+ |  |
| 12 | GND | GND terminal. |
| 13 | ILED4 | Constant Sink Current Driver terminal.$\operatorname{ILED}(\mathrm{mA})=0.61 \mathrm{~V} \times 400 / \mathrm{R}_{\mathrm{SET}}(\mathrm{k} \Omega)$ |
| 14 | ILED3 |  |
| 15 | ILED2 |  |
| 16 | ILED1 |  |

Absolute Maximum Ratings ( $\mathrm{T}_{\mathrm{opr}}=25^{\circ} \mathrm{C}$ if without notice)

| Characteristics | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: |
| Power Supply Voltage | $\mathrm{V}_{\text {IN }}$ | $-0.3 \sim+6.0$ | V |
| $1 \mathrm{n} p \mathrm{ut} V$ olt a g e | $\mathrm{V}_{\text {in }}$ | $-0.3 \sim \mathrm{~V}_{\text {IN }}+0.3$ (*1) | mA |
| Output Cur ren t | lout | 200 | mA/ch |
| Operating Temperature | $\mathrm{T}_{\text {opr }}$ | -40~+85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {stg }}$ | -55~+150 | ${ }^{\circ} \mathrm{C}$ |
| Junction Temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |

*1 : please do not exceed 6V.
Recommended Operating Condition ( $\mathrm{T}_{\mathrm{opr}}=-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ if without notice)

| Characteristics | Symbol | Test Condition | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| P o w e r S u p p I y | $\mathrm{V}_{\text {IN }}$ | - | 2.8 | - | 4.2 | V |
| Logic Input Voltage | $\mathrm{V}_{\text {in }}$ | EN,CTLO,CTL1,CTL2 | 0 | - | $\mathrm{V}_{\mathrm{IN}}$ | V |
| Input Ripple Voltage | $\mathrm{V}_{\text {IN(ripple) }}$ | - | - | - | 40 | mVpp |
| Capacitance for Charge Pump | $\mathrm{C}_{1}, \mathrm{C}_{2}$ | - | 0.8 | 1.0 | 2.2 | $\mu \mathrm{~F}$ |
| Capacitance for output | $\mathrm{C}_{\text {out }}$ | - | 0.8 | 2.2 | 4.7 | $\mu \mathrm{~F}$ |
| Capacitance for input | $\mathrm{C}_{\text {IN }}$ | - | 0.8 | 2.2 | 10.0 | $\mu \mathrm{~F}$ |
| R set resistance | $\mathrm{R}_{\text {SET }}$ | - | 2 | 8 | 80 | $\mathrm{k} \Omega$ |

## Electrical Characteristics

DC-DC Regulator part ( $\mathrm{V}_{\mathrm{IN}}=3.6 \mathrm{~V}, \mathrm{~T}_{\mathrm{opr}}=-40$ to $85^{\circ} \mathrm{C}$, if it is not specified.)

| Characteristics | Symbol | Test Condition | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Current Ability | lout(max) | 2 time up converting | 120 | - | - | mA |
|  |  | 1.5 time up converting | 120 | - | - |  |
|  |  | 1 time up converting | 120 | - | - |  |
| Consumption Current | InN(ON) | $\mathrm{l}_{\text {OUT }}=5 \mathrm{~mA}$ | - | 1 | 2 | mA |
| Stand By Consumption Current | $\mathrm{I}_{\text {IN(OFF) }}$ | $\begin{aligned} & \text { lout }=0 \mathrm{~mA} \\ & \mathrm{EN}=" \mathrm{~L} " \end{aligned}$ | - | 0 | 1 | $\mu \mathrm{A}$ |
| Logic Input Voltage | $\mathrm{V}_{1 H}$ | $\begin{gathered} \text { EN, CTLO,CTL1,CTL2 } \\ V_{I N}=2.8 \mathrm{~V} \text { to } 4.2 \mathrm{~V} \end{gathered}$ | $0.7 \mathrm{~V}_{\mathrm{IN}}$ | - | - | V |
|  | $V_{\text {IL }}$ | $\begin{gathered} \text { EN,CTLO,CTL1,CTL2 } \\ V_{I N}=2.8 \mathrm{~V} \text { to } 4.2 \mathrm{~V} \end{gathered}$ | - | - | $0.3 \mathrm{~V}_{\text {IN }}$ |  |
| Logic Input Current | $\mathrm{l}_{\text {leak }}$ | EN,CTL0,CTL1,CTL2 | - | - | 0.1 | $\mu \mathrm{A}$ |
| C I ock Frequency | fosc | - | - | 1000 | - | kHz |
| T O T A L R $\circ$ N | Ron | 1.5 time up converting | - | 5 | 10 | $\Omega$ |
| $\begin{array}{\|l\|l\|} \hline 1 \times \mathrm{mode} \text { to } & 1.5 X \operatorname{mode} \\ \mathrm{transitton} & \text { voltage } \\ \hline \end{array}$ | $V_{\text {trans1x }}$ | LED $V_{f}=3.6 \mathrm{~V}$, lout $=80 \mathrm{~mA}$ <br> $\mathrm{V}_{\mathrm{IN}}$ falling | - | 4.0 | - | V |

Constant Current Driver part
( $\mathrm{V}_{\text {IN }}=2.8 \mathrm{~V}$ to $4.2 \mathrm{~V}, \mathrm{~T}_{\text {opr }}=-40$ to $85^{\circ} \mathrm{C}$, if it is not specified.)

| Characteristics | Symbol | Test Condition | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Constant Current Drive Setting | ILED1~4 | $\mathrm{R}_{\text {SET }}=47 \mathrm{k} \Omega$ | - | 5.1 | - | mA |
|  |  | $\mathrm{R}_{\mathrm{SET}}=12 \mathrm{k} \Omega$ | - | 19.6 | - |  |
|  |  | $\mathrm{R}_{\text {SET }}=8.2 \mathrm{k} \Omega$ | - | 28 | - |  |
| ISET Terminal Output Voltage | $V_{\text {SET }}$ | $\mathrm{R}_{\text {SET }}=8.2 \mathrm{k} \Omega$ | - | 0.61 | - | V |
| Constant Current Between Chs | \|ILED-LED-ERR| | - | - | 2.5 | - | \% |
| A c curacy Between ICs | \|lied-ERR ${ }^{\text {\| }}$ | - | - | 5 | - | \% |
| Constant Sink Current <br> Supply Voltage Regulation | $\left\|\Delta l_{\text {LED }}\right\|$ | $\begin{gathered} \hline \mathrm{V}_{\text {IN }}=3.6 \mathrm{~V} \text { center } \\ \mathrm{V}_{\text {IN }}=2.8 \text { to } 4.2 \mathrm{~V} \\ \mathrm{I}_{\text {OUT }}=80 \mathrm{~mA} \\ \mathrm{C}_{\text {IN }}=2.2 \mu \mathrm{~F} \end{gathered}$ | - | 1 | - | \% |
| Output leakage current | $\mathrm{l}_{\text {LEAK 1~4 }}$ | $\begin{gathered} \text { EN="H" } \\ \text { ILED1 to4="OFF" } \end{gathered}$ | - | - | 1 | $\mu \mathrm{A}$ |

Reference data






This datasheet is tentative, the values and contents are subject to change without any notice. 2005-04-26 (Page 5 of 12)

## Method of setting ILED

The current of the terminal ILED1 to 4 is set by resistance RSET connected with the terminal ISET. ILED can be set according to the next expression.

$$
\operatorname{ILED}[\mathrm{mA}]=\frac{400 \times 0.61[\mathrm{~V}]}{R_{\mathrm{SET}}[\mathrm{k} \Omega]}
$$




## Method of Current Dimming control

1) Input PWM signal to SHDN terminal

ILED can be set according to the next expression.

$$
\operatorname{ILED}[\mathrm{mA}]=\frac{0.61[\mathrm{~V}] \times 400 \times \mathrm{ON} \text { Duty }[\%]}{\mathrm{R}_{\mathrm{SET}}[\mathrm{k} \Omega]}
$$

$\mathrm{f}_{\mathrm{Pw}}$ will recommend 100 Hz .



*In this PWM control operation, This IC repeats ON/OFF. In this result, rush current is occur when ON timing with supplying charge to $\mathrm{C}_{20}$.

This datasheet is tentative, the values and contents are subject to change without any notice. 2005-04-26 (Page 7 of 12)
2) Input analog voltage to ISET terminal

1. Precondition

- Please set the range of the analog voltage input by 0 to 0.61 V .

2. The maximum current is defined as amA.

$$
\alpha[\mathrm{mA}]=0.61[\mathrm{~V}] \times \frac{\mathrm{R}_{1}[\mathrm{k} \Omega]+\mathrm{R}_{2}[\mathrm{k} \Omega]}{R_{1}[k \Omega] \times R_{2}[k \Omega]} \times 400
$$

3. A minimum current is defined as $\beta \mathrm{mA}$.

$$
\beta[\mathrm{mA}]=0.61[\mathrm{~V}] \times \frac{1}{\mathrm{R}_{2}[\mathrm{k} \Omega]} \times 400
$$

4. ILED can be set according to the next expression.

$$
\operatorname{ILED}[\mathrm{mA}]=\mathrm{V}_{\mathrm{ADJ}}[\mathrm{~V}] \times \frac{\beta[\mathrm{mA}]-\alpha[\mathrm{mA}]}{0.61[\mathrm{~V}]}+\alpha[\mathrm{mA}]
$$



*This method is without repeating IC ON/OFF, and no need to consider holding rash current.

## 3) Input Logic signal

User can adjust ILED with Logic signal input as indicated in recommended circuit.
The Resistor connected the ON-State Nch MOS Drain and $\mathrm{R}_{\text {SET }}$ determines ILED.
ILED can be set according to the next expression.


About combined resistance $\mathrm{R}[\mathrm{k} \Omega$ ]

| M1 | M2 | $R[k \Omega]$ |
| :---: | :---: | :---: |
| ON | ON | $\frac{R_{S E T}[k \Omega] \times R_{1}[k \Omega] \times R_{2}[k \Omega]}{R_{1}[k \Omega] \times R_{S E T}[k \Omega]}+R_{2}[k \Omega] \times R_{S E T}[k \Omega]+R_{1}[k \Omega] \times R_{2}[k \Omega]$ |
| ON | OFF | $R_{\text {SET }}[k \Omega] \times R_{1}[k \Omega]$ |
| OFF | ON | $R_{S E T}[k \Omega]+R_{1}[k \Omega]$ |
| OFF | OFF | $R_{\text {SET }}[k \Omega] \times R_{2}[k \Omega]$ |



[^0]
## Selection of an output number by CTL0, CTL1, and CTL2 Terminal

Truth Table

| Input |  |  |  | Output |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CTL2 | CTL1 | CTL0 | EN | ILED4 | ILED3 | ILED2 | ILED1 |
| L | L | L | H | OFF | OFF | OFF | ON |
| L | L | H | H | OFF | OFF | ON | OFF |
| L | H | L | H | OFF | ON | OFF | OFF |
| L | H | H | H | ON | OFF | OFF | OFF |
| H | L | L | H | OFF | OFF | ON | ON |
| H | L | H | H | OFF | ON | ON | ON |
| H | H | L | H | ON | ON | ON | ON |
| H | H | H | H | OFF | OFF | OFF | OFF |
| L | L | L | L | OFF | OFF | OFF | OFF |
| L | L | H | L | OFF | OFF | OFF | OFF |
| L | H | L | L | OFF | OFF | OFF | OFF |
| L | H | H | L | OFF | OFF | OFF | OFF |
| H | L | L | L | OFF | OFF | OFF | OFF |
| H | L | H | L | OFF | OFF | OFF | OFF |
| H | H | L | L | OFF | OFF | OFF | OFF |
| H | H | H | L | OFF | OFF | OFF | OFF |

## *Soft Start Function

This device is integrated Soft start function. When the power supply is ON or output is started to operate, the transition time is controlled in order to decrease the rush current. (Reference data: The output voltage is time $200 \mu \mathrm{~s}$ of made from 0 to 4.0 V at the $\mathrm{V}_{\mathbb{1}}=2.8 \mathrm{~V}$ time.)
*Inrush Current of Input Current
The inrush current flows when start-up and mode switching. (Reference data: Inrush current at CE1/CE2="L" to "H" is 500mA.)
*Thermal Shut Down Function
This device has Thermal Shutdown Function to protect from thermal damage when the output is shorted.
The temperature to operate this function is set around from 140 to $160^{\circ} \mathrm{C}$. (This is not guaranteed Value.)
*The Selection of Capacitor for Charge Pump, Input and Output
The input capacitor is effective to decrease the impedance of power supply and also input current is averaged.
The input capacitor should be selected by impedance of power supply, it is better to choose with lower ESR
(Equivalent Series Resistor). (i.e. ceramic capacitor etc.) Regarding to the capacitance values, it is recommended to choose in the range from $0.8 \mu \mathrm{~F}$ to $10 \mu \mathrm{~F}$, however larger than $2.2 \mu \mathrm{~F}$ should be better.
The output capacitor is effective to decrease the ripple noise of the output line. Also, it is better to choose the capacitor.) Regarding to the capacitance values, it is recommended to
choose in the range from $0.8 \mu \mathrm{~F}$ to $4.7 \mu \mathrm{~F}$, however larger than $2.2 \mu \mathrm{~F}$ should be better.
The capacitor for charge pump operation is also selected the capacitor with low ESR. .) Regarding to the capacitance values, it is recommended to choose in the range from $0.8 \mu \mathrm{~F}$ to $2.2 \mu \mathrm{~F}$, however larger than $1.0 \mu \mathrm{~F}$ should be better.

## Package Dimensions

QFN16 Unit:mm


Weight: 0.016 g (Typ.)

## Regarding solder ability

Regarding solder ability, the following conditions have been confirmed.

- Solder ability
(1) Use of Sn -63Pb solder bath
- solder bath temperature $=230^{\circ} \mathrm{C}$, dipping time $=5$ seconds, number of times $=$ once, use of R -type flux
(2) Use of $\mathrm{Sn}-3.0 \mathrm{Ag}-0.5 \mathrm{Cu}$ solder bath
- solder bath temperature $=245^{\circ} \mathrm{C}$, dipping time $=5$ seconds, number of times $=$ once, use of R -type flux


## NOTES

- Utmost care is necessary in the design of the output line, VCC, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.
- Do not insert devices in the wrong orientation. Make sure that the positive and negative terminals of power supplies are connected correctly. Otherwise, the rated maximum current of power dissipation may be exceeded and the device may break down or undergo performance degradation, causing it to catch fire or explode and resulting in injury.
- Please take care that IC might be destroyed in case external components were destroyed or not connected exactly.


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[^0]:    *This method is without repeating IC ON/OFF, and no need to consider holding rash current.

