

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



3-TERMINAL POSITIVE VOLTAGE REGULATOR

CL7824DT

TO-252 (DPAK)
Plastic Package



Pin: 1 Input 2 Ground 3 Output

Fixed Voltage Monolithic Integrated Circuit Voltage Regulators is Designed for a Wide Range of Applications

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Input Voltage	V_{IN}	40	V
Power Dissipation at T _a =25°C	P_{D}	2.0	W
Power Dissipation at T _C =25°C	P_{D}	15	W
Operating Free Air, Case or Virtual Junction Temperature Range	T_{j}	0 to 150	°C
Storage Temperature Range	T_{stg}	- 65 to +150	°C
Lead Temperature 1.6mm (1/16 inch) from Case for 10 seconds	T_L	260	°C

Recommended Operating Conditions

recommended operating conditions					
DESCRIPTION	SYMBOL	MIN	TYP	MAX	UNIT
Input Voltage	V _I	27		38	V
Output Current	I _O			1.5	Α
Operating Junction Temperature	T _i	0		125	°C

ELECTRICAL CHARACTERISTICS

(At Specified Virtual Junction Temperature, V_I=33V, I_O=500mA, (unless specified otherwise)

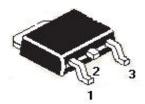
DESCRIPTION	SYMBOL	*TEST CONDITION	MIN	TYP	MAX	UNIT
Output Voltage	**V _O	25°C	23		25	V
		I _O =5mA to 1A, 0°C to 125°C				
		V _I =27V to 38V, P <u><</u> 15W,	22.8		25.2	V
		0°C to 125°C				
Line Regulation	R_{BGIN}	V _I =27V to 38V, 25°C			480	mV
		V_1 =30 to 36V, 25°C			240	mV
Pinnla Paigation	R _R	V _I =28V to 38V, f=120Hz,	50			٩D
Ripple Rejection	IX _R	0º to 125°C	50			dB
Load Regulation	R_{BGL}	I _O =5mA to1.5A, 25°C			480	mV
		I _O =250mA to 750mA,			240	mV
		25°C			240	111 V
Output Resistance	,O	f=1KHz , 0°C to 125°C		0.028		Ω
Temperature Coefficient of Output	$\Delta V_{O}/\Delta T$	I _O =5mA , 0°C to 125°C		- 1.5		mV/ºC
Voltage	Δ ν 0/Δ1	10-31177, 0 0 10 123 0		- 1.5		IIIV/°C
Output Noise Voltage	V_{NO}	f=10Hz to 100KHz, 25°C		170		μV
Dropout Voltage	V _{DIF (min)}	I _O =1A , 25°C		2.0		V

^{*}Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately.

^{**}This specification applies only for dc power dissipation permitted by absolute maximum rating.

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ELECTRICAL CHARACTERISTICS

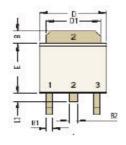
(At Specified Virtual Junction Temperature, V_I=33V, I_O=500mA, (unless specified otherwise)

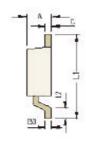
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DESCRIPTION	SYMBOL	*TEST CONDITION	MIN	TYP	MAX	UNIT
Quiescent Current	l _Q	25°C			8.0	mA
Quiescent Current Change	ΔI_{QIN}	V _i =27V to 38V, 0°C to 125°C			1.0	mA
	ΔI_{QL}	I _O =5mA to 1A, 0°C to 125°C			0.5	mA
Short Circuit Output Current	I _{os}	25°C		150		mA
Peak Output Current	I _{omax}	25°C		2.1		Α

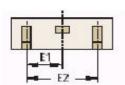
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MARKING	CL	
	7824DT	
	XY MX	
XY= Date Code		

Package TO-252 (DPAK)







DIM	Min	max
Α	2.20	2.40
В	1.30	1.50
B1	0.55	0.65
B2	0.75	0.85
B3	0.46	0.58
C	0.46	0.58
D	6.40	6.60
D1	5.20	5.40
E	5.40	5.60
E1	2.25	2.35
E2	4.50	4.70
E1	9.25	9.75
L2	0.50	
L3	0.90	1.10

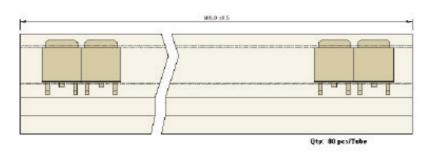
All Dimensions are in mm



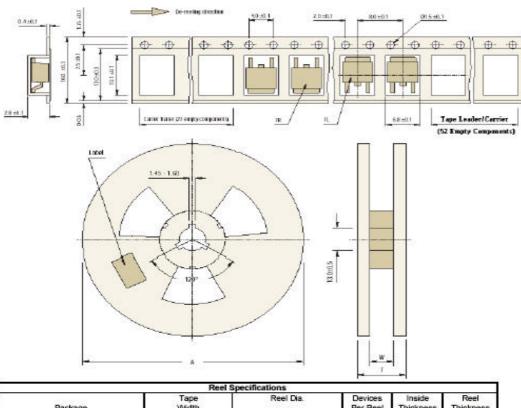
Pin: 1 Input 2 Ground 3 Output

TO-252 (DPAK) Tube Packing





TO-252 (DPAK) Tape and Reel Specification



	Reel S	pecifications			
Package	Tape Width	Reel Dia. A - Max	Devices Per Reel and MOQ	Inside Thickness W	Reel Thickness T - max
TO-252 (DPAK)	16	330	2,500	17.5 ± 1.5	24.0

Packaging Information

Package/ Case Type	Packaging Type	Std. Packing Qty
TO-252 (DPAK)	Tube	1,600
TO-252 (DPAK)	T&R	2.500

T & R: Tape and Reel Tube : 80 pcs/Tube

Customer Notes CL7824DT

TO-252 (DPAK) Plastic Package

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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