

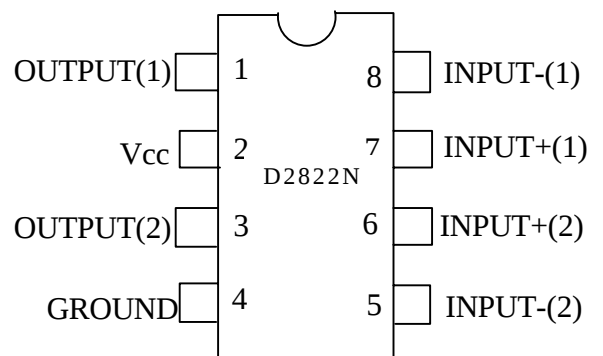


GENERAL DESCRIPTION

Outline Drawing

- Dual Low-voltage power amplifier
- Supply voltage down to 1.8V
- Low crossover distortion
- Low quiescent current
- Bridge or stereo configuration

PINNING



MAXIMUM RATINGS

Characteristics	Symbol	Value	Unit
Supply Voltage	V _{CC}	15	V
Output Peak Current	I _{pk}	1	A
Total Power Dissipation(at T _{amb} =50°C)	P _D	1	W
Total Power Dissipation(at T _{case} =50°C)	P _D	1.4	W
Operating Ambient Temperature Range	T _a	-20~70	°C
Storage Temperature Range	T _{stg}	-40~150	°C

ELECTRICAL CHARACTERISTICS

1.Stereo Test Circuit(Unless otherwise specified V_{CC}=6V,T_{amb}=25°C)

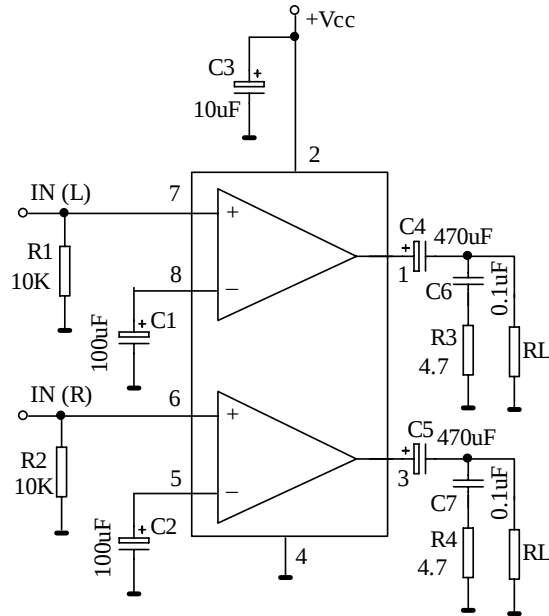
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Supply Voltage	V _{CC}		1.8		15	V
Quiescent Output Voltage	V _O			2.7		V
		V _{CC} =3V		1.2		V
Quiescent Drain Current	I _D			6	9	mA
Input Bias Current	I _{BA}			100		nA
Output Power (Each Channel)	P _O	d=10% f=1kHz V _{CC} =3V R _L =4Ω V _{CC} =3V R _L =32Ω		110 20		mW
		d=10% f=1kHz V _{CC} =9V R _L =8Ω V _{CC} =6V R _L =4Ω V _{CC} =4.5V R _L =4Ω	0.4	1 0.65 0.32		W
Distortion	THD	P _O =0.5W R _L =8Ω f=1kHz V _{CC} =9V		0.3		%
Closed Loop Voltage Gain	A _{vf}	f=1kHz		40		dB
Channel Balance	ΔA _{vf}				± 1	dB
Input Resistance	R _i	f=1kHz	100			kΩ
Total Input Noise	V _{NI}	R _s =10kΩ B=Cure A		2		μV
		R _s =10kΩ B=22Hz to22kHz		3		
Supply Voltage rejection	SVR	f=100Hz C1=C2=100μF	24	30		dB
Channel Separation	CSR	f=1kHz		50		dB

2. Bridge Test Circuit (Unless otherwise specified V_{cc}=6V, T_{amb}=25°C)

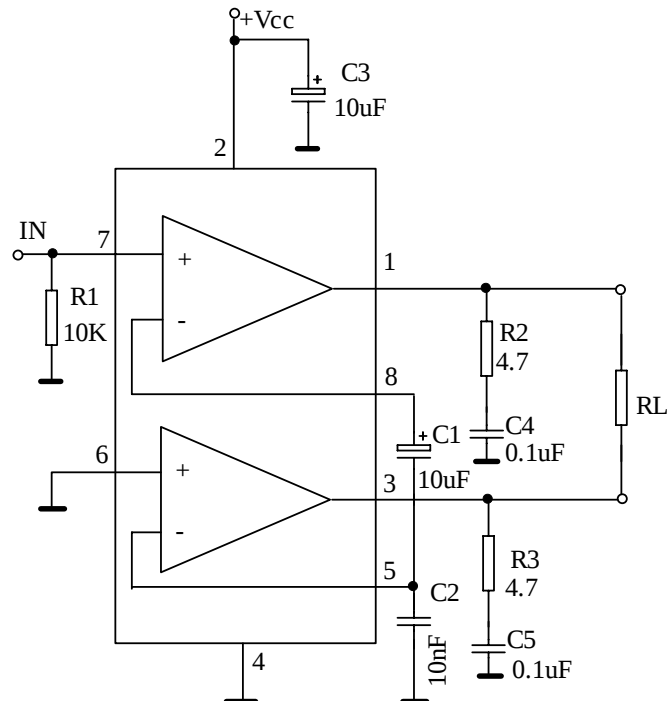
Characteristic	Sym-bol	Test Condition	Min.	Typ.	Max.	Unit
Supply Voltage	V _{cc}		1.8		15	V
Quiescent Drain Current	I _d	R _L =∞		6	9	mA
Output Offset Voltage (Between the Outputs)	V _{os}	R _L =8 Ω			± 50	mV
Input Bias Current	I _b			100		nA
Output Power	P _o	d=10% f=1kHz V _{cc} =9V R _L =16 Ω V _{cc} =6V R _L =8 Ω V _{cc} =4.5V R _L =8 Ω V _{cc} =4.5V R _L =4 Ω		2 1.35 0.7 1		W
		V _{cc} =3V R _L =4 Ω V _{cc} =2V R _L =4 Ω	200	350 80		mW
Distortion	THD	P _o =0.5W R _L =8 Ω f=1kHz		0.2		%
Closed Loop Voltage Gain	A _{vf}	f=1kHz		40		dB
Input Resistance	R _i	f=1kHz	100			k Ω
Total Input Noise	V _{NI}	R _s =10k Ω Curve A		2.5		μ V
		R _s =10k Ω B=22Hz to 22kHz		3		
Supply Voltage Rejection	SVR	f=100Hz		40		dB
Power Bandwidth (-3dB)	B	R _L =8 Ω P _o =1W		120		kHz

TEST CIRCUIT

1.STEREO TEST CIRCUIT

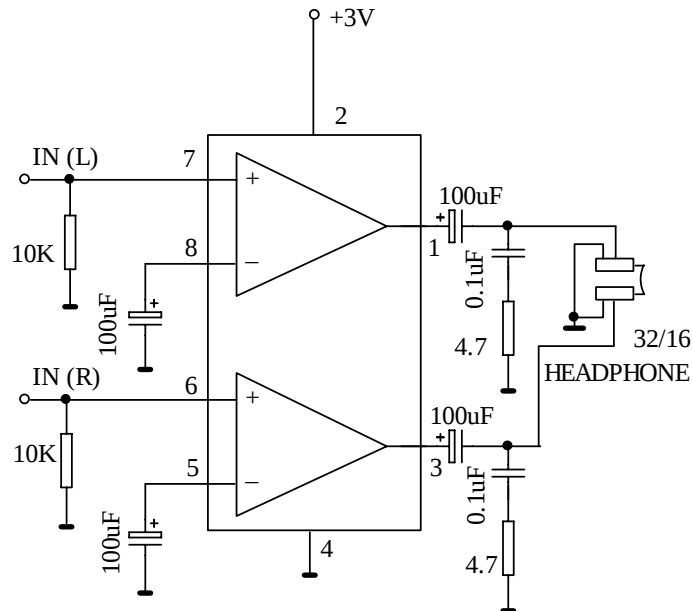


2.BRIDGE TEST CIRCUIT



APPLICATION CIRCUIT

1. Typical application in portable players



CHARACTERISTICS CURVES

