TOSHIBA 2SC2783

### TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# 2 S C 2 7 8 3

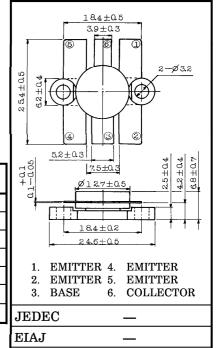
### **UHF BAND POWER AMPLIFIER APPLICATIONS**

• Output Power: Po=40W (Min.) (f=470MHz, V<sub>CC</sub>=12.5V, Pi=13W)

### MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	$v_{CBO}$	36	V
Collector-Emitter Voltage	$V_{CEO}$	16	V
Emitter-Base Voltage	$V_{EBO}$	4	V
Collector Current	$I_{\mathbf{C}}$	8	A
Collector Power Dissipation	PC	150	W
Junction Temperature	$T_j$	175	°C
Storage Temperature Range	$T_{ m stg}$	-65~175	°C

### Unit in mm



2-13C1A

## TOSHIBA Weight: 5.5g

### ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	I <sub>CBO</sub>	$V_{CB} = 15V, I_{E} = 0$	_	_	6	mA
Collector-Base Breakdown Voltage		$I_C=10$ mA, $I_E=0$	36	_	_	V
Collector-Emitter Breakdown Voltage	V <sub>(BR)</sub> CEO	$I_C=50$ mA, $I_B=0$	16	_	_	V
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	$I_E=1mA, I_C=0$	4	_	_	V
DC Current Gain	$h_{ ext{FE}}$	$V_{CE}=5V, I_{C}=5A$ *	10	_	150	
Collector Output Capacitance	Cob	$V_{CB} = 12.5V, I_{E} = 0$ f = 1MHz	_	110	150	pF
Output Power	Po	(Fig.)	40	45	_	W
Power Gain	$G_{\mathrm{p}}$	$V_{\rm CC} = 12.5 V, f = 470 MHz$	4.88	5.4	_	dB
Collector Efficiency	$\eta_{\mathbf{C}}$	Pi=13W	60	65	_	%
Series Equivalent Input Impedance	Zin	V <sub>CC</sub> =12.5V f=470MHz, Po=40W		3.0 +j3.2	_	Ω
Series Equivalent Output Impedance	Z <sub>out</sub>		1	1.7 +j4.7	_	Ω

<sup>\*</sup> Pulse Test: Pulse Width $\leq 100 \mu$ s, Duty Cycle $\leq 3\%$ 

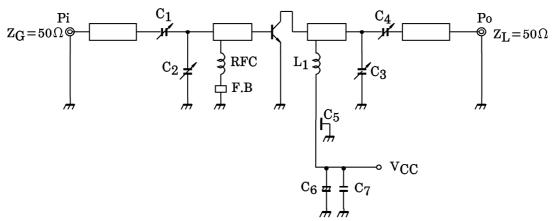
#### **CAUTION**

Beryllia Ceramics is used in this product. The dust or vapor can be dangerous to humans. Do not break, cut, crush or dissolve chemically. Dispose of this properly according to law. Do not intermingle with normal industrial or domestic waste.

TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

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Fig. Po TEST CIRCUIT

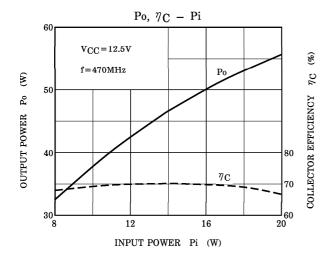


C1~C4 : ~20pF L1 :  $\phi$ 1 SILVER PLATED COPPER WIRE, 6ID, 3T C5 :  $\phi$ 1 ENAMEL COATED COPPER WIRE, 6ID, 10T

 $C_6$ :  $10\mu F$  BOARD: 1.6mm TEFLONGLASS  $\varepsilon_R = 2.5$ 

 $C_7$  :  $0.1\mu F$ 

F·B : FERRITE BEADS



### **CAUTION**

These are only typical curves and devices are not necessarily guaranteed at these curves.

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