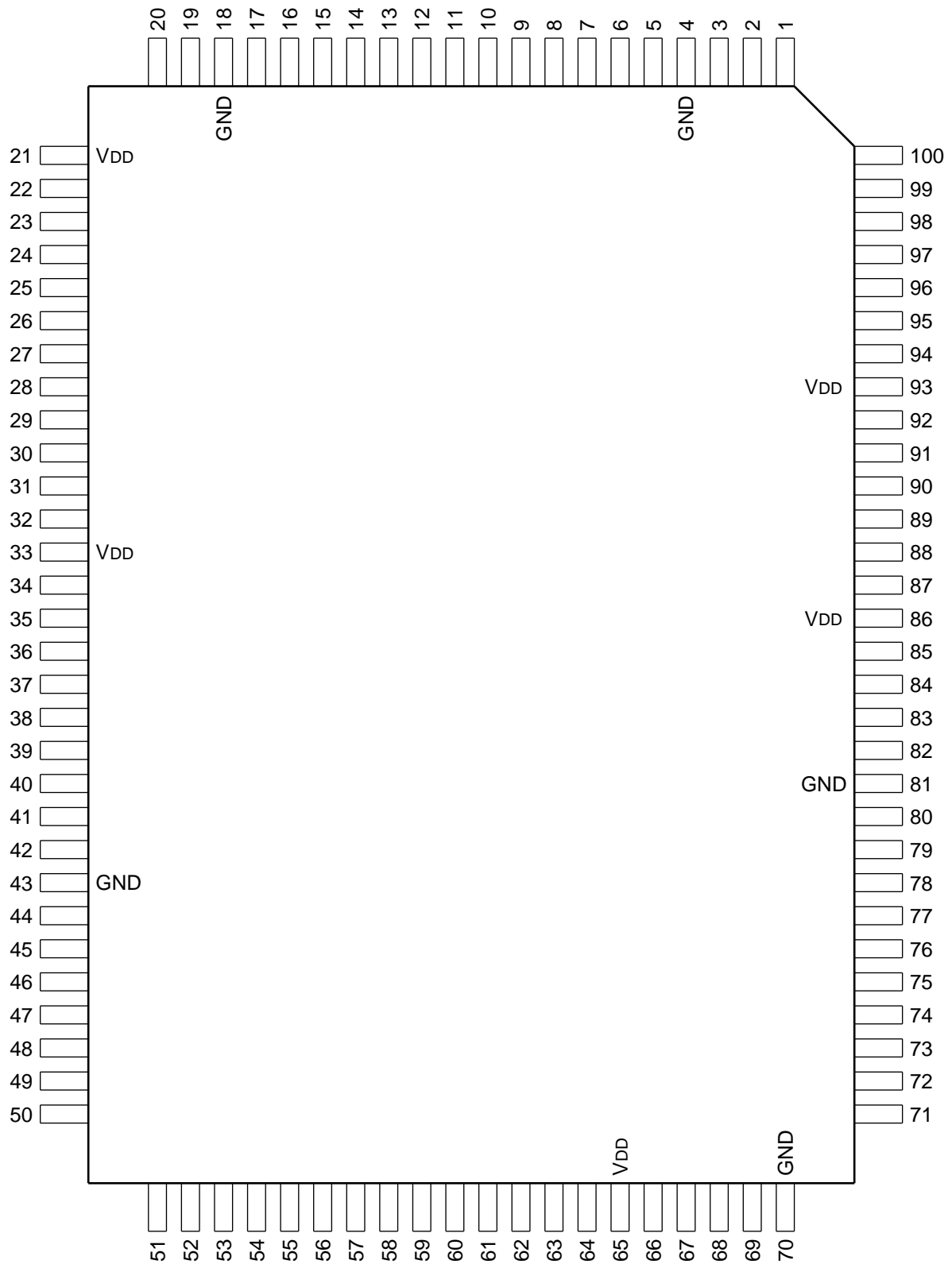

32-BIT RISC PROCESSOR

- TOP VIEW -



		21	33	65	86	93														
		V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}														
8	ANALYSE						14	ERROR												
20	PROC SPEED SELECT 0						30	PROC CLOCK												
9	PROC SPEED SELECT 1							NOT MEM RD												
16	PROC SPEED SELECT 2							NOT MEM WRB0												
24	CLOCK IN							NOT MEM WRB1												
12	RESET							NOT MEM WRB2												
17	ERROR IN							NOT MEM WRB3												
13	BOOT FROM ROM							NOT MEM S0												
10	DISABLE INT RAM							NOT MEM S1												
55	MEM WAIT							NOT MEM S2												
52	MEM REQ							NOT MEM S3												
51	MEM CONFIG							NOT MEM S4												
44	EVENT REQ						26	REFRESH PENDING												
35	LINK IN 0						53	MEM GRANTED												
37	LINK IN 1						42	EVENT ACK												
39	LINK IN 2						25	EVENT WAITING												
41	LINK IN 3						34	LINK OUT 0												
28	LINK SPECIAL						36	LINK OUT 1												
29	LINK 0 SPECIAL						38	LINK OUT 2												
31	LINK 123 SPECIAL						40	LINK OUT 3												
							72	MEM NOT RF D1												
							71	MEM NOT WR D0												
							7	MEM AD 31												
							6	MEM AD 30												
							5	MEM AD 29												
							3	MEM AD 28												
							2	MEM AD 27												
							1	MEM AD 26												
							99	MEM AD 25												
							98	MEM AD 24												
							97	MEM AD 23												
							96	MEM AD 22												
							95	MEM AD 21												
							94	MEM AD 20												
							92	MEM AD 19												
	GND																			
	GND																			
	GND																			
	MEM AD 2																			
	MEM AD 3																			
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	MEM AD 15																			
	MEM AD 16																			
	MEM AD 17																			
	MEM AD 18																			
43		70	81	73	74	75	76	77	78	79	80	82	83	84	85	87	88	89	90	91

INPUT

CLOCK IN ; CLOCK
 PROC SPEED SELECT 0-2 ; PROCESSOR SPEED SELECTOR
 RESET ; SYSTEM RESET
 ERROR IN ; ERROR DAISYCHAIN
 ANALYSE ; ERROR ANALYSIS
 BOOT FROM ROM ; BOOT FROM EXTERNAL ROM
 DISABLE INT RAM ; DISABLE INTERNAL RAM
 MEM WAIT ; MEMORY CYCLE EXTENDER
 MEM REQ ; DIRECT MEMORY ACCESS REQUEST
 MEM CONFIG ; MEMORY CONFIGURATION DATA
 EVENT REQ ; EVENT REQUEST
 LINK IN0-3 ; SERIAL DATA
 LINK SPECIAL ; SELECT NON-STANDARD SPEED
 LINK 0-3 SPECIAL ; SELECT SPECIAL SPEED

OUTPUT

ERROR ; ERROR INDICATOR
 PROC CLOCK OUT ; PROCESSOR CLOCK
 NOT MEM RD ; REED STROBE
 NOT MEM WRB0-3 ; BYTE-ADDRESSING WRITE STROBES
 NOT MEM S0-4 ; FIVE GENERAL PURPOSE STROBES
 NOT MEM RF ; DYNAMIC MEMORY REFRESH INDICATOR
 REFRESH PENDING ; DYNAMIC REFRESH IS PENDING
 MEM GRANTED ; DIRECT MEMORY ACCESS GRANTED
 EVENT ACK ; EVENT REQUEST ACKNOWLEDGE
 EVENT WAITING ; EVENT INPUT REQUESTED BY SOFTWARE
 LINK OUT 0-3 ; SERIAL DATA

INPUT/OUTPUT

MEM NOT WR D0 ; MULTIPLEXED DATA BIT 0
 MEM NOT RF D1 ; MULTIPLEXED DATA BIT 1
 MEM AD2-31 ; MULTIPLEXED DATA AND ADDRESS BUS

