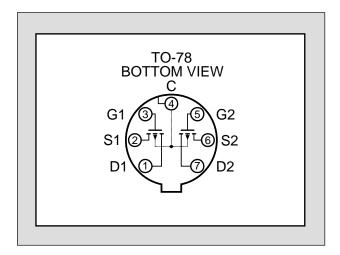


Linear Integrated Systems

FEATURES							
DIRECT REPLACEMENT FOR INTERSIL 3N190 & 3N191							
LOW GATE LEAKAGE CURRENT	I _{GSS} ≤ ±10pA						
LOW TRANSFER CAPACITANCE C _{rss} ≤ 1.0pF							
ABSOLUTE MAXIMUM RATINGS ¹							
@ 25 °C (unless otherwise stated)	@ 25 °C (unless otherwise stated)						
Maximum Temperatures							
Storage Temperature	-65 to +150 °C						
Operating Junction Temperature	-55 to +135 °C						
Maximum Power Dissipation							
Continuous Power Dissipation One Side	300mW						
Continuous Power Dissipation Both Sides	525mW						
Maximum Current							
Drain to Source ²	50mA						
Maximum Voltages							
Drain to Gate ²	30V						
Drain to Source ²	30V						
Transient Gate to Source ^{2,3}	±125V						
Gate to Gate	±80V						

3N190 3N191

P-CHANNEL DUAL MOSFET ENHANCEMENT MODE



MATCHING CHARACTERISTICS @ 25 °C (unless otherwise stated) (V_{BS} = 0V unless otherwise stated)

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
$g_{\rm fs1}/g_{\rm fs2}$	Forward Transconductance Ratio	0.85		1.0		$V_{DS} = -15V$, $I_{D} = -500\mu A$, $f = 1kHz$
V_{GS1-2}	Gate to Source Threshold Voltage Differential			100	mV	V _{DS} = -15V, I _D = -500μA
$\frac{\Delta V_{GS1-2}}{\Delta T}$	Gate to Source Threshold Voltage Differential with Temperature ⁴			100	μV/°C	V_{DS} = -15V, I_D = -500 μ A T_S = -55 TO +25 °C
$\frac{\Delta V_{GS1-2}}{\Delta T}$	Gate to Source Threshold Voltage Differential with Temperature ⁴			100	μν/ Ο	V_{DS} = -15V, I_D = -500 μ A T_S = +25 TO +125 °C

ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated) (V_{SB} = 0V unless otherwise stated)

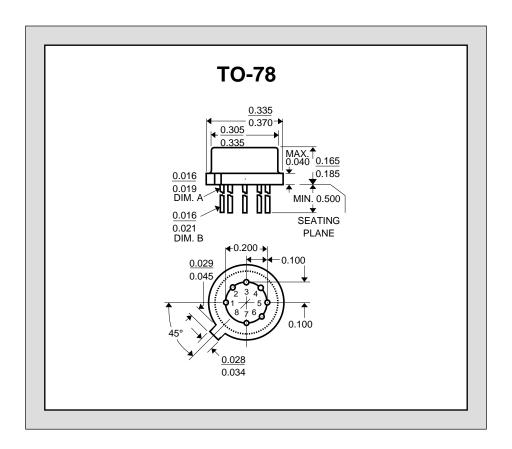
SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
BV _{DSS}	Drain to Source Breakdown Voltage	-40				$I_D = -10 \mu A$
BV _{SDS}	Source to Drain Breakdown Voltage	-40				$I_S = -10\mu A, V_{BD} = 0V$
V_{GS}	Gate to Source Voltage	-3.0		-6.5	V	$V_{DS} = -15V$, $I_D = -500\mu A$
V	Cata to Source Threshold Voltage	-2.0		-5.0		$V_{DS} = V_{GS}$, $I_D = -10\mu A$
$V_{GS(th)}$	Gate to Source Threshold Voltage	-2.0		-5.0		V _{DS} = -15V, I _D = -500μA
I _{GSSR}	Reverse Gate Leakage Current			10		V _{GS} = 40V
I _{GSSF}	Forward Gate Leakage Current			-10	n 1	V _{GS} = -40V
I _{DSS}	Drain Leakage Current "Off"			-200	рA	V _{DS} = -15V
I _{SDS}	Source to Drain Leakage Current "Off"			-400		V _{SD} = -15V, V _{DB} = 0V
I _{D(on)}	Drain Current "On"	-5.0		-30.0	mA	V _{DS} = -15V, V _{GS} = -10V

ELECTRICAL CHARACTERISTICS CONT. @ 25 °C (unless otherwise stated) (V_{SB} = 0V unless otherwise stated)

SYMBOL					
g fs	Forward Transconductance ⁵	1500	4000	Su	$V_{DS} = -15V, I_{D} = -5mA, f = 1kHz$
Yos	Output Admittance		300	μΟ	
r _{ds(on)}	Drain to Source "On" Resistance		300	Ω	V_{DS} = -20V, I_{D} = -100 μ A
C_{rss}	Reverse Transfer Capacitance		1.0		
C _{iss}	Input Capacitance Output Shorted		4.5	pF	$V_{DS} = -15V$, $I_{D} = -5mA$, $f = 1MHz$
C _{oss}	Output Capacitance Input Shorted		3.0		

SWITCHING CHARACTERISTICS

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
t _{d(on)}	Turn On Delay Time			15		45)/ 1
t _r	Turn On Rise Time			30	ns	V_{DD} = -15V, $I_{D(on)}$ = -5mA, $R_G = R_I = 1.4k\Omega$
t _{off}	Turn Off Time			50		110 110 11102



- Absolute maximum ratings are limiting values above which serviceability may be impaired.
- 2. Per transistor.
- Approximately doubles for every 10 $^{\circ}\text{C}$ increase in T_A. 3.
- Pulse: t = 300µs, Duty Cycle ≤ 3%
- Measured at end points, T_{A} and T_{B} .

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