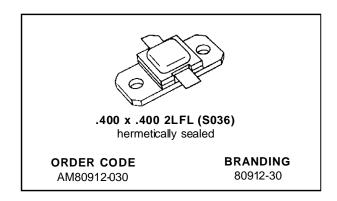


AM80912-030

RF & MICROWAVE TRANSISTORS SPECIALITY AVIONICS/JTIDS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 15:1 VSWR CAPABILITY
- LOW RF THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 30 W MIN. WITH 7.8 dB GAIN



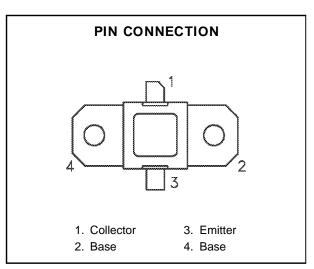
DESCRIPTION

The AM80912-030 device is a high power Class C transistor specifically designed for JTIDS pulsed output and driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles and temperatures and is capable of withstanding 15:1 output VSWR at rated RF conditions.

Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM80912-030 is supplied in the hermetic metal/ceramic package with internal input matching structures.



ABSOLUTE MAXIMUM RATINGS (Tcase = 25°C)

| Symbol | Parameter | Value | Unit |
|-------------------|--|--------------|------|
| P _{DISS} | Power Dissipation* (T _C ≤ 85°C) | 75 | W |
| Ic | Collector Current* | 3.5 | А |
| Vcc | Collector-Supply Voltage* | 40 | V |
| TJ | Junction Temperature (Pulsed RF Operation) | 250 | °C |
| T _{STG} | Storage Temperature | - 65 to +200 | °C |

THERMAL DATA

| R _{TH(j-c)} | Junction-Case Thermal Resistance | 2.2 | °C/W |
|----------------------|----------------------------------|-----|------|

^{*}Applies only to rated RF amplifier operation.

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ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

| | | | | Value | | | |
|-------------------|-----------------------|---------------------|--|-------|------|------|------|
| Symbol | Test Conditions | | | Min. | Тур. | Max. | Unit |
| BV _{CBO} | I _C = 10mA | | | 55 | _ | | V |
| BV _{EBO} | I _E = 1mA | | | 3.5 | _ | _ | V |
| BV _{CER} | I _C = 20mA | $R_{BE} = 10\Omega$ | | 55 | _ | _ | V |
| Ices | V _{CE} = 35V | | | _ | _ | 5.0 | mA |
| h _{FE} | V _{CE} = 5V | $I_C = 1.0A$ | | 15 | _ | 150 | _ |

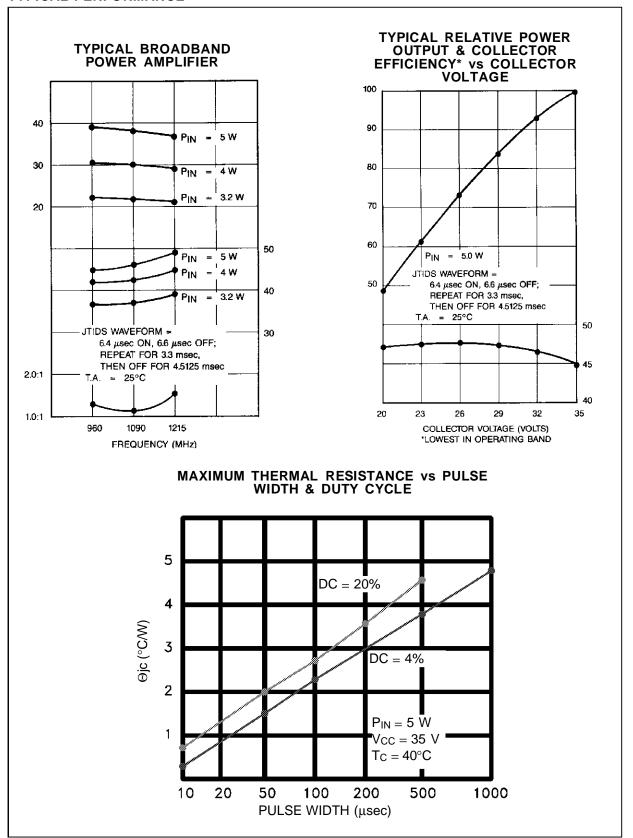
DYNAMIC

| | | | Value | | | | |
|----------------|-------------------|------------------------|-----------------|------|------|------|------|
| Symbol | 7 | Test Conditions | | Min. | Тур. | Max. | Unit |
| Роит | f = 960 — 1215MHz | $P_{IN} = 5.0W$ | $V_{CC} = +35V$ | 30 | 36 | _ | W |
| η _c | f = 960 — 1215MHz | $P_{IN} = 5.0W$ | $V_{CC} = +35V$ | 40 | 45 | _ | % |
| G _P | f = 960 — 1215MHz | P _{IN} = 5.0W | Vcc = +35V | 7.8 | 8.6 | _ | dB |

te: Pulse format: 6.4 $\,\mu s$ on 6.6 $\,\mu s$ off, repeat for 3.3 ms, then off for 4.5125 ms.

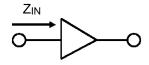
Duty Cycle: Burst 49.2%, overall 20.8%

TYPICAL PERFORMANCE

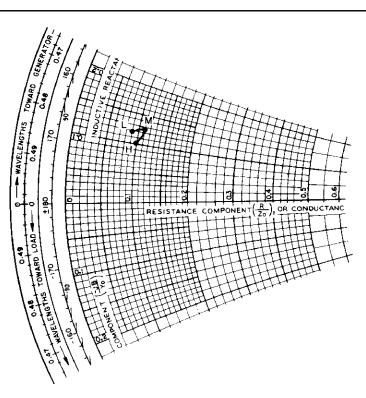


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

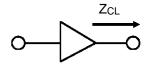


 $\begin{array}{l} P_{IN} = 5W \\ V_{CC} = +35V \\ Z_{O}^* = 50\Omega \end{array}$



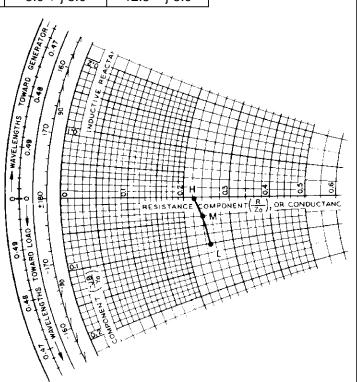
| FREQ. | Z _{IN} (Ω) | Z _{CL} (Ω) |
|--------------|---------------------|---------------------|
| L = 960 MHz | 4.5 + j 6.0 | 11.0 – j 0.5 |
| M = 1090 MHz | 5.5 + j 6.3 | 12.0 – j 2.0 |
| H = 1215 MHz | 5.0 + j 5.0 | 12.5 – j 5.0 |

TYPICAL COLLECTOR LOAD IMPEDANCE

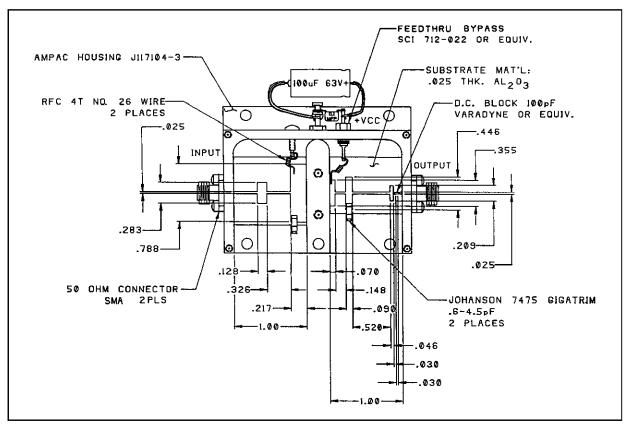


 $\begin{array}{l} P_{IN} = 5W \\ V_{CC} = +35V \\ Z_{O}^* = 50\Omega \end{array}$

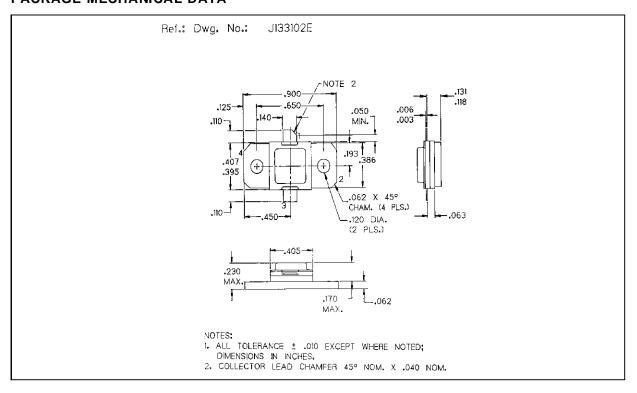
*Normalized Impedance



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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