❑ MN101C273

Type			MN101C273
ROM (×8-bit)			4 K
RAM (×8-bit)			0.5 K
Package **(Conventional Package)**			SDIP028-P-0400D *Lead-free (SDIP028-P-0400)
Minimum Instruction Execution Time			0.238 µs (at 2.7 V to 5.5 V, 8.39 MHz) 1.00 µs (at 2.0 V to 5.5 V, 2 MHz)* * The lower limit for operation guarantee for EPROM built-in type is 2.7 V.
Interrupts			• RESET • Watchdog • External 0 • External 1 • Timer 2 • Timer 3 • Serial 0 • A/D conversion finish
Timer Counter			Timer counter 2 : 8-bit × 1 (square-wave/8-bit PWM output, event count, synchronous output event) Clock source ····· 1/1, 1/4 of system clock frequency; external clock input Interrupt source ····· coincidence with compare register 2 Timer counter 3 : 8-bit × 1 (square-wave output, event count, generation of remote control carrier, serial 0 baud rate timer) Clock source ····· 1/4, 1/16 of system clock frequency; 1/1 of OSC oscillation clock frequency; external clock input Interrupt source ····· coincidence with compare register 3 Timer counter 2, 3 can be cascade-connected. Watchdog timer Interrupt source ····· 1/1048576 of system clock frequency
Serial Interface			Serial 0 : synchronous type/simple UART (half-duplex) × 1 Clock source ····· 1/2, 1/4, 1/16 of system clock frequency; output of timer counter 3
I/O Pins	**I/O**	16	• Common use • Specified pull-up resistor available • Input/output selectable (bit unit)
	Input	6	• Common use • Specified pull-up resistor available
A/D Inputs			10-bit × 4-ch. (with S/H)
Special Ports			Remote control carrier signal output, high-current drive port

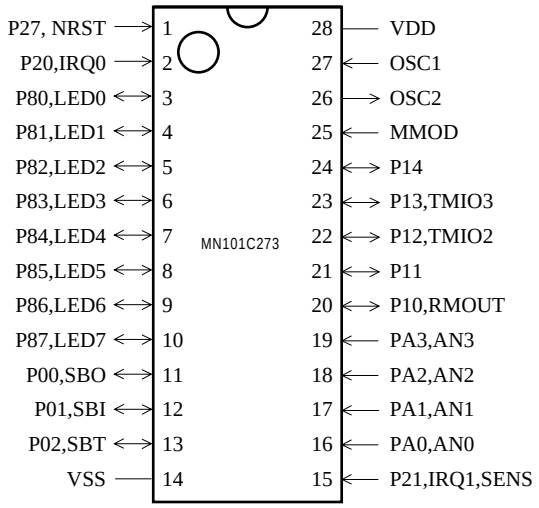
Electrical Characteristics

Supply current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating supply current	IDD1	fosc = 8.39 MHz, VDD = 5 V		10	25	mA
Supply current at HALT	IDD2	fosc = 8.39 MHz, VDD = 5 V		1.2	3	mA
Supply current at STOP	IDD3	VDD = 5 V, Ta = 25°C			2	µA
		VDD = 5 V, Ta = –40°C to +85°C			20	µA

Pin Assignment

( ) : Conventional Package



SDIP028-P-0400D *Lead-free

(SDIP028-P-0400)

Support Tool

In-circuit Emulator	PX-ICE101C/D+PX-PRB101C27-SDIP028-P-0400	
EPROM Built-in Type	Type	MN101CP273
	ROM (× 8-bit)	4 K
	RAM (× 8-bit)	0.5 K
	Minimum instruction execution time	0.238 µs (at 2.7 V to 5.5 V, 8.39 MHz)
	Package	SDIP028-P-0400D *Lead-free
	(Conventional Package)	(SDIP028-P-0400)

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(1) An export permit needs to be obtained from the competent authorities of the Japanese Government if any of the products or technologies described in this material and controlled under the "Foreign Exchange and Foreign Trade Law" is to be exported or taken out of Japan.

(2) The technical information described in this material is limited to showing representative characteristics and applied circuits examples of the products. It neither warrants non-infringement of intellectual property right or any other rights owned by our company or a third party, nor grants any license.

(3) We are not liable for the infringement of rights owned by a third party arising out of the use of the product or technologies as described in this material.

(4) The products described in this material are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
- Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
- Any applications other than the standard applications intended.

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(6) When designing your equipment, comply with the guaranteed values, in particular those of maximum rating, the range of operating power supply voltage, and heat radiation characteristics. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.

(7) When using products for which damp-proof packing is required, observe the conditions (including shelf life and amount of time let standing of unsealed items) agreed upon when specification sheets are individually exchanged.