

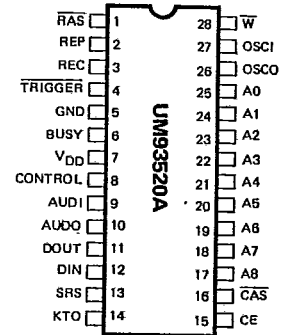
UM93520A/B Speech Recording and Reproduction IC (with DRAM)

Features

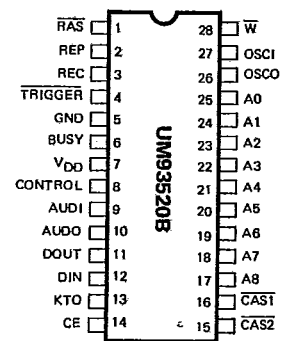
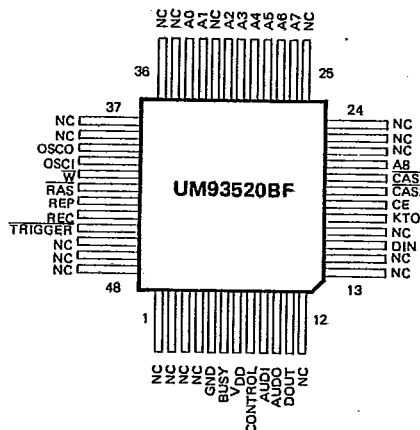
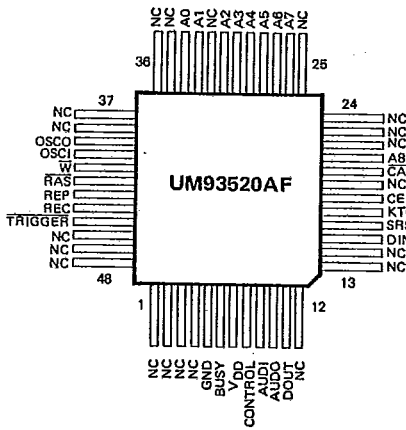
- Uses ADM algorithm to process voice data
- Built-in DRAM refresh circuit
- Three selectable sampling rates:
 UM93520A with one 256K DRAM: 16 KHz or 22 KHz by pin option
 UM93520B with two 256K DRAMs: 32 KHz
- On-chip oscillator for 3.579545 MHz crystal oscillator
- Single 5-volt power supply
- Available in 28 pin DIP (UM93520A/B) or 48-pin flat package (UM93520AF/BF)

Pin Configurations

28-Pin DIP



48-Pin Flat Packages



Ordering Information

Part No.	Sampling Rate	DRAM	Package
UM93520A	16 KHz or 22 KHz by pin option	one 256K DRAM	28 pin DIP
UM93520AF			48 pin FP
UM93520B	32 KHz	two 256K DRAMs	28 pin DIP
UM93520BF			48 pin FP