



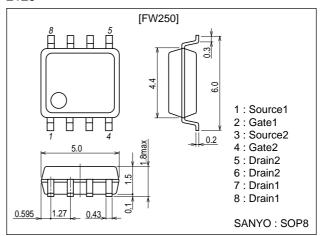
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switcing.
- · 4V drive.

Package Dimensions

unit : mm 2129



Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|--------|--|-------------|------|
| Drain-to-Source Voltage | VDSS | | 60 | V |
| Gate-to-Source Voltage | VGSS | | ±20 | V |
| Drain Current (DC) | ID | | 3 | Α |
| Drain Current (PW≤10s) | ID | duty cycle≤1% | 3.5 | А |
| Drain Current (PW≤100ms) | ID | duty cycle≤1% | 5.5 | Α |
| Drain Current (Pulse) | IDP | PW≤10μs, duty cycle≤1% | 20 | Α |
| Allowable Power Dissipation | PD | Mounted on a ceramic board(2000mm²X0.8mm)1unit, PW≤10s | 1.8 | W |
| Total Dissipation | PT | Mounted on a ceramic board(2000mm²X0.8mm), PW≤10s | 2.2 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Electrical Characteristics at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|--|-----------------------|--|---------|-----|-----|-------|
| | | | min | typ | max | Offic |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | ID=1mA, VGS=0 | 60 | | | V |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =60V, V _{GS} =0 | | | 1 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} = ±16V, V _{DS} =0 | | | ±10 | μΑ |
| Cutoff Voltage | Vgs(off) | V _{DS} =10V, I _D =1mA | 1.2 | | 2.6 | V |
| Forward Transfer Admittance | yfs | V _{DS} =10V, I _D =3A | 2.8 | 4 | | S |
| Static Drain-to-Source On-State Resistance | R _{DS} (on)1 | I _D =3A, V _{GS} =10V | | 110 | 145 | mΩ |
| | R _{DS} (on)2 | I _D =1.5A, V _{GS} =4V | | 150 | 215 | mΩ |

Marking: W250 Continued on next page.

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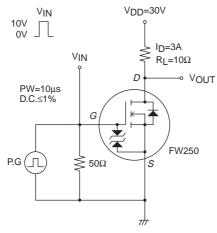
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| Parameter | Cumbal | Conditions | | Ratings | | |
|-------------------------------|----------------------|--|-----|---------|-----|------|
| | Symbol | | min | typ | max | Unit |
| Input Capacitance | Ciss | V _{DS} =20V, f=1MHz | | 300 | | pF |
| Output Capacitance | Coss | V _{DS} =20V, f=1MHz | | 54 | | pF |
| Reverse Transfer Capacitance | Crss | V _{DS} =20V, f=1MHz | | 34 | | pF |
| Turn-ON Delay Time | t _d (on) | See specified Test Circuit. | | 8 | | ns |
| Rise Time | t _r | See specified Test Circuit. | | 23 | | ns |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit. | | 30 | | ns |
| Fall Time | tf | See specified Test Circuit. | | 40 | | ns |
| Total Gate Charge | Qg | V _{DS} =30V, V _{GS} =10V, I _D =3A | | 7.8 | | nC |
| Gate-to-Source Charge | Qgs | V _{DS} =30V, V _{GS} =10V, I _D =3A | | 2.4 | | nC |
| Gate-to-Drain "Miller" Charge | Qgd | V _{DS} =30V, V _{GS} =10V, I _D =3A | | 1.7 | | nC |
| Diode Forward Voltage | V _{SD} | I _S =3A, V _{GS} =0 | | 0.86 | 1.2 | V |

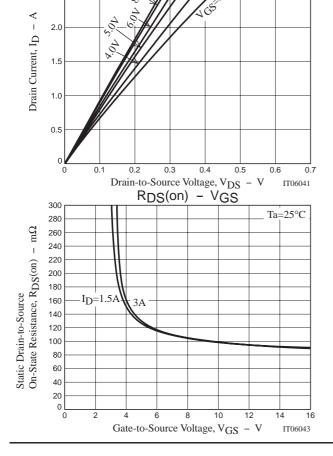
Switching Time Test Circuit

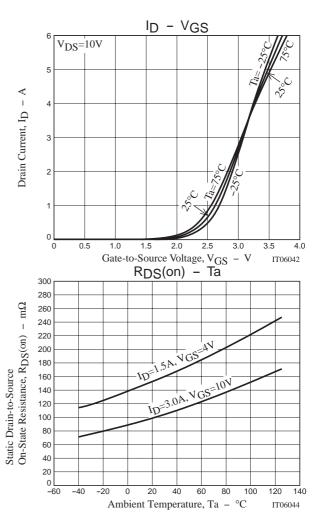
3.0

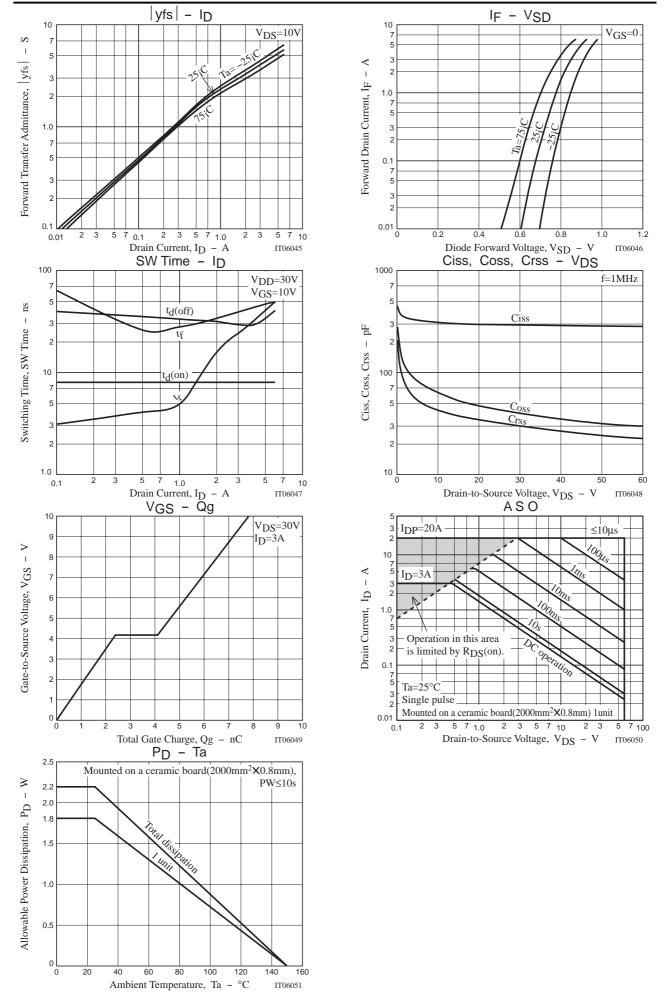
2.5



ID - VDS







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