

# NEC's 7.5 V UHF BAND NE5511279A RF POWER SILICON LD-MOS FET

# **FEATURES**

#### HIGH OUTPUT POWER:

Pout = 40.0 dBm TYP., f = 900 MHz, VDS = 7.5 V, Pout = 40.5 dBm TYP., f = 460 MHz, VDS = 7.5 V,

#### • HIGH POWER ADDED EFFICIENCY:

 $\eta$ add = 48% TYP., f = 900 MHz, VDS = 7.5 V,  $\eta$ add = 50% TYP., f = 460 MHz, VDS = 7.5 V,

#### • HIGH LINEAR GAIN:

GL = 15.0 dB TYP., f = 900 MHz, VDS = 7.5 V,GL = 18.5 dB TYP., f = 460 MHz, VDS = 7.5 V,

#### • SURFACE MOUNT PACKAGE:

5.7 x 5.7 x 1.1 mm MAX

#### • SINGLE SUPPLY:

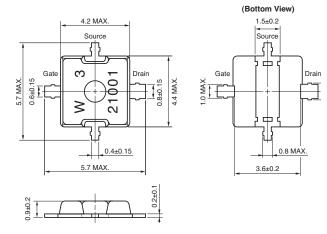
VDS = 2.8 to 8.0 V

### **APPLICATIONS**

- UHF RADIO SYSTEMS
- CELLULAR REPEATERS
- **TWO-WAY RADIOS**
- FRS/GMRS
- FIXED WIRELESS

### **OUTLINE DIMENSIONS** (Units in mm)

#### **PACKAGE OUTLINE 79A**



# **DESCRIPTION**

NEC's NE5511279A is an N-Channel silicon power laterally diffused MOSFET specially designed as the transmission power amplifier for 7.5 V radio systems. Die are manufactured using NEC's NEWMOS1 technology and housed in a surface mount package. This device can deliver 40.0 dBm output power with 48% power added efficiency at 900 MHz using a 7.5 V supply voltage.

# ELECTRICAL CHARACTERISTICS (TA = 25°C)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT	TEST CONDITIONS
Pout	Output Power	38.5	40.0	ı	dBm	f = 900 MHz, V <sub>DS</sub> = 7.5 V,
ΙD	Drain Current	_	2.5	ı	А	P <sub>in</sub> = 27 dBm,
ηadd	Power Added Efficiency	42	48	ı	%	IDSQ = 400 mA (RF OFF)
GL	Linear Gain	-	15.0	ı	dB	P <sub>in</sub> = 5 dBm
Pout	Output Power	-	40.5	_	dBm	f = 460 MHz, V <sub>DS</sub> = 7.5 V,
ΙD	Drain Current	-	2.75	_	Α	P <sub>in</sub> = 25 dBm,
ηadd	Power Added Efficiency	-	50	_	%	IDSQ = 400 mA (RF OFF)
GL	Linear Gain	-	18.5	_	dB	P <sub>in</sub> = 5 dBm
Igss	Gate to Source Leak Current	-	_	100	nA	Vgs = 6.0 V
IDSS	Drain to Source Leakage Current (Zero Gate Voltage Drain Current)	-	-	100	nA	V <sub>DS</sub> = 8.5 V
V <sub>th</sub>	Gate Threshold Voltage	1.0	1.5	2.0	V	V <sub>DS</sub> = 4.8 V, I <sub>DS</sub> = 1.5 mA
Rth	Thermal Resistance	_	5	-	°C/W	Channel to Case
<b>g</b> m	Transconductance	-	2.3	-	S	Vps = 3.5 V, lps = 900 mA
BVDSS	Drain to Source Breakdown Voltage	20	24	_	V	Ibss = 15 μA

#### Notes:

DC performance is 100% tested. RF performance is tested on several samples per wafer. Wafer rejection criteria for standard devices is 1 reject for several samples.

# ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = 25 °C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
VDS	Drain Supply Voltage <sup>2</sup>	V	20.0
Vgs	Gate Supply Voltage	V	6.0
ID	Drain Current	Α	3.0
Ртот	Total Power Dissipation	W	20
Тсн	Channel Temperature	°C	125
Тѕтс	Storage Temperature	°C	-55 to +125

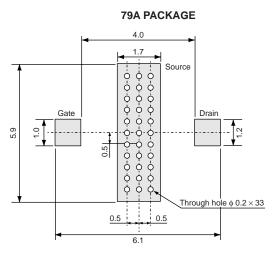
#### Note:

- 1. Operation in excess of any one of these parameters may result in permanent damage.
- 2. Vps must be used under 12 V on RF operation.

# RECOMMENDED OPERATING LIMITS

SYMBOLS	PARAMETERS	UNITS	TYP	MAX
VDS	Drain to Source Voltage	V	7.5	8.0
Vgs	Gate Supply Voltage	V	2.0	3.0
IDS	Drain Current <sup>1</sup>	Α	2.5	3.0
Pin	Input Power f = 900 MHz, VDs = 7.5 V	dBm	27	30

# P.C.B. LAYOUT (Units in mm)



#### Note:

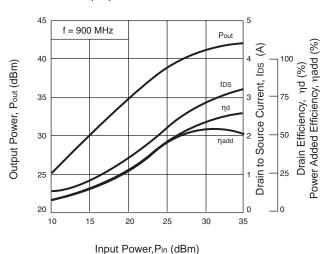
Use rosin or other material to prevent solder from penetrating through-holes.

# ORDERING INFORMATION

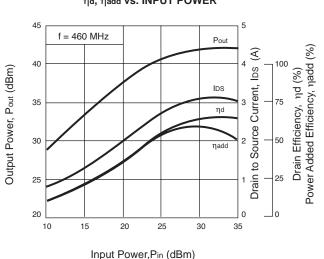
PART NUMBER	QTY
NE5511279A-T1	<ul> <li>12 mm wide embossed taping.</li> <li>Gate pin faces the perforation side of the tape.</li> <li>1 Kpcs/Reel</li> </ul>
NE5511279A-T1A	12 mm wide embossed taping.     Gate pin faces the perforation side of the tape.     5 Kpcs/Reel

# TYPICAL PERFORMANCE CURVES (TA = 25°C)

# OUTPUT POWER, DRAIN CURRENT, $\eta$ d, $\eta$ add vs. INPUT POWER



# OUTPUT POWER, DRAIN CURRENT, $\eta \text{d},\, \eta \text{add}$ vs. INPUT POWER



### RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) Soldering time (per pin of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350-P3

Caution Do not use different soldering methods together (except for partial heating).

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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