**TOSHIBA** TD62M2701F

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

# T D 6 2 M 2 7 0 1 F

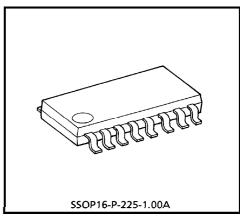
## LOW SATURATION VOLTAGE H-BRIDGE DRIVER

TD62M2701F is multi-chip H-bridge driver IC incorporates 4 low saturation discrete transistors which equipped biasresistor and fly-wheel diode. This IC is suitable for forward-reverse control on a battery use motor drive applications.

### **FEATURES**

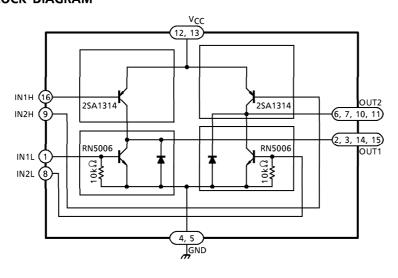
- Suitable for high efficiency motor drive circuit
- Built-in fly-wheel diode (lower side)
- Built-in bias resistor (lower side) :  $R = 10k\Omega$  (Typ.)
- SSOP 16 1mm pitch package sealed
- Low saturation voltage

:  $V_{CE (sat)}$  (upper + lower) = 0.23V (Typ.) :  $I_{O}$  = 1A  $= 0.45V (Typ.) : I_O = 2A$ 

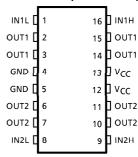


Weight: 0.14g (Typ.)

### **BLOCK DIAGRAM**



### PIN CONNECTION (TOP VIEW)



- 961001EBA.

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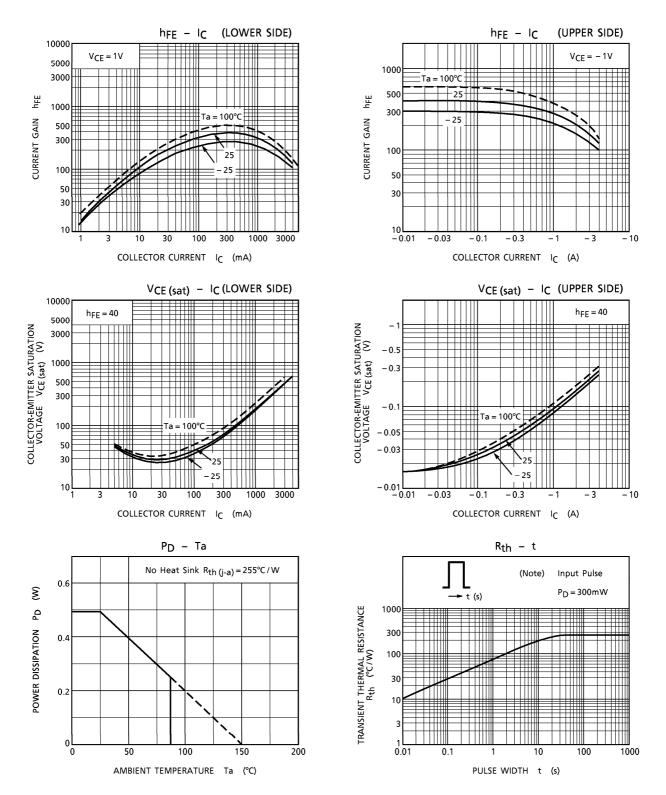
# **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	Vcc	10	V	
Collector-Base Voltage	V <sub>CBO</sub>	10	V	
Collector-Emitter Voltage	V <sub>CER</sub>	10	٧	
Emitter-Base Voltage	V <sub>EBO</sub>	6	V	
Output Current	lout	2	^	
	O (PEAK)	4 (Note 1)	Α	
Base Current	IB	±0.4	А	
base current	IB (PEAK)	±0.8 (Note 1)		
Diode Forward Current	IF	2 (Note 2)	Α	
Power Dissipation	PD	490	mW	
Junction Temperature	Tj	150	°C	
Operating Temperature	Topr	- 40~85	°C	
Storage Temperature	T <sub>stg</sub>	- 55~150	°C	

(Note 1) T = 10ms Max. and maximum duty is less than 30% (Note 2) T = 10ms single pulse

# **ELECTRICAL CHARACTERISTICS** (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Current Gain	Upper Side	h <sub>FE</sub> (1)	—	$V_{CE} = -1V, I_{C} = -0.5A$	200	_	700		
	Lower Side	h <sub>FE</sub> (1)	—	$V_{CE} = 1V, I_{C} = 0.5A$	160	-	700	_	
		h <sub>FE</sub> (2)	_	$V_{CE} = 1V, I_{C} = 2.0A$	60	130	_		
Output Saturation Voltage	Upper Side	VCE (sat)	_	$I_C = -1A$ , $I_B = -25mA$	_	- 0.10	- 0.22	V	
				$I_C = -2A$ , $I_B = -50mA$	_	-0.20	- 0.45		
	Lower Side			I <sub>C</sub> = 1A, I <sub>B</sub> = 25mA	_	0.13	0.22		
				$I_C = 2A$ , $I_B = 50mA$	_	0.25	0.45		
	Summing Total			I <sub>C</sub> = 0.5A, I <sub>B</sub> = 12.5mA	_	_	0.20		
				I <sub>C</sub> = 1A, I <sub>B</sub> = 25mA	_	0.23	0.42		
				$I_C = 2A$ , $I_B = 50mA$	_	0.45	0.85		
Transition Frequency		f <sub>T</sub>	_	$V_{CE} = 2V, I_{C} = 0.5A$	_	150	_	MHz	
Output Leakage	Upper Side	1		V <sub>CC</sub> = -10V	_	0	- 5	μΑ	
Current	Lower Side	lOL	-	V <sub>CC</sub> = 10V	_	0	5		
Diode Forward Voltage		V <sub>F</sub>	_	I <sub>F</sub> = 300mA	_	0.89	1.2	V	
(Lower Side)				I <sub>F</sub> = 450mA 10ms	_	1.60	_		
Base-Emitter Resistance		R <sub>BE</sub>	_	_	7	10	13	kΩ	
Base-Emitter Upper Side		_	$V_{CE} = -1V, I_{C} = -2A$	_	- 0.84	- 1.5			
Forward Voltage	Lower Side		_	$V_{CE} = 1V$ , $I_C = 2A$	_	0.84	1.5	1.5 V	



### PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V<sub>CC</sub> and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

# OUTLINE DRAWING SSOP16-P-225-1.00A Unit:mm 0.6TYP 8.7MAX 8.2±0.2 7.0±5-1.00A Unit:mm 0.525±0.2

Weight: 0.14g (Typ.)