MA2SV05

Silicon epitaxial planar type

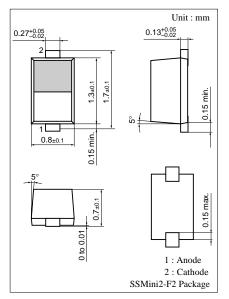
For VCO

■ Features

- \bullet Good linearity and large capacitance-ratio in $C_D \!-\!\!\!-\!\!\!\!- V_R$ relation
- Small series resistance r_D
- SS-mini type package, allowing downsizing of equipment and automatic insertion through the taping package

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Reverse voltage (DC)	V_R	10	V
Junction temperature	T _j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C



Marking Symbol: 3A

\blacksquare Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse current (DC)	I_R	$V_R = 5 V$			10	nA
Diode capacitance	C _{D(1V)}	$V_R = 1 V, f = 1 MHz$	18.5		20.5	pF
	C _{D(4V)}	$V_R = 4 V, f = 1 MHz$	3.6		4.1	pF
Capacitance ratio	C _{D(1V)} /C _{D(4V)}		4.7			_
Series resistance*	r_{D}	$V_R = 4 \text{ V, f} = 470 \text{ MHz}$			0.65	Ω

Note) 1 .Rated input/output frequency: 470 MHz

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^{2.*}: r_f measuring instrument: YHP MODEL 4191A RF IMPEDANCE ANALYZER

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