TOSHIBA TG2006F

TOSHIBA GaAs LINEAR INTEGRATED CIRCUIT GaAs MONOLITHIC

TG2006F

1.9 GHz BAND POWER AMPLIFIER PHS, DIGITAL CORDLESS TELECOMMUNICATION

FEATURES

• Positive Voltage Operation : $V_d = 3 V$, $V_q = 0$ or 1 V

• Low Current Consumption : It = 130 mA (Typ.)

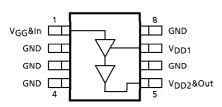
Small Package : SM8 Package

 $(2.9 \times 2.8 \times 1.1 \text{ mm})$

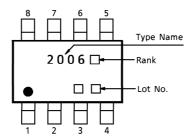
• Low Cost : Can be achieved minimum

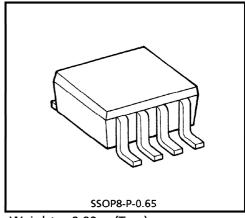
function.

PIN ASSIGNMENT (TOP VIEW)



MARKING





Weight: 0.02 g (Typ.)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD1}	5	V
Supply Voltage	V_{DD2}	5	V
Gate Voltage	V_{GG}	1	V
Input Power	Pi	10	mW
Power Dissipation	P _d (Note)	250	mW
Operating Temperature Range	T _{opr}	- 40∼85	°C
Storage Temperature Range	T _{stg}	- 55∼150	°C

(Note): When mounted on 2.5 cm² x 1.6 t glass epoxy board.

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CLASSIFY RANK

This device is classified by Fig.1.

And satisfy ELECTRICAL CHARACTERISTICS by $V_{\mbox{\scriptsize q}}$ Condition on each rank.

The rank division is performed for every reel and can't order to choose any rank.

Table 1

RANK	V _g CONDITION
Α	$V_g = 0 V$
В	$V_g = 1 V$

CAUTION

This device is electrostatic sensitivity. Please handle with caution.

ELECTRICAL CHARACTERISTICS

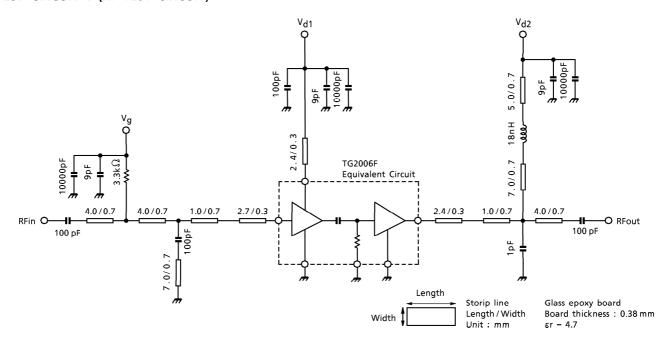
 $(V_d = 3 \text{ V}, V_g = (\text{Note 1}), f = 1.9 \text{ GHz}, Ta = 25^{\circ}\text{C}, Z_g = ZI = 50 \Omega 1/2 \text{ duty operation})$

$(v_d = 3 \text{ v}, v_g = (\text{Note } 1), 1 = 1.9 \text{ GHz}, 1a = 25 \text{ C}, z_g = z_1 = 50 \text{ Mz}$ 1/2 duty operation)										
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT		
Frequency	f _{range}	_	_		1895	_	1918	MHz		
Total Current	lt	1	$P_0 = 21 \text{ dBmW}, P_i = \text{Regulation}$		_	130	150	mA		
Gate Current	IG	1			_	_	1			
Output Power	Ро	1	P _i = 1 dBmW		21	_	_	dBmW		
Small Signal Gain	Gp	1	$P_i = -20 dBmW$		21	23	_	dB		
Adjacent Channel Leakage Power Ratio	ACP (1)	1	\dashv P _i = Regulation \vdash	$\Delta_{f} = 600 \text{ kHz}$		- 60	- 55	dB		
	ACP (2)	1		$\Delta_{f} = 900 \text{ kHz}$	_	- 65	- 60			
Harmonics -	2f ₀	1	$P_0 = 21 \text{ dBmW}, P_i = \text{Regulation}$		_	_	- 30	dB		
	3f ₀	1			_	_	- 30			
Input VSWR	VSWR _{in}	1	$P_0 = 21 dBmW, P_i$	= Regulation	_	1.5	2.5	_		
Load Mismatch	_	_	V_d = 4.0 V, V_g = (Note 1), P_o = 21 dBmW, P_i = Regulation, Z_g = 50 Ω VSWR Load = 20 : 1 all phase		No Degradation			_		
Stability	_	_	$V_d = 2.7 \sim 4.0 \text{ V}, V_g = (\text{Note 1}),$ $P_i = -2 \text{ mW} \sim 4 \text{ dBmW},$ $Z_g = 50 \Omega$ VSWR Load = 6 : 1 all phase			All spurious output than 60 dB below desired signal				

(Note 1) : Vg Voltage is decided on Fig.1. (Note 2) : Input signal is modulated to π / 4QPSK (α = 0.5). Bit rate is 384 kbps.

(Note 3) : $V_d = V_{d1} = V_{d2}$, It = Id1 + Id2

TEST CIRCUIT 1 (RF TEST CIRCUIT)



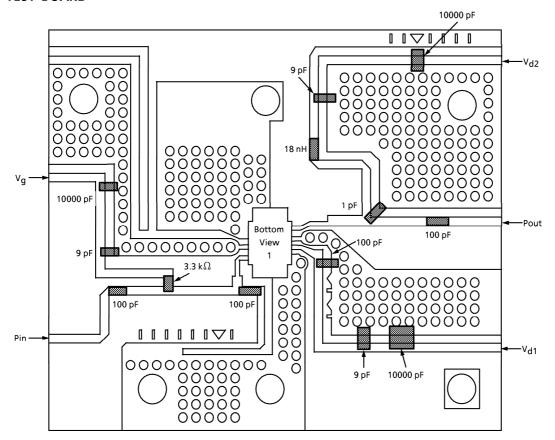
NOTICE

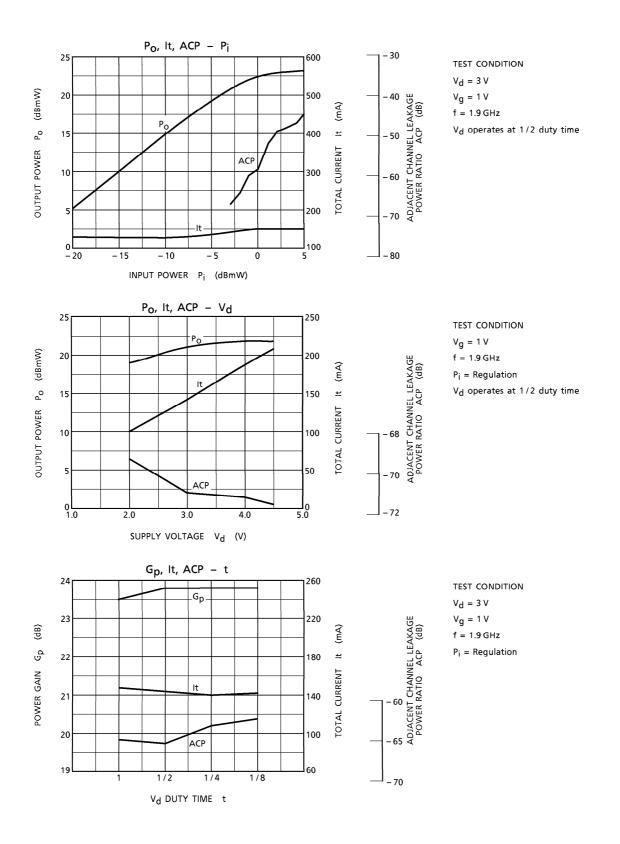
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions. It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

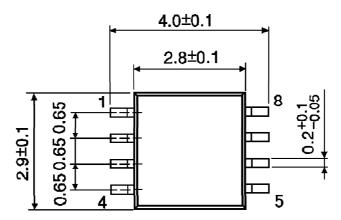
RF TEST BOARD

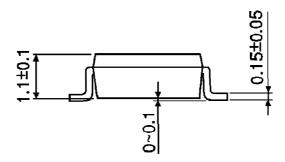




PACKAGE DIMENSIONS

SSOP8-P-0.65 Unit: mm





Weight: 0.02 g (Typ.)