

## AAT3526/7/8 MicroPower Supervisory Circuit with Manual Reset

## **General Description**

The AAT3526/7/8 PowerManager™ products are members of AATI's Total Power Management™ IC (TPM IC<sup>™</sup>) product family. These microprocessor reset circuits are ideal for monitoring voltage supplies in portable systems, where extended battery life is critical. They provide a low cost, reliable solution by eliminating external components. The AAT3526/7/8 products operate by monitoring the system power supply voltage. When the input voltage drops below a fixed threshold, the device asserts a reset signal for a minimum of 150ms after  $V_{CC}$  has risen back above the fixed threshold. They are guaranteed to operate down to 1.2V and designed to ignore fast line transients appearing on  $V_{CC}$ . The AAT3526/7/8 series is available with three output stage versions: AAT3526 push-pull active low output, AAT3527 push-pull active high output and AAT3528 open drain active low output. The quiescent supply current is extremely low, typically 1µA, making this device ideal for portable battery operated equipment.

The AAT3526/7/8 are available in a 4 pin SOT143 package and are specified over -40° to 85°C operating temperature range.

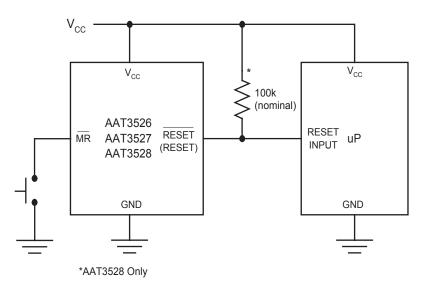
## Features

## **PowerManager**<sup>™</sup>

- 1.2V to 5.5V Input voltage range
- Operation down to 1.2V
- Extremely low quiescent current: less than 2µA
- High accuracy detection threshold: ±1.5%
- Monitor Power Supply Voltages
- Fixed thresholds from 2.2V to 4.6V
- Minimum 150ms reset pulse width with fast delay time <20µs</li>
- Debounced Manual Reset Input
- Available output configurations:
  - Open-drain output
  - CMOS active high output
  - CMOS active low output
- Temp range -40 to 85°C
- 4 pin SOT143 package
- 4kV ESD rating

## **Applications**

- Notebook Computers
- Cell Phones
- Portable Electronics
- Embedded Systems
- Intelligent Instruments



## **Typical Application**



## **Pin Descriptions**

## AAT3526, 3527, 3528 (SOT143)

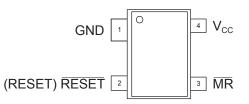
Pin #	Symbol	Function				
1	GND	Ground connection				
2	RESET (AAT3527)	RESET output remains high while $V_{CC}$ is below the reset threshold and remains so for a minimum of 150ms after $V_{CC}$ raises above the reset threshold.				
	RESET (AAT3526, AAT3528)	$\overline{\text{RESET}}$ output remains low while $V_{\text{CC}}$ is below the reset threshold and remains so for a minimum of 150ms after $V_{\text{CC}}$ raises above the reset threshold				
3	MR	Manual Reset active low input. A logic low signal on $\overline{\text{MR}}$ asserts a reset condition. Asserted reset continues as long as $\overline{\text{MR}}$ is low and for a minimum of 150ms after $\overline{\text{MR}}$ returns high				
4	Vcc	Supply Voltage (+1.2V to +5.5V). Due to extremely low operating current it is recommended to place a 10nF capacitor between $\rm V_{\rm CC}$ and GND.				

## **Part Number Descriptions**

Part Number	Output Type
AAT3526	Reset Output Push Pull Active Low with Delay
AAT3527	Reset Output Push Pull Active High with Delay
AAT3528	Reset Output Open Drain Active Low with Delay

# **Pin Configuration**







# Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Description	Value	Units	
V <sub>CC</sub>	V <sub>CC</sub> to GND	-0.3 to 5.5	V	
V <sub>MR</sub>	MR to GND	-0.3 to V <sub>CC</sub> +0.3	V	
V <sub>RESET</sub>	RESET to GND (Push-Pull Output)	-0.3 to V <sub>CC</sub> +0.3		
* RESET	RESET to GND (Open Drain Output)	-0.3 to 5.5	V	
I <sub>VCC</sub> , I <sub>MR</sub>	Maximum Continuous Input Current	20	mA	
I <sub>RESET</sub>	RESET/RESET Current	20	mA	
dVcc/dt	Rate of rise of $V_{CC}$	100	V/µs	
TJ	Operating Junction Temperature Range	-40 to 150	°C	
T <sub>LEAD</sub>	Maximum Soldering Temperature (at Leads) for 10s	300	°C	

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

# **Thermal Information**

Symbol	Description	Value	Units
$\Theta_{JA}$	Maximum Thermal Resistance (SOT143) <sup>1</sup>	200	°C/W
P <sub>D</sub>	Maximum Power Dissipation (SOT143)1	320	mW

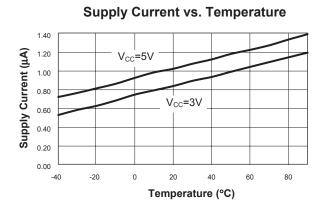
Note 1: Mounted on an FR4 board.

ANALOGIC TECH MicroPower Supervisory Circuit with Manual Reset

Symbol	Description	Conditions	Min	Тур	Max	Units		
N/	Operation Voltage	$T_A = 0^{\circ}C$ to +70°C	1.0		5.5	v		
V <sub>CC</sub>	Operation voltage	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	1.2		5.5			
1	Quiescont Current	V <sub>CC</sub> = 5.5V		1.05	3			
Ι <sub>Q</sub>	Quiescent Current	$V_{CC} = 3V$		0.85	2	μΑ		
V <sub>TH</sub>	RESET Threshold (Table 1)	T <sub>A</sub> = 25°C	V <sub>TH</sub> -1.5%	$V_{TH}$	V <sub>TH</sub> +1.5%	V		
V TH	RESET THESHOLD (TAble T)	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	V <sub>TH</sub> -2.5%	$V_{TH}$	V <sub>TH</sub> +2.5%	V		
$\Delta V_{TH}/^{\circ}C$	RESET Threshold Tempco			40		ppm/°C		
t <sub>P</sub>	RESET Propagation Delay	$V_{CC} = V_{TH}$ to ( $V_{TH}$ -100mV)		15		μs		
t <sub>RDY</sub>	<b>RESET</b> Active Timeout Period		150	250	400	ms		
t <sub>MR</sub>	MR Minimum Pulse Width		10			μs		
MRGI	MR Glitch Immunity			100		ns		
t <sub>MD</sub>	MR to Reset Propagation Delay			0.5		μs		
V	MR Input Threshold	$V_{CC} = V_{TH(MAX)}, V_{TH} \ge 4.38V$	2.3			V		
V <sub>IH</sub>		$V_{CC} = V_{TH(MAX)}, V_{TH} < 4.38V$	$0.7 \times V_{CC}$					
N/	MR Input Threshold	$V_{CC} = V_{TH(MAX)}, V_{TH} \ge 4.38V$			0.8	V		
V <sub>IL</sub>		$V_{CC} = V_{TH(MAX)}, V_{TH} < 4.38V$			$0.25 \times V_{CC}$			
R <sub>MR</sub>	MR Pull-Up Resistance		30	65	90	kΩ		
	RESET Low Output Voltage AAT3526, AAT3528	$I_{SINK}$ =1.2mA, $V_{CC}$ = $V_{TH(min)}$ ,			0.3	- V		
V <sub>OL</sub>		$V_{TH} \leq 3.08V$ , RESET asserted						
V OL		$I_{SINK}$ =3.2mA, $V_{CC}$ = $V_{TH(min)}$ ,			0.4			
		V <sub>TH</sub> > 3.08V, RESET asserted						
	RESET High Output Voltage AAT3526	$I_{SOURCE}$ = 800 µA, $V_{TH}$ > 3.08V,	V <sub>CC</sub> -1.5					
V <sub>OH</sub>		V <sub>CC</sub> > V <sub>TH (max)</sub>						
V OH		$I_{SOURCE}$ = 500 $\mu A, \ V_{TH} \leq 3.08 \text{V},$	0.8 V <sub>CC</sub>			V		
		$V_{CC} > V_{TH (max)}$						
	RESET Low Output Voltage AAT3527	$I_{SINK}$ =1.2mA, $V_{CC} > V_{TH (max)}$			0.3			
V <sub>OL</sub>		$V_{TH} \le 3.08V$ , RESET not asserted				- V		
OL		$I_{SINK} = 3.2 \text{mA}, V_{CC} > V_{TH (max)}$			0.4			
		$V_{TH} > 3.08V$ , RESET not asserted	0.01/					
V <sub>OH</sub>	RESET High Output Voltage AAT3527	I <sub>SOURCE</sub> = 500μA, V <sub>CC</sub> > 2.1V, RESET asserted	0.8V <sub>CC</sub>			- V		
		$I_{SOURCE}$ = 50µA, $V_{CC}$ > 1.2V, RESET asserted	0.8V <sub>CC</sub>					
IDOFF	RESET leakage current, AAT3528	$V_{\rm CC} > V_{\rm TH}$			1	μA		



# **Typical Characteristics**



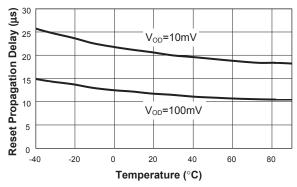
vs. Temperature 1.003 Normalized Threshold 1.002 1.001 1.000 0.999 0.998 0.997 -40 -20 0 20 40 60 80 Temperature (°C)

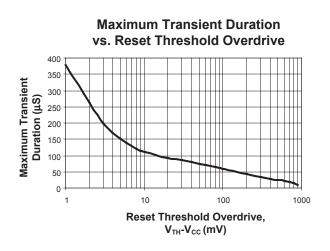
**Normalized Reset Threshold** 

Power-Up Reset Timeout vs. Temperature 320 300 Power-Up Reset Timeout (ms) 280 260 240 220 200 180 160 -40 -20 0 20 40 60 80 100

Temperature (°C)

Power-Down Reset Propagation Delay vs. Temperature

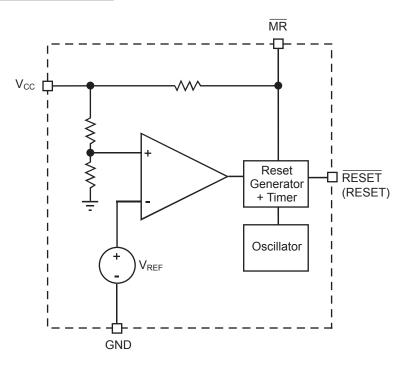




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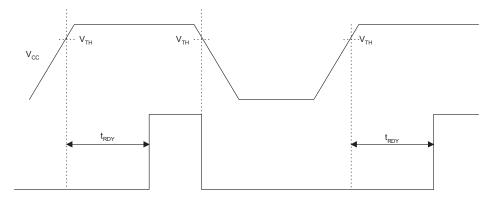
## **Functional Block Diagram**



## **Functional Description**

#### General

During start-up, the reset pin on a microprocessor ensures that it is fully reset and starts up in a known condition. The AAT3526/7/8 series of microprocessor reset devices monitor the supply voltage to a microprocessor and assert a reset signal whenever the V<sub>CC</sub> voltage falls below a factory programmed threshold. This threshold is accurate within +/-1.5% at 25°C and within +/- 2.5% over the entire operating temperature range. The reset signal remains asserted for a minimum of 150ms after V<sub>CC</sub> has risen above the threshold as shown in Figure 1.



#### Figure 1: Reset Timing Diagram



# **Applications Information**

### **Reset Output Options**

The AAT3528 has an active low open drain output and the AAT3526/7 have a push-pull output. The AAT3528 may be used in multiple voltage systems, with an external pull-up resistor. This allows the AAT3528 to interface to microprocessors with higher supply voltages up to 5.5V (Figure 2). In the event of a power down or brown-out condition the reset signal remains valid until the  $V_{CC}$  drops below 1.2V. To ensure validity down to ground an external 100k $\Omega$  resistor should be connected between the RESET output and GND.

### **Manual Reset Input**

A logic low signal on  $\overline{\text{MR}}$  asserts a reset condition. Reset continues to be asserted as long as  $\overline{\text{MR}}$  is low and for a minimum of 150ms after  $\overline{\text{MR}}$  returns high. This input is internally pulled up to  $V_{CC}$  via a 20k $\Omega$  resistor, so leaving the pin unconnected is acceptable if manual reset function is not needed. The  $\overline{\text{MR}}$ 

input is internally debounced which allows use of a mechanical switch. It should be a normally-open momentary switch connected from  $\overline{MR}$  to GND. Additionally, the  $\overline{MR}$  pin can be driven from TTL, CMOS, or open drain logic outputs.

#### **Supply Voltage Transient Behavior**

In some cases fast negative transients of short duration can appear on the V<sub>CC</sub> power supply. The AAT3526/7/8 series devices provide some immunity to line transients which can generate invalid reset pulses. Figure 1 shows typical behavior to short duration pulses verses RESET comparator overdrive. As shown in the Maximum Transient Duration vs. Reset Threshold Overdrive graph, when the transient voltage becomes larger, the time allowed before asserting a reset becomes shorter. e.g. typically a transient of 100mV below the reset threshold would have to present for more than 50µs to cause a reset. Immunity can be increased by the addition of a small by-pass capacitor of 0.1µF connected as close to the V<sub>CC</sub> pin as possible.

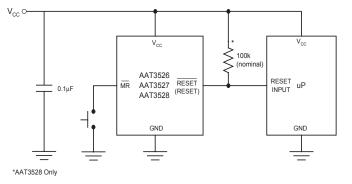


Figure 2: Using Recommended Bypass Capacitor

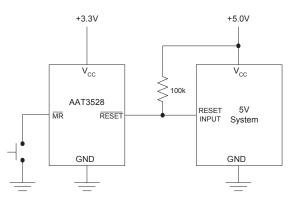


Figure 3: Using AAT3528 Open Drain Output with Multiple Supplies

# Factory Trimmed Reset Thresholds\* and Ordering Information Table 1

		Reset Threshold Voltage, V <sub>TH</sub> (V)				/)
Ordering		T <sub>A</sub> = 25°C			T <sub>A</sub> = 40 to 85°C	
Part Number	<b>Marking</b> <sup>1</sup>	Min	Тур	Max	Min	Max
AAT3526ICX-2.32-200-T1 AAT3527ICX-2.32-200-T1 AAT3528ICX-2.32-200-T1		2.285	2.320	2.355	2.262	2.378
AAT3526ICX-2.63-200-T1 AAT3527ICX-2.63-200-T1 AAT3528ICX-2.63-200-T1	DNXYY DRXYY	2.591	2.630	2.669	2.564	2.696
AAT3526ICX-2.93-200-T1 AAT3527ICX-2.93-200-T1 AAT3528ICX-2.93-200-T1	DOXYY DQXYY FQXYY	2.886	2.930	2.974	2.857	3.003
AAT3528ICX-3.00-200-T1	FIXYY	2.955	3.000	3.045	2.925	3.075
AAT3526ICX-3.08-200-T1 AAT3527ICX-3.08-200-T1 AAT3528ICX-3.08-200-T1	EQXYY FHXYY FJXYY	3.034	3.080	3.126	3.003	3.157
AAT3526ICX-4.38-200-T1 AAT3527ICX-4.38-200-T1 AAT3528ICX-4.38-200-T1	GCXYY HLXYY	4.314	4.380	4.446	4.271	4.490
AAT3526ICX-4.63-200-T1 AAT3527ICX-4.63-200-T1 AAT3528ICX-4.63-200-T1	DPXYY HMXYY	4.561	4.630	4.699	4.514	4.746

Note: Sample stock is generally held on all part numbers listed in BOLD.

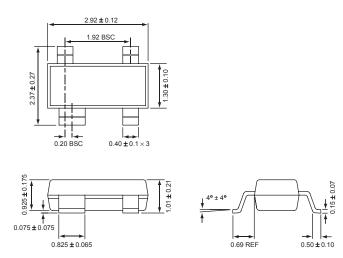
Note 1: XYY = assembly and date code.

\*Contact Local Sales Office for custom trimmed options



# Package Information

SOT143



All dimensions in millimeters.



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Advanced Analogic Technologies, Inc. 830 E. Arques Avenue, Sunnyvale, CA 94085 Phone (408) 737-4600 Fax (408) 737-4611

