AN7513S

0.5-W BTL audio power amplifier

Overview

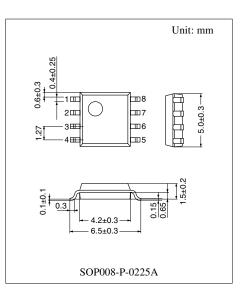
The AN7513S is an audio power amplifier IC with 1-ch output. The BTL (Balanced Transformer-Less) method can provide fewer external parts and more easy design for applications.

Features

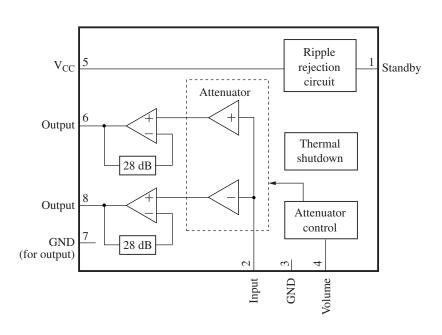
- \bullet 0.5-W output (16 $\Omega)$ with supply voltage of 5 V
- On-chip standby function
- On-chip volume function

Applications

• Televisions, radios, and personal computers



Block Diagram



Pin Descriptions

Pin No.	Description
1	Standby (standby state if this pin is open.)
2	Input
3	Ground (for input)
4	Volume (max. volume if this pin is open.)
5	Supply voltage
6	+ Output
7	Ground (for output ch.1)
8	– Output

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage *2	V _{CC}	14	V
Supply current	I _{CC}	1.0	А
Power dissipation *3	P _D	541	mW
Operating ambient temperature *1	T _{opr}	-25 to +70	°C
Storage temperature *1	T _{stg}	-55 to +150	°C

Note) *1: Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25^{\circ}C$.

*2: At no signal

*3: The power dissipation shown is the value for $T_a = 70^{\circ}C$.

Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	3.0 to 12.0	V

■ Electrical Characteristics at V_{CC} = 5.0 V, R_L = 8 Ω, f = 1 kHz, T_a = 25°C ± 2°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent circuit current	I _{CQ}	$V_{IN} = 0 \text{ mV}, \text{ Vol.} = 0 \text{ V}$	_	20	60	mA
Standby current	I _{STB}	$V_{IN} = 0 \text{ mV}, \text{ Vol.} = 0 \text{ V}$	_	1	10	μΑ
Output noise voltage *	V _{NO}	$R_g = 10 \text{ k}\Omega$, Vol. = 0 V	_	0.10	0.4	mV[rms]
Voltage gain	G _V	$P_0 = 0.25 \text{ W}, \text{ Vol.} = 2.5 \text{ V}$	31	33	35	dB
Total harmonic distortion	THD	$P_0 = 0.25 \text{ W}, \text{ Vol.} = 2.5 \text{ V}$	_	0.10	0.5	%
Maximum output power	P _{O1}	THD = 10%, Vol. = 2.5 V	0.35	0.5		W
Ripple rejection ratio *	RR	$\begin{aligned} R_g &= 10 \text{ k}\Omega, \text{ Vol.} = 0 \text{ V}, \\ V_R &= 1 \text{ V}[\text{rms}], f_R &= 120 \text{ Hz} \end{aligned}$	30	50	_	dB
Output offset voltage	V _{OFF}	$R_g = 10 \text{ k}\Omega$, Vol. = 0 V	-250	0	250	mV
Volume attenuation rate *	Att	$P_0 = 0.25 \text{ W}, \text{ Vol.} = 0 \text{ V}$	70	85		dB
Intermediate voltage gain	G _{VM}	P ₀ = 0.25 W, Vol. = 1.25 V	20.5	23.5	26.5	dB

Note) *: In measuring, the filter for the range of 15 Hz to 30 kHz (12 dB/OCT) is used.

Terminal Equivalent Circuits

Pin No.	Pin name	Equivalent circuit	Voltage
1	Standby pin	V_{CC} C_{CC} C_{CC} C_{CC} V_{RF} $(\approx V_{CC})$ V_{RF} V_{CC} V_{CC} V_{CC} V_{RF} V_{CC}	5 V
2	Input pin	$V_{CC} \circ \underbrace{\nabla_{REF1} (1.4 V)}_{Q_{C}} \xrightarrow{\nabla_{REF1} (1.4 V)}$	1.4 V
3	GND	3	0 V
4	Volume pin	$V_{CC} \sim \qquad $	

Terminal Equivalent Circuits (continued)

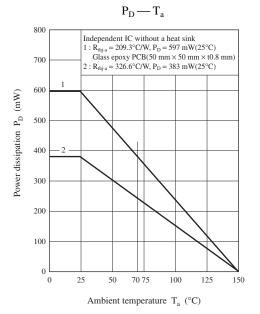
Pin No.	Pin name	Equivalent circuit	Voltage
5	V _{CC}	_	5.0 V
6	+ Output pin	$1/2 V_{CC} 0 \Omega 200 \Omega 50 \Omega 6$	2.15 V
7	GND	(7)	0 V
8	– Output pin	$1/2 V_{CC} \longrightarrow 0 V_{CC}$	2.15 V

Usage Notes

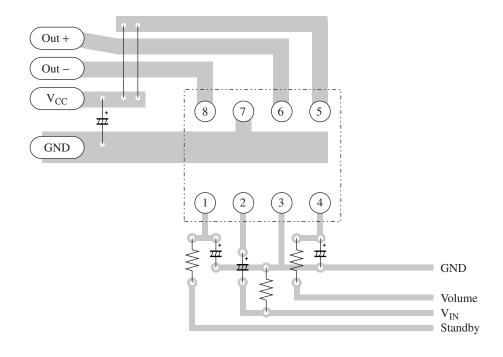
- \bullet Please avoid the short circuit to V_{CC} , ground, or load short circuit.
- Please connect the cooling fin with the GND potential.
- The thermal shutdown circuit operates at about $T_j = 150^{\circ}$ C. However, the thermal shutdown circuit is reset automatically if the temperature drops.
- \bullet Please carefully design the heat radiation especially when you take out high power at high V_{CC} .
- Please connect only the ground of signal with the signal GND of the amplifier in the previous stage.
- Use a speaker with 16 Ω or more impedance.

Technical Data

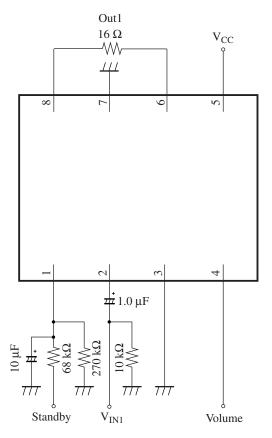
• PD — Ta curves of SOP008-P-0225A



2. Example of PCB pattern



■ Application Circuit Example



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