

Photon Coupled Isolator H11A520-H11A550 -H11A5100

Ga As Infrared Emitting Diode & NPN Silicon Photo-Transistor

The GE Solid State H11A520, H11A550 and H11A5100 consist of a gallium arsenide, infrared emitting diode coupled with a silicon photo-transistor in a dual in-line package. These devices are also available in surface-mount packaging.

FEATURES:

- High isolation voltage, 5000V minimum.
- GE unique patented glass isolation construction.
- High efficiency liquid epitaxial IRED.
- High humidity resistant silicone encapsulation.
- Fast switching speeds.

Covered under U.L. component recognition program, reference file E51868

absolute maximum ratings: (25°C) (unless otherwise specified)

INFRARED EMITTING DIODE			
Power Dissipation - T _A = 25°C	*100	milliwatts	
Forward Current (Continuous)	60	milliamps	
Forward Current (Peak) (Pulse width 1 μsec, 300 pps)	3	amperes	
Reverse Voltage	6	volts	
*Derate 1.33mW/°C above 25°C.			

PHOTO-TRANSISTOR			
Power Dissipation - T _A = 25°C	**300	milliwatts	
V _{CEO}	30	volts	
V _{CBO}	70	volts	
V _{EBO}	7	volts	
Collector Current (Continuous)	100	milliamps	
**Derate 4.0mW/°C above 25°C.			

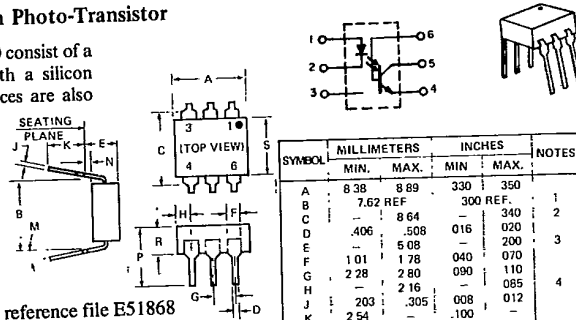
TOTAL DEVICE			
Storage Temperature -55 to 150°C.			
Operating Temperature -55 to 100°C.			
Lead Soldering Time (at 260°C) 10 seconds.			
Surge Isolation Voltage (Input to Output).			
5656V _(peak)		4000V _(RMS)	
Steady-State Isolation Voltage (Input to Output).			
5300V _(DC)		3750V _(RMS)	

individual electrical characteristics (25°C) (unless otherwise specified)

INFRARED EMITTING DIODE	MIN.	MAX.	UNITS
Forward Voltage - V _F (I _F = 10mA)	.8	1.5	volts
Forward Voltage - V _F (I _F = 10mA) T _A = -55°C	.9	1.7	volts
Forward Voltage - V _F (I _F = 10mA) T _A = +100°C	.7	1.4	volts
Reverse Current - I _R (V _R = 6V)	-	10	microamps
Capacitance - C _J (V = 0, f = 1 MHz)	-	100	picofarads

PHOTO-TRANSISTOR	MIN.	TYP.	MAX.	UNITS
Breakdown Voltage - V _{(BR)CEO} (I _C = 10mA, I _F = 0)	30	-	-	volts
Breakdown Voltage - V _{(BR)CBO} (I _C = 100μA, I _F = 0)	70	-	-	volts
Breakdown Voltage - V _{(BR)EBO} (I _E = 100μA, I _F = 0)	7	-	-	volts
Collector Dark Current - I _{CEO} (V _{CE} = 10V, I _F = 0)	-	5	50	nano-amps
Collector Dark Current - I _{CEO} (V _{CE} = 10V, I _F = 0) T _A = 100°C	-	-	500	micro-amps
Capacitance - C _{CE} (V _{CE} = 10V, f = 1MHz)	-	2	-	pico-farads

VDE Approved to 0883/6.80 0110b Certificate # 35025



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	8.38	8.89	.330	.350	
B	7.62	REF.	.300	REF.	1
C	-	8.64	-	.340	2
D	.406	.508	.016	.020	
E	-	6.08	-	.200	3
F	1.01	1.78	.040	.070	
G	2.28	2.80	.090	.110	
H	-	2.16	-	.085	4
J	2.03	.305	.008	.012	
K	-	.15	-	.015	
M	3.81	-	.15	-	
N	-	9.63	-	.375	
P	2.92	3.43	.115	.135	
R	6.10	6.86	.240	.270	
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NOTES
 1 INSTALLED POSITION LEAD CENTERS
 2 OVERALL INSTALLED DIMENSION
 3 THESE MEASUREMENTS ARE MADE FROM THE SEATING PLANE
 4. FOUR PLACES

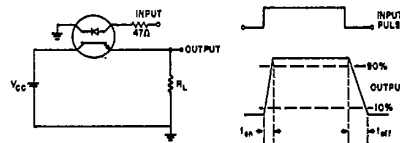
coupled electrical characteristics (25°C) (unless otherwise specified)

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		MIN.	TYP.	MAX.	UNITS
DC Current Transfer Ratio ($I_F = 10\text{mA}$, $V_{CE} = 10\text{V}$)	H11A5100	100	-	-	%
	H11A550	50	-	-	%
	H11A520	20	-	-	%
Saturation Voltage - Collector to Emitter ($I_F = 20\text{mA}$, $I_C = 2\text{mA}$)		-	-	0.4	volts
Isolation Resistance (Input to Output Voltage = $500V_{DC}$. See Note 1)		100	-	-	gigaohms
Input to Output Capacitance (Input to Output Voltage = 0, $f = 1\text{MHz}$. See Note 1)		-	-	2.0	picofarads
Turn-On Time - t_{on} ($V_{CC} = 10\text{V}$, $I_C = 2\text{mA}$, $R_L = 100\Omega$). (See Figure 1)		-	5	10	microseconds
Turn-Off Time - t_{off} ($V_{CC} = 10\text{V}$, $I_C = 2\text{mA}$, $R_L = 100\Omega$). (See Figure 1)		-	5	10	microseconds

NOTE 1:

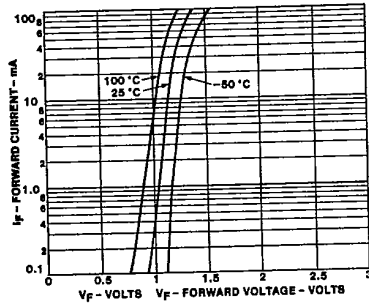
Tests of input to output isolation current resistance, and capacitance are performed with the input terminals (diode) shorted together and the output terminals (transistor) shorted together.



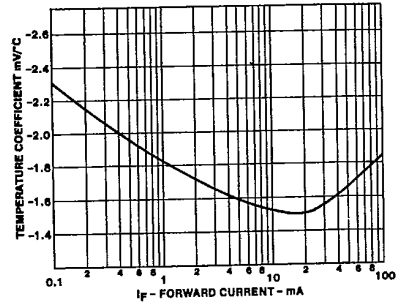
Adjust Amplitude of Input Pulse for Output (I_C) of 2mA

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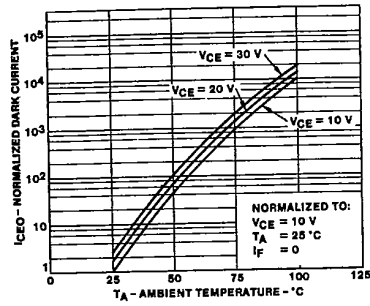
TYPICAL CHARACTERISTICS



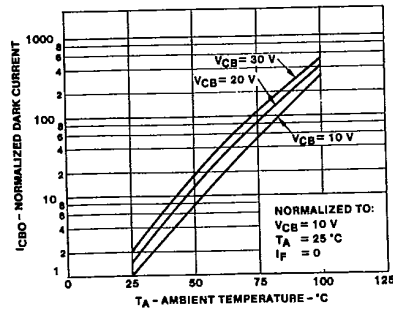
1. INPUT CHARACTERISTICS



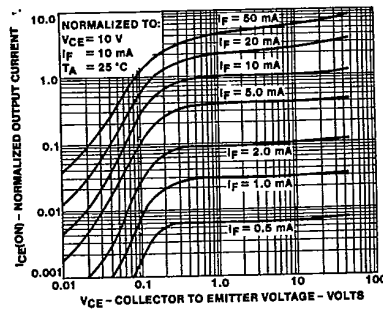
2. OUTPUT CURRENT VS INPUT CURRENT



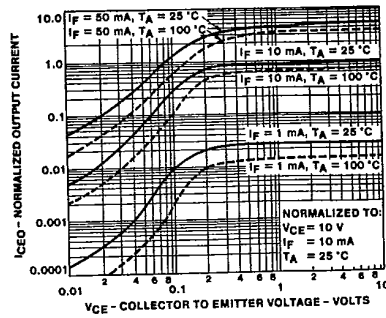
3. DARK I_{CEO} CURRENT VS TEMPERATURE



4. OUTPUT CURRENT VS TEMPERATURE



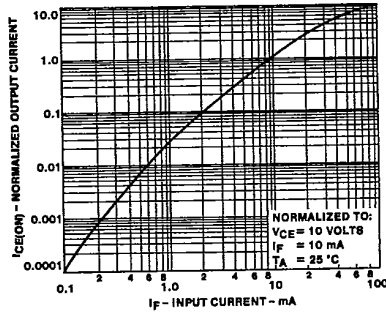
5. OUTPUT CHARACTERISTICS



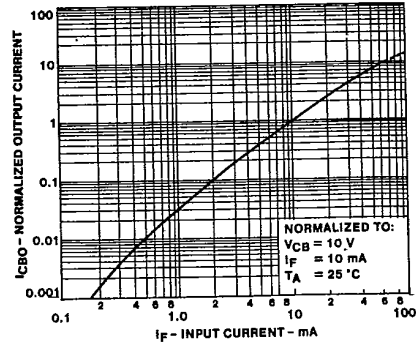
6. OUTPUT CURRENT VS BASE
 EMITTER RESISTANCE

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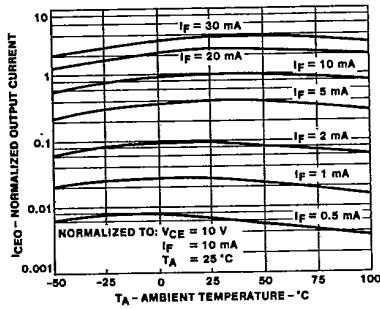
TYPICAL CHARACTERISTICS



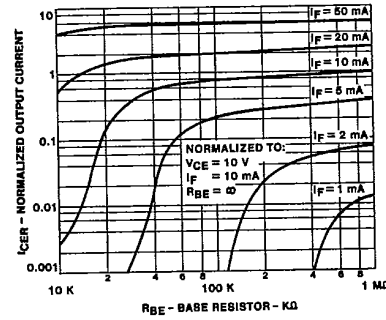
7. OUTPUT CURRENT VS. INPUT CURRENT



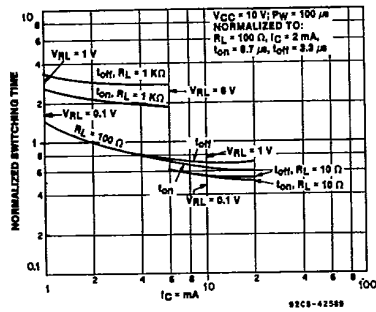
8. OUTPUT CURRENT - COLLECTOR-TO-BASE VS. INPUT CURRENT



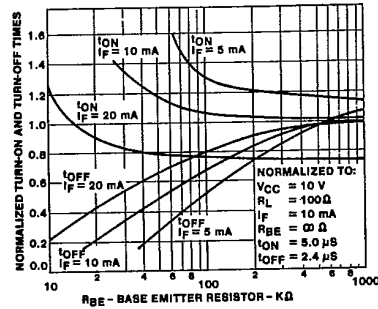
9. OUTPUT CURRENT VS. TEMPERATURE



10. OUTPUT CURRENT VS. BASE EMITTER RESISTANCE



11. SWITCHING SPEED VS. COLLECTOR CURRENT (NOT SATURATED)



12. SWITCHING TIME VS. R_{BE}

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