

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA1873 is a switching device which can be driven directly by a 2.5 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

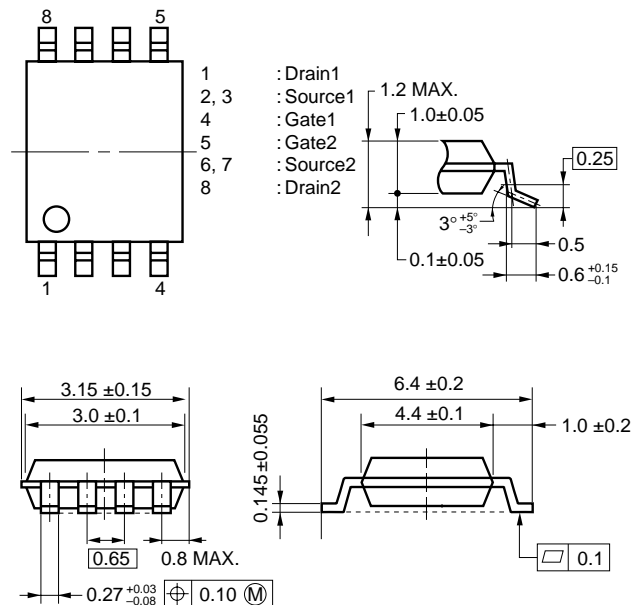
FEATURES

- 2.5 V drive available
- Low on-state resistance
 - $R_{DS(on)1} = 23.0 \text{ m}\Omega \text{ MAX. (} V_{GS} = 4.5 \text{ V, } I_D = 3.0 \text{ A)}$
 - $R_{DS(on)2} = 24.0 \text{ m}\Omega \text{ MAX. (} V_{GS} = 4.0 \text{ V, } I_D = 3.0 \text{ A)}$
 - $R_{DS(on)3} = 28.0 \text{ m}\Omega \text{ MAX. (} V_{GS} = 3.1 \text{ V, } I_D = 3.0 \text{ A)}$
 - $R_{DS(on)4} = 29.0 \text{ m}\Omega \text{ MAX. (} V_{GS} = 2.5 \text{ V, } I_D = 3.0 \text{ A)}$
- Built-in G-S protection diode against ESD

ORDERING INFORMATION

PART NUMBER	PACKAGE
μ PA1873GR-9JG	Power TSSOP8

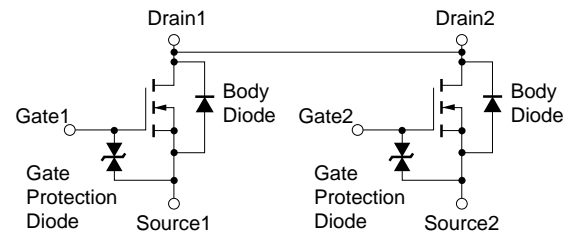
PACKAGE DRAWING (Unit : mm)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Drain to Source Voltage ($V_{GS} = 0 \text{ V}$)	V_{DSS}	20	V
Gate to Source Voltage ($V_{DS} = 0 \text{ V}$)	V_{GSS}	± 12	V
Drain Current (DC) ($T_A = 25^\circ\text{C}$)	$I_{D(DC)}$	± 6.0	A
Drain Current (pulse) ^{Note1}	$I_{D(pulse)}$	± 80	A
Total Power Dissipation (2 unit) ^{Note2}	P_T	2.0	W
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

EQUIVALENT CIRCUIT



- Notes 1.** $PW \leq 10 \mu\text{s}$, Duty Cycle $\leq 1\%$
2. Mounted on ceramic substrate of $5000 \text{ mm}^2 \times 1.1 \text{ mm}$

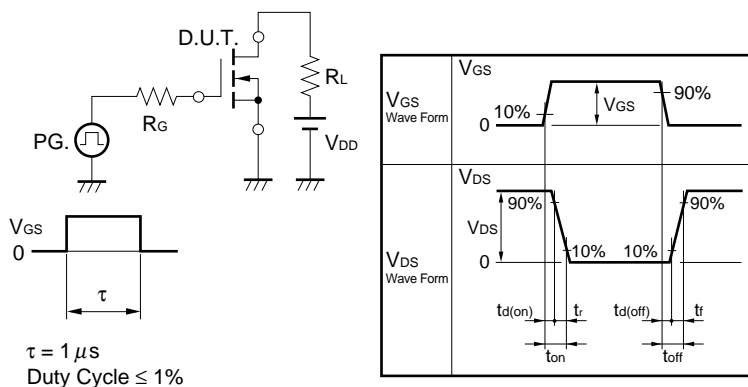
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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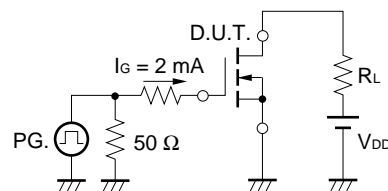
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±12 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1.0 mA	0.5	1.0	1.5	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 3.0 A	5.0			S
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = 4.5 V, I _D = 3.0 A	13.0	18.0	23.0	mΩ
	R _{DS(on)2}	V _{GS} = 4.0 V, I _D = 3.0 A	14.0	19.0	24.0	mΩ
	R _{DS(on)3}	V _{GS} = 3.1 V, I _D = 3.0 A	14.5	21.5	28.0	mΩ
	R _{DS(on)4}	V _{GS} = 2.5 V, I _D = 3.0 A	15.0	24.5	29.0	mΩ
Input Capacitance	C _{iss}	V _{DS} = 10 V		705		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V		205		pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz		145		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 10 V, I _D = 3.0 A		60		ns
Rise Time	t _r	V _{GS} = 4.0 V		310		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		380		ns
Fall Time	t _f			420		ns
Total Gate Charge	Q _G	V _{DD} = 16 V		9.0		nC
Gate to Source Charge	Q _{GS}	V _{GS} = 4.0 V		2.0		nC
Gate to Drain Charge	Q _{GD}	I _D = 6.0 A		4.0		nC
Body Diode Forward Voltage	V _{F(S-D)}	I _F = 6.0 A, V _{GS} = 0 V		0.84		V
Reverse Recovery Time	t _{rr}	I _F = 6.0 A, V _{GS} = 0 V		480		ns
Reverse Recovery Charge	Q _{rr}	di/dt = 50 A / μs		1200		nC

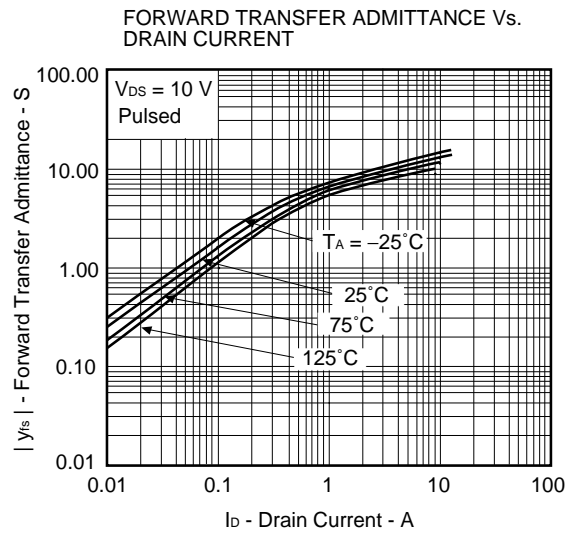
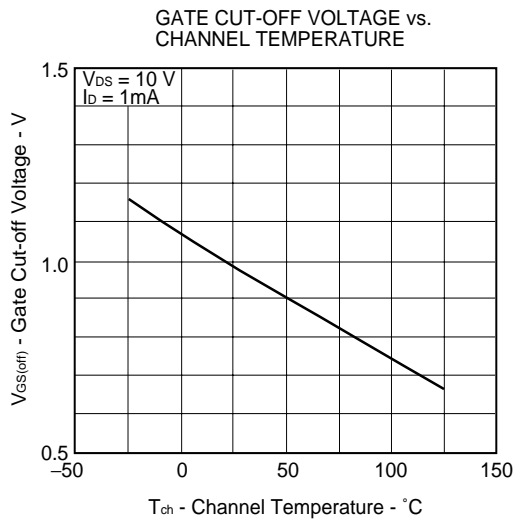
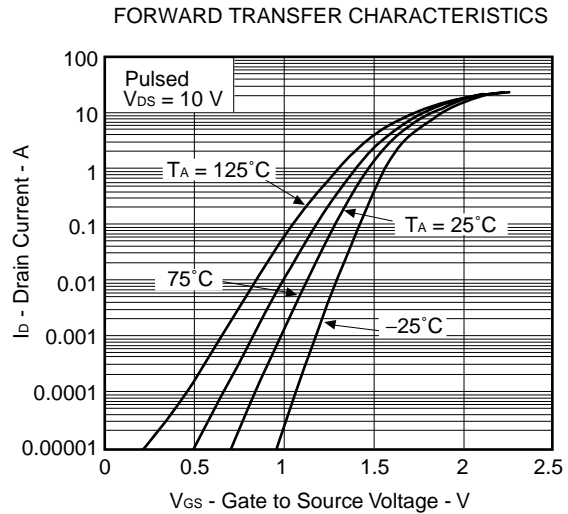
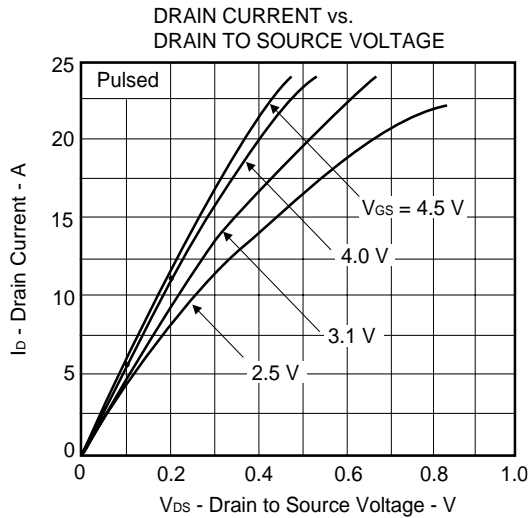
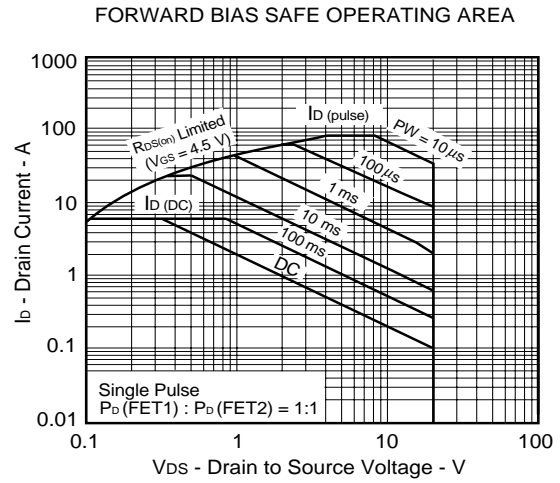
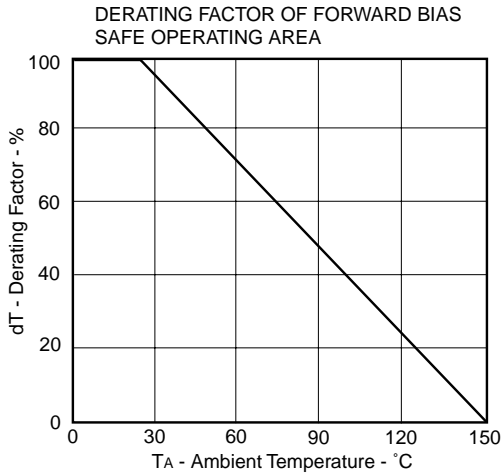
TEST CIRCUIT 1 SWITCHING TIME

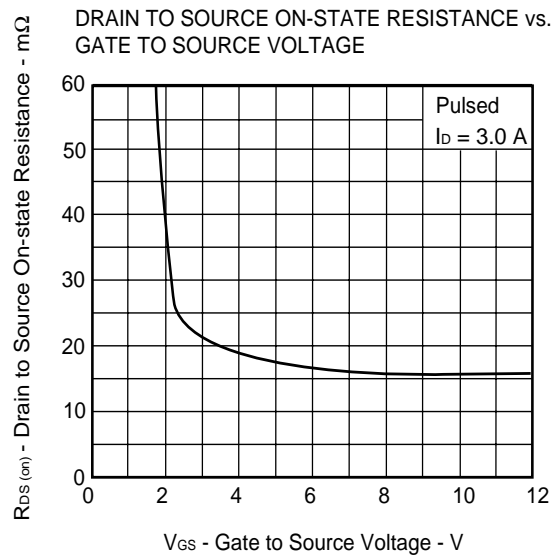
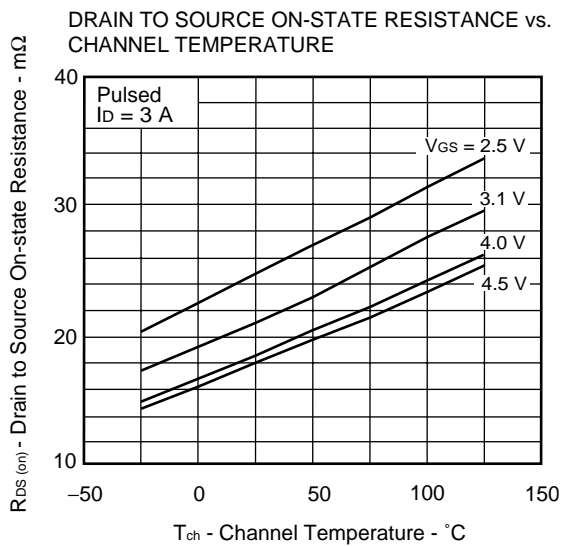
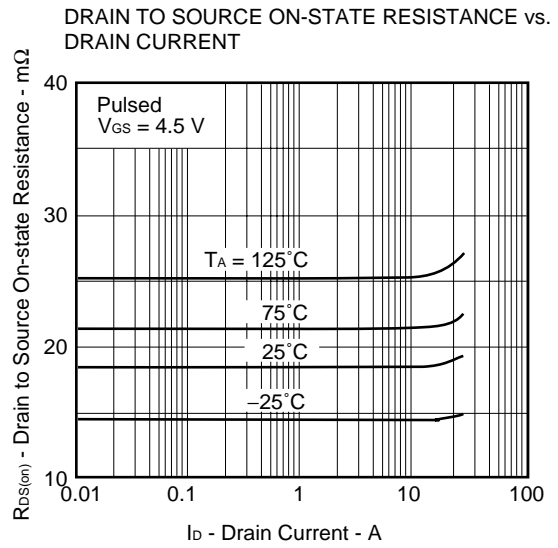
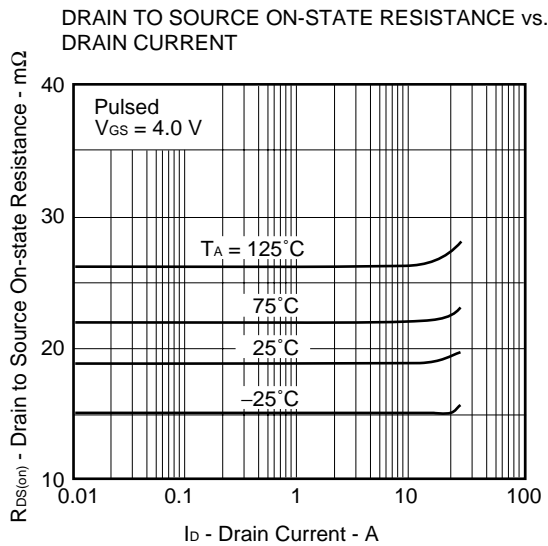
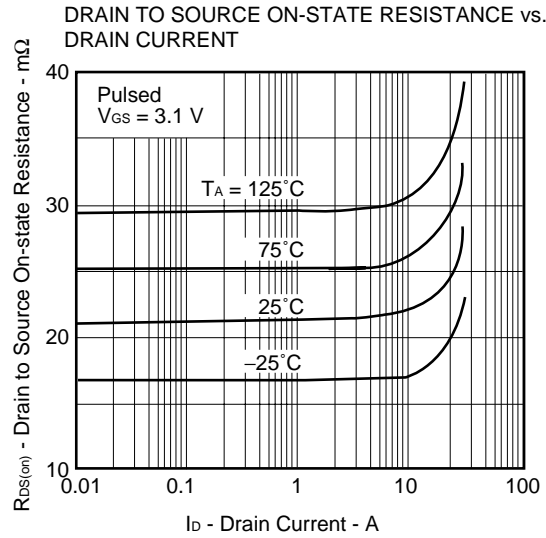
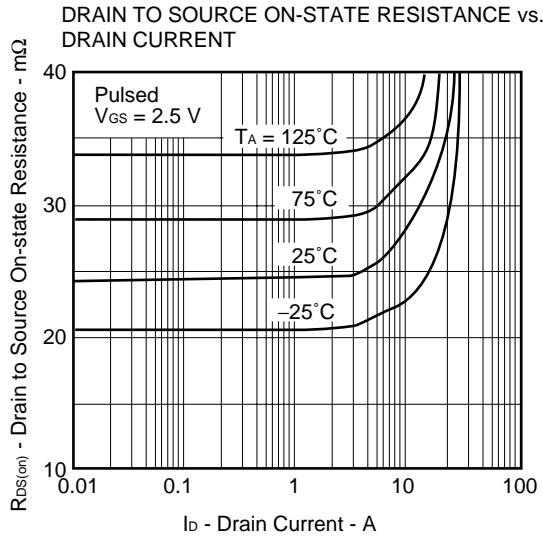


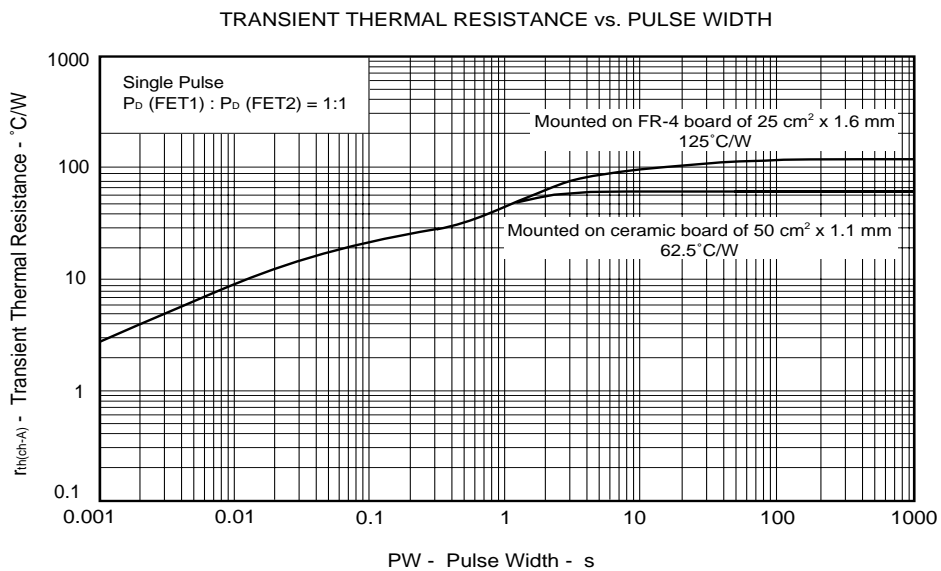
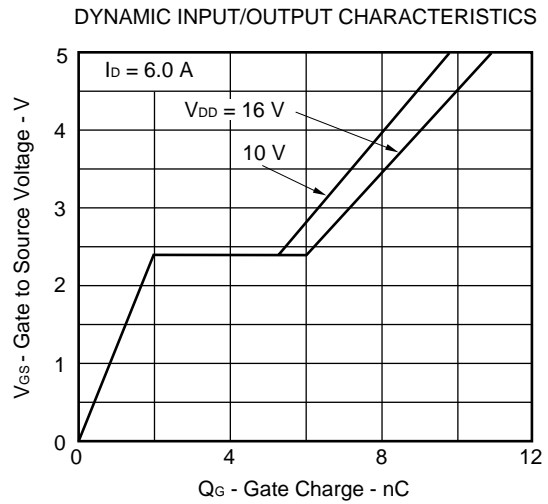
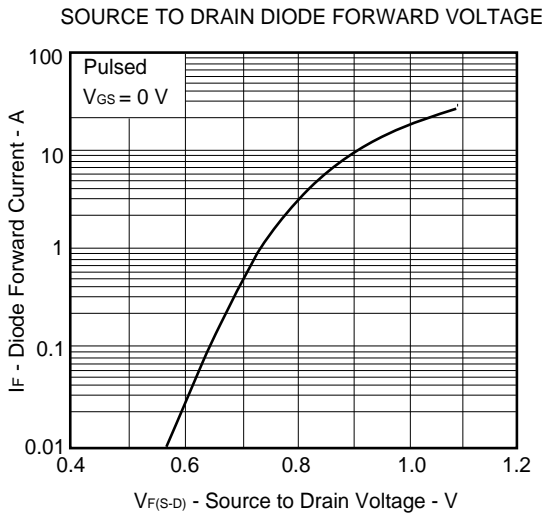
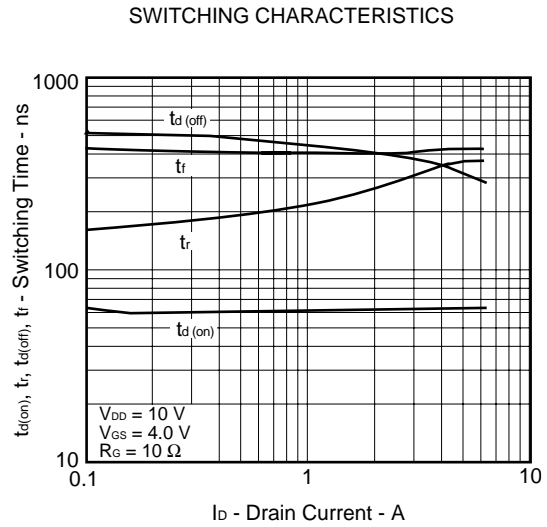
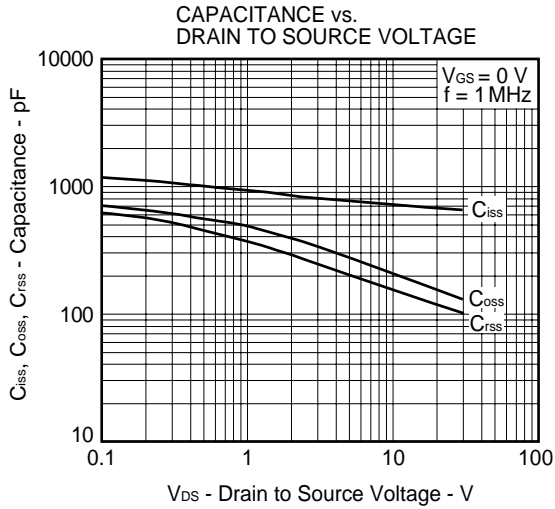
TEST CIRCUIT 2 GATE CHARGE



TYPICAL CHARACTERISTICS (T_A = 25°C)







[MEMO]

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