

# MOS FIELD EFFECT TRANSISTOR

# **2SK1958**

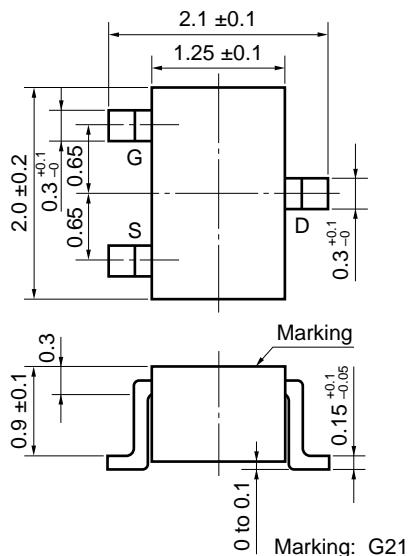
## N-CHANNEL MOS FET FOR HIGH-SPEED SWITCHING

The 2SK1958 is an N-channel vertical MOS FET. Because it can be driven by a voltage as low as 1.5 V and it is not necessary to consider a drive current, this FET is ideal as an actuator for low-current portable systems such as headphone stereos and video cameras.

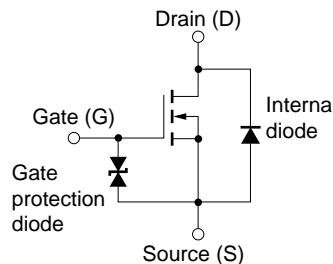
### FEATURES

- Gate can be driven by 1.5 V
- Because of its high input impedance, there's no need to consider drive current
- Since bias resistance can be omitted, the number of components required can be reduced

### PACKAGE DIMENSIONS (in mm)



### EQUIVALENT CIRCUIT



### PIN CONNECTIONS

S: Source

D: Drain

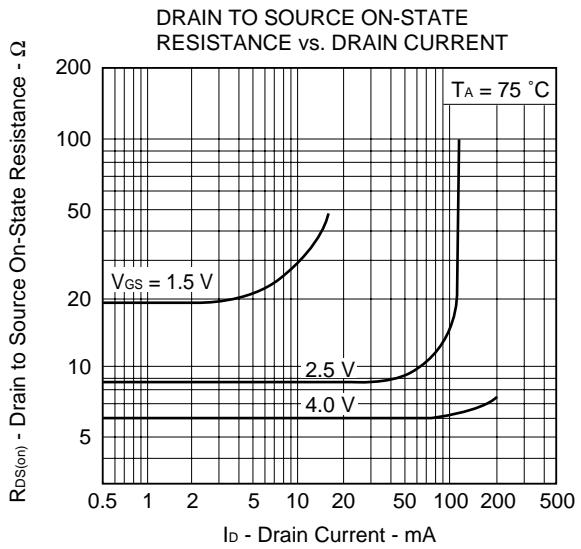
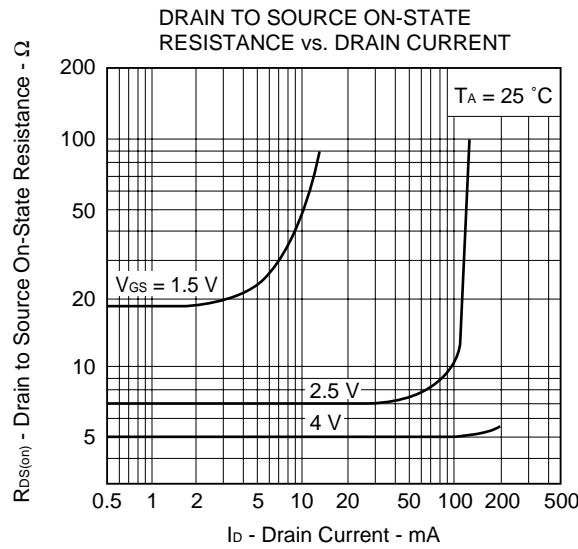
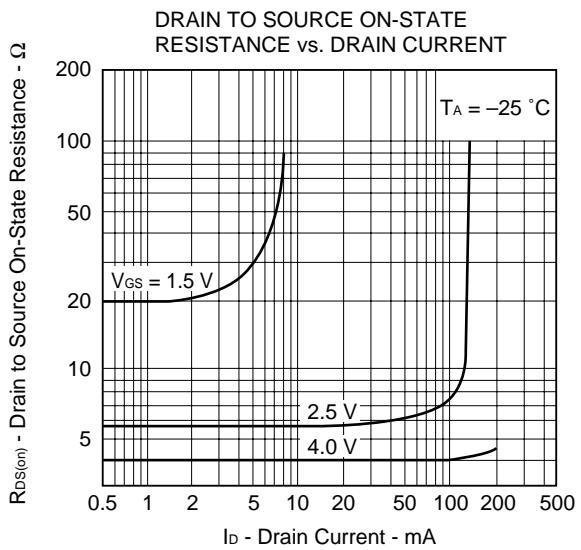
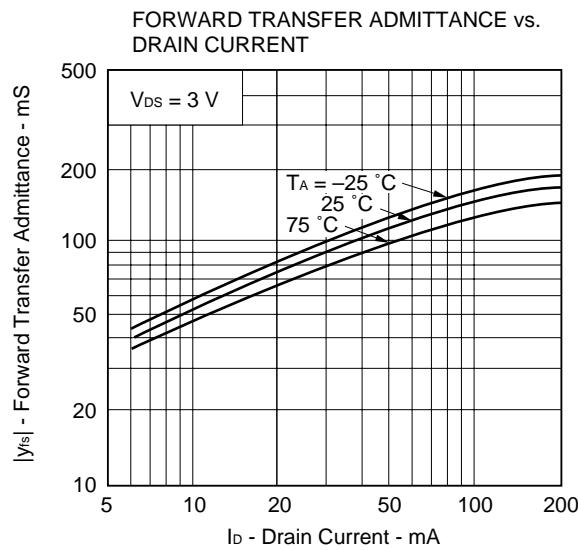
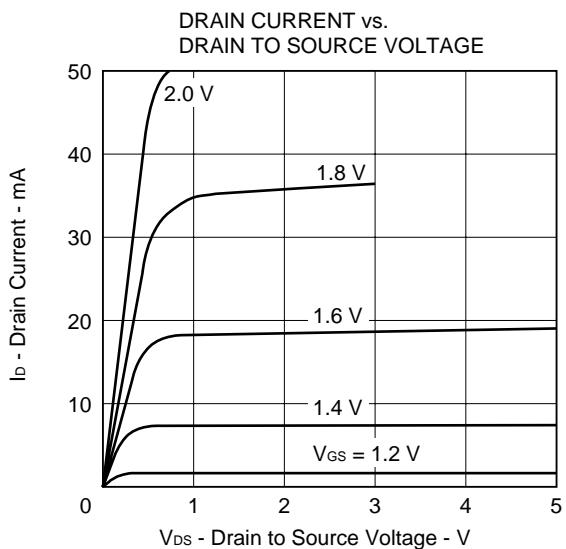
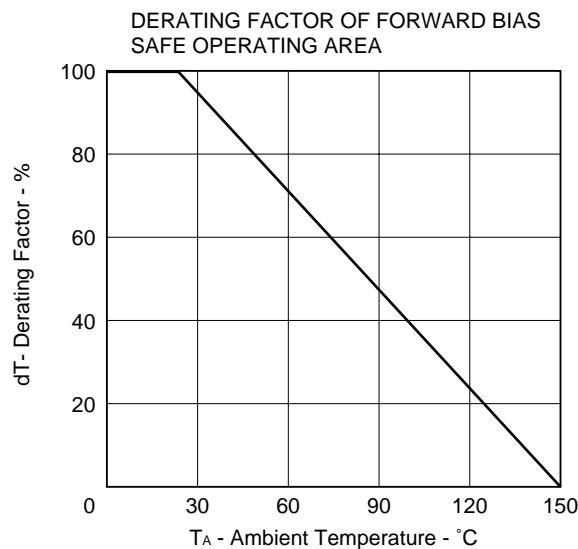
G: Gate

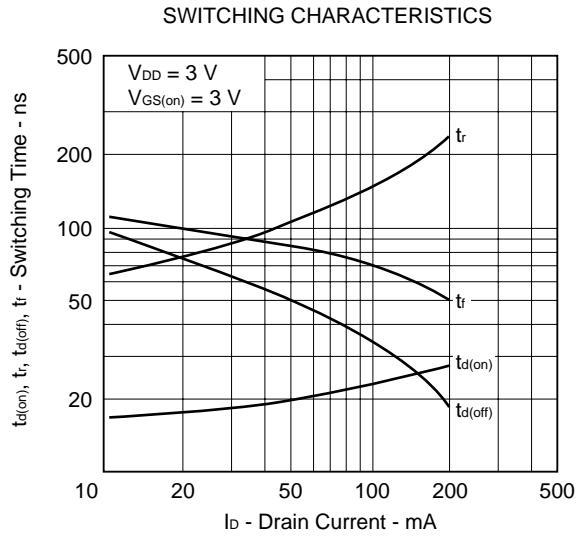
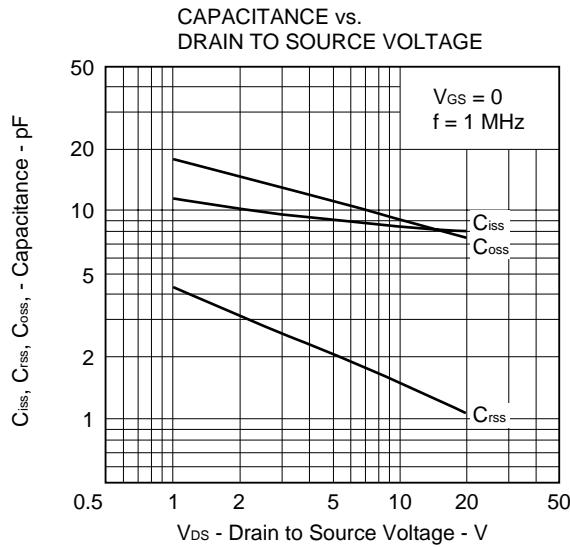
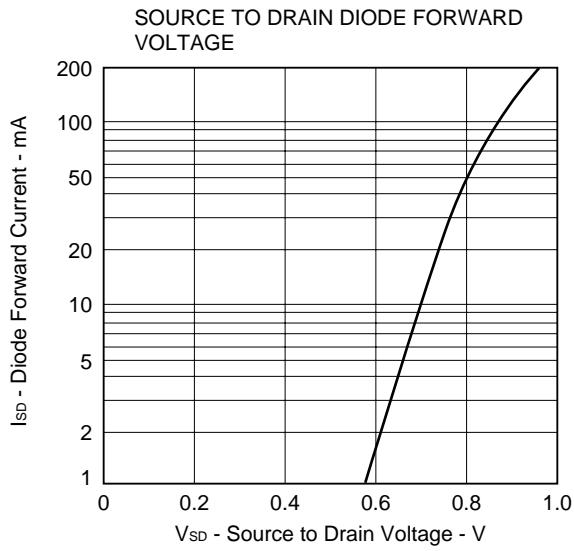
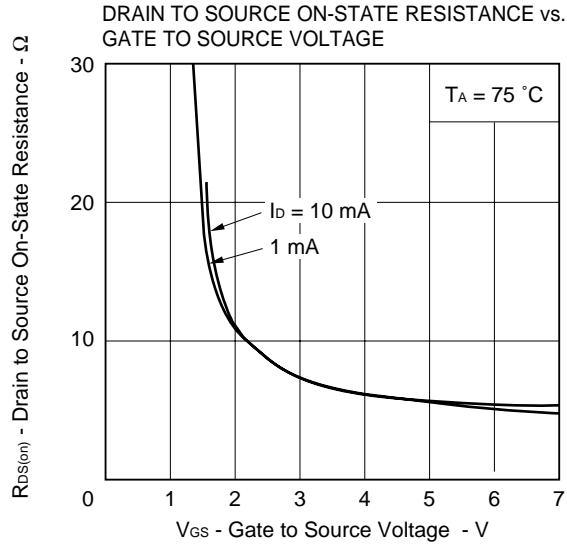
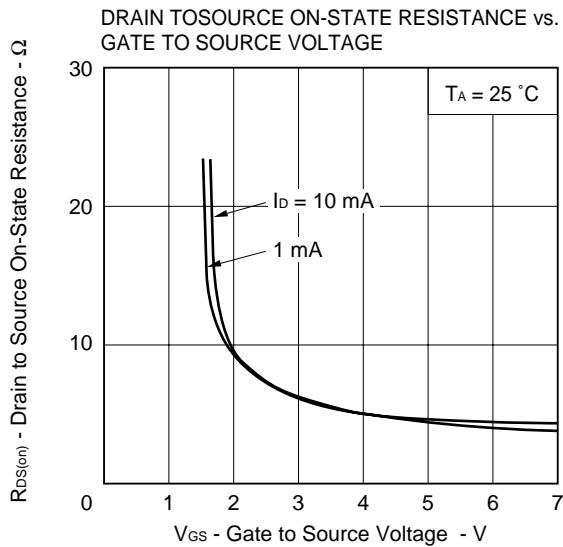
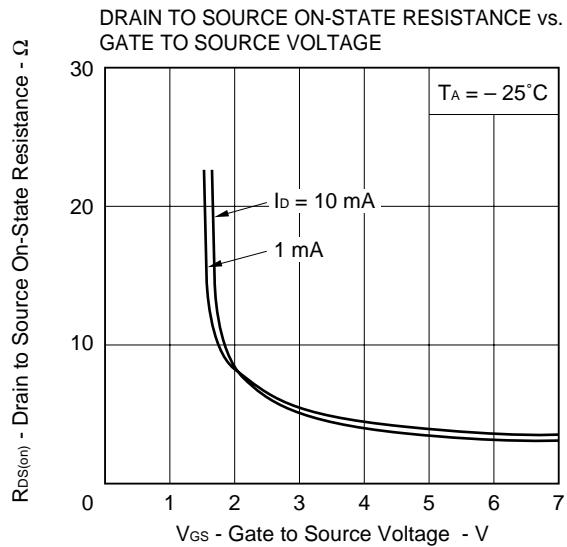
### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

PARAMETER	SYMBOL	TEST CONDITIONS	RATING	UNIT
Drain to Source Voltage	$V_{DSS}$	$V_{GS} = 0$	16	V
Gate to Source Voltage	$V_{GSS}$	$V_{DS} = 0$	$\pm 7.0$	V
Drain Current (DC)	$I_{D(\text{DC})}$		$\pm 0.1$	A
Drain Current (Pulse)	$I_{D(\text{pulse})}$	$PW \leq 10 \text{ ms, duty cycle} \leq 50\%$	$\pm 0.2$	A
Total Power Dissipation	$P_T$		150	mW
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ C$ )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-Off Current	$I_{DSS}$	$V_{DS} = 16 V, V_{GS} = 0$			1.0	$\mu A$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 7.0 V, V_{DS} = 0$			$\pm 3.0$	$\mu A$
Gate Cut-Off Voltage	$V_{GS(off)}$	$V_{DS} = 3 V, I_D = 10 \mu A$	0.5	0.8	1.1	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 3 V, I_D = 10 mA$	20			$mS$
Drain to Source On-State Resistance	$R_{DS(on)1}$	$V_{GS} = 1.5 V, I_D = 1 mA$		20	50	$\Omega$
Drain to Source On-State Resistance	$R_{DS(on)2}$	$V_{GS} = 2.5 V, I_D = 10 mA$		7	15	$\Omega$
Drain to Source On-State Resistance	$R_{DS(on)3}$	$V_{GS} = 4.0 V, I_D = 10 mA$		5	12	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS} = 3 V, V_{GS} = 0, f = 1.0 MHz$		10		$pF$
Output Capacitance	$C_{oss}$			13		$pF$
Reverse Transfer Capacitance	$C_{rss}$			3		$pF$
Turn-ON Delay Time	$t_{d(on)}$	$V_{DD} = 3 V, I_D = 10 mA, V_{GS(on)} = 3 V, R_G = 10 \Omega, R_L = 300 \Omega$		15		ns
Rise Time	$t_r$			70		ns
Turn-OFF Delay Time	$t_{d(off)}$			100		ns
Fall Time	$t_f$			110		ns

TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )



**REFERENCE**

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	IEI-1209
Semiconductor device mounting technology manual	C10535E
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E