

IT8871 APPLICATION CIRCUIT V0.3

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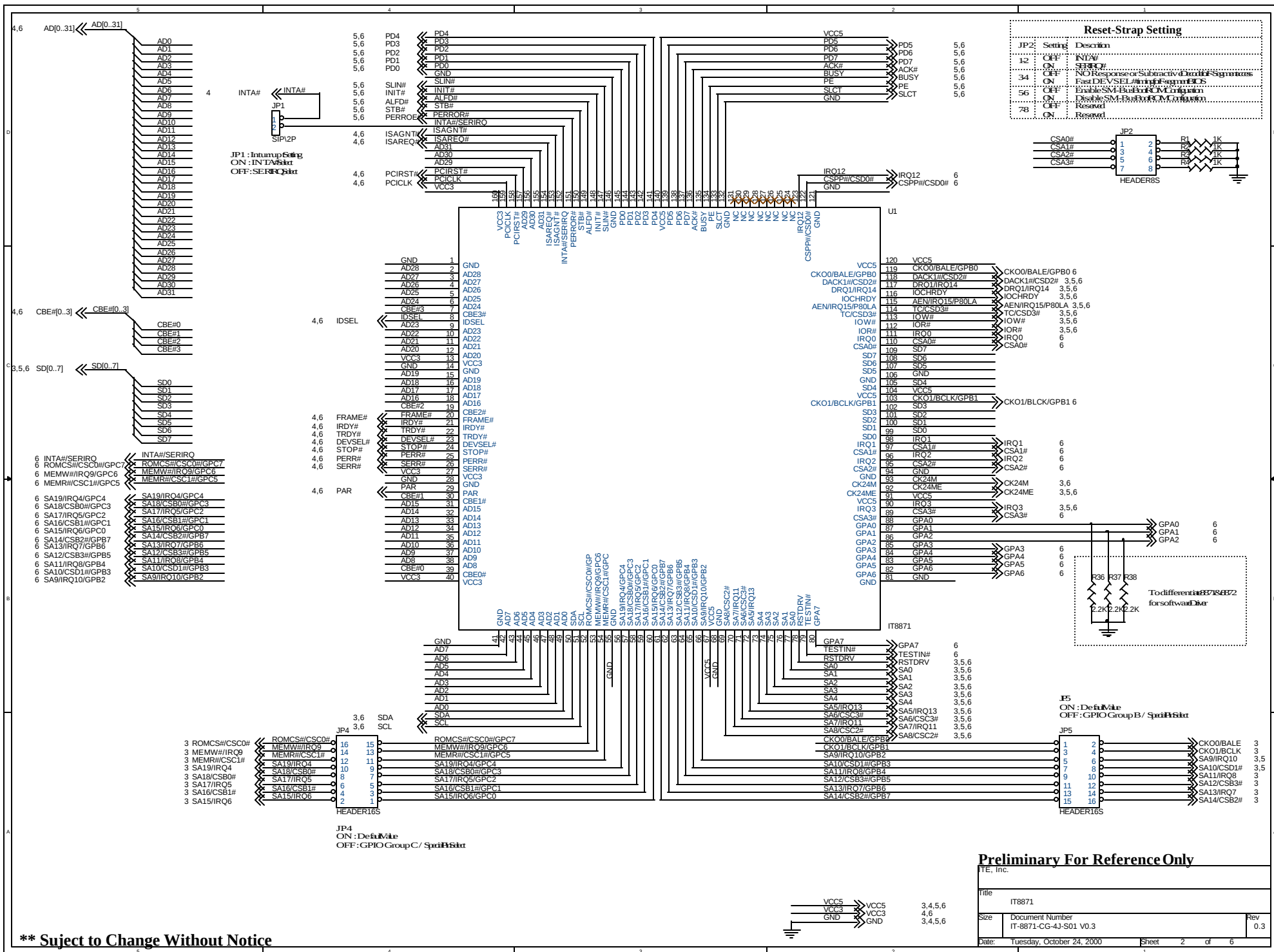
REVISION HISTORY

SCHEMATIC REVISION	PCB REVISION	MODIFICATION ITEMS	DRAWING AND MODIFICATION DESCRIPTION	DATE
IT-8871-CG-4(S01) VER01	DK-8871-4(S01)			Nov.18,1999
IT-8871-CG-4(S01) VER02	DK-8871-4HD01A	1.Add:RN19,R34,R30==>Pull-upRQSignal 2.Add:JP31,JP32 ==> for QbankEDMAAdDMA 3.Add:RJP5 ==>for MemoryWaitst 4.Add:Y3,R33,C71,C72,R20==>forMHCystal 5.Remove:U4 ==>ChangEBUtoCystal 6.Remove:R22 ==>Pull-upIRQtoISA 7.Swapping J1B11,B12==>SwappingMEMM#MEMR#	1.SwapISA SignalBU(MEMR#)==BU(MEMW#)/B12(MEMW#)==B12(MEMR#) 2.Option OSC or Crystal Clock Input 3.Add Jump of MemoryWrite 4.Add Jump of EDMA 5.Modify IRQx Pull-up Resistor Value	July 27,2000
IT-8871-CG-4(S01) VER03	DK-8871-4HD01A	1.Add:R36,R37,R38==>Pull-DownGPIOSignal	1.Pull-Down GPIO Signals for Differentiating 8871/8872 Invertives	Oct.24,2000

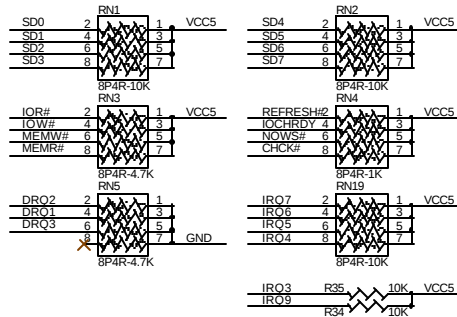
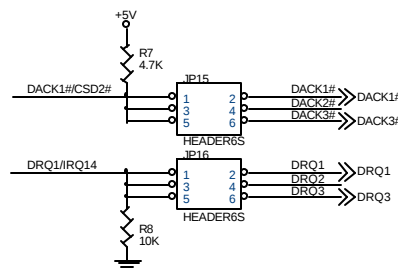
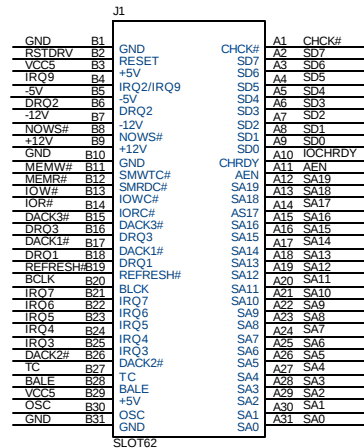
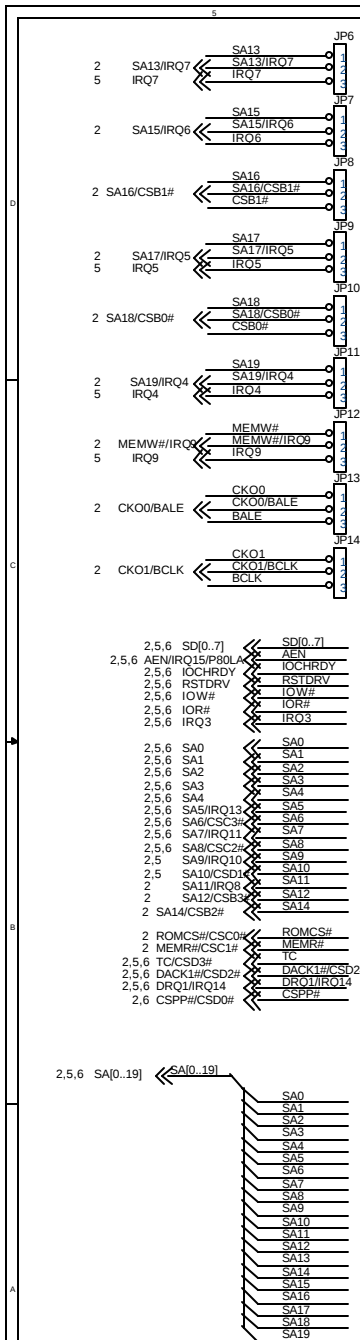
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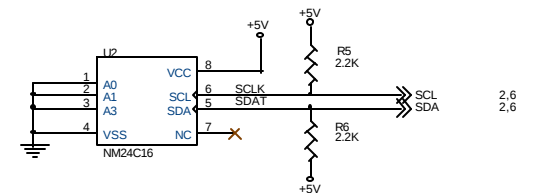
ITE, Inc.			
Title IT8871 Revision History			
Size	Document Number IT-8871-CG-4J-S01 V0.3		Rev 0.3
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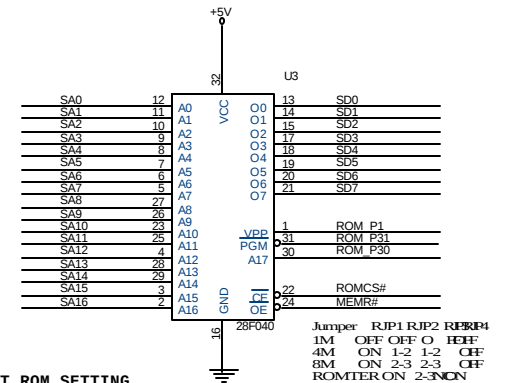
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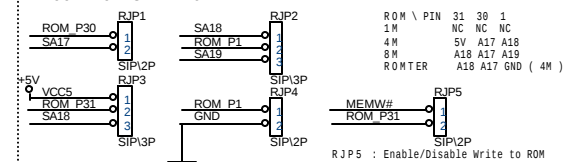
Optional SM ROM



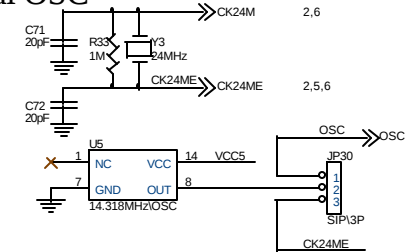
Optional EEPROM



BOOT ROM SETTING



Optional OSC

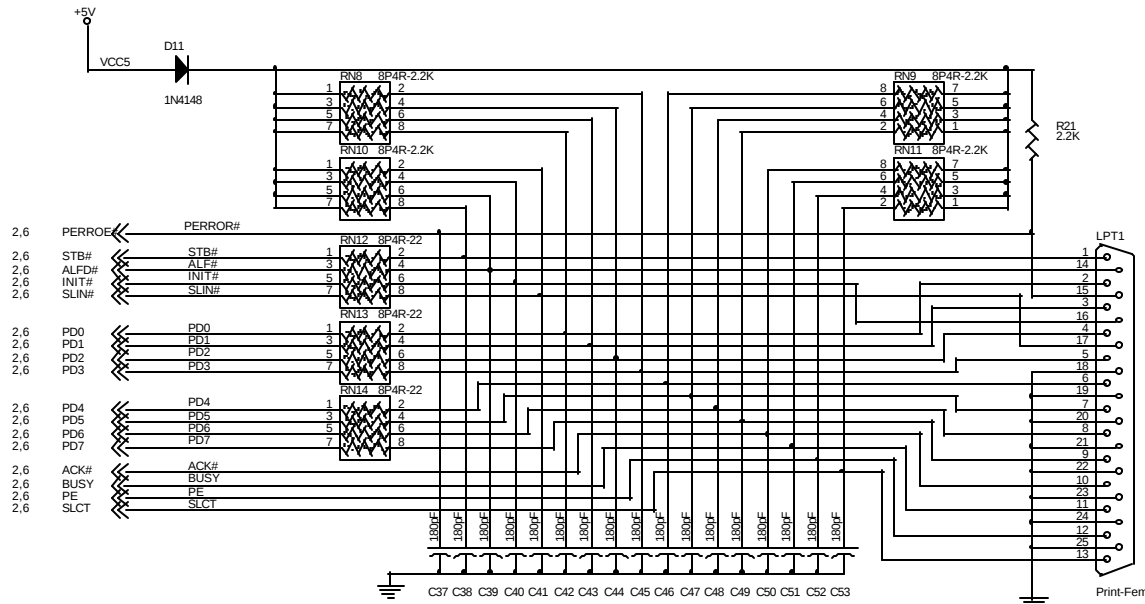


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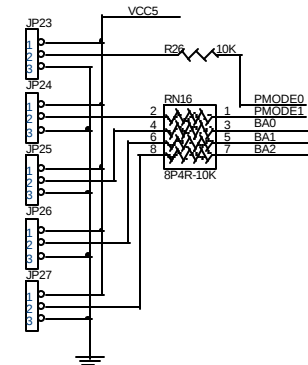
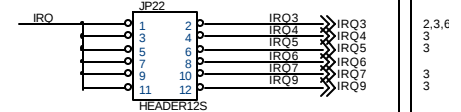
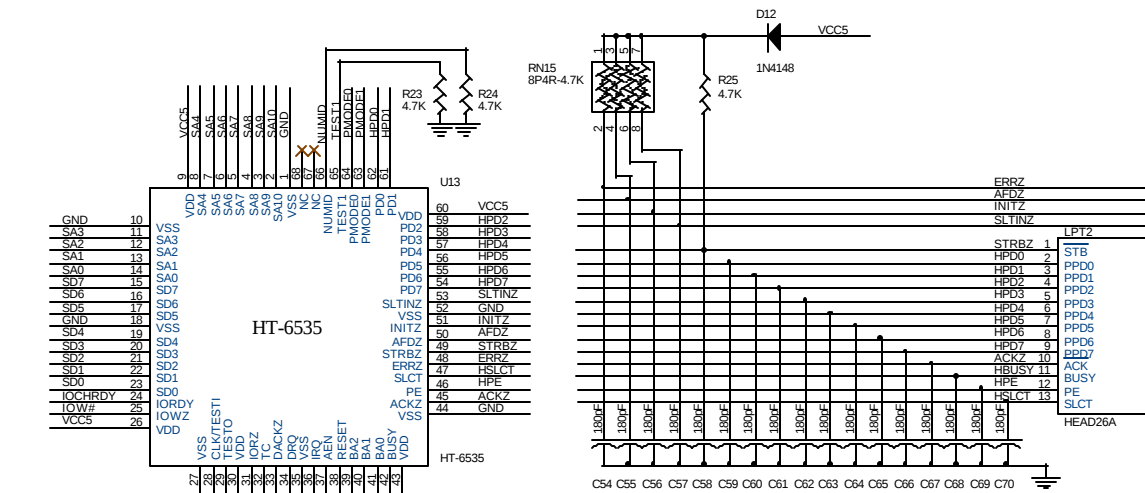
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Title		
ISA Slot, SM ROM & EEPROM		
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Parallel Port



SD[0..7]	SD[0..7]	2,3,6
SA[0..19]	SA[0..19]	2,3,6
AEN	AEN/IRQ15/P80LA	2,3,6
IOCHRDY	IOCHRDY	2,3,6
RSTDRV	RSTDRV	2,3,6
IOW#	IOW#	2,3,6
IOR#	IOR#	2,3,6
TC	TC/CSD3#	2,3,6
CK24M	CK24M	2,3,6
DRQ	DRQ1/IRQ15	2,3,6
CK24ME	CK24ME	2,3,6

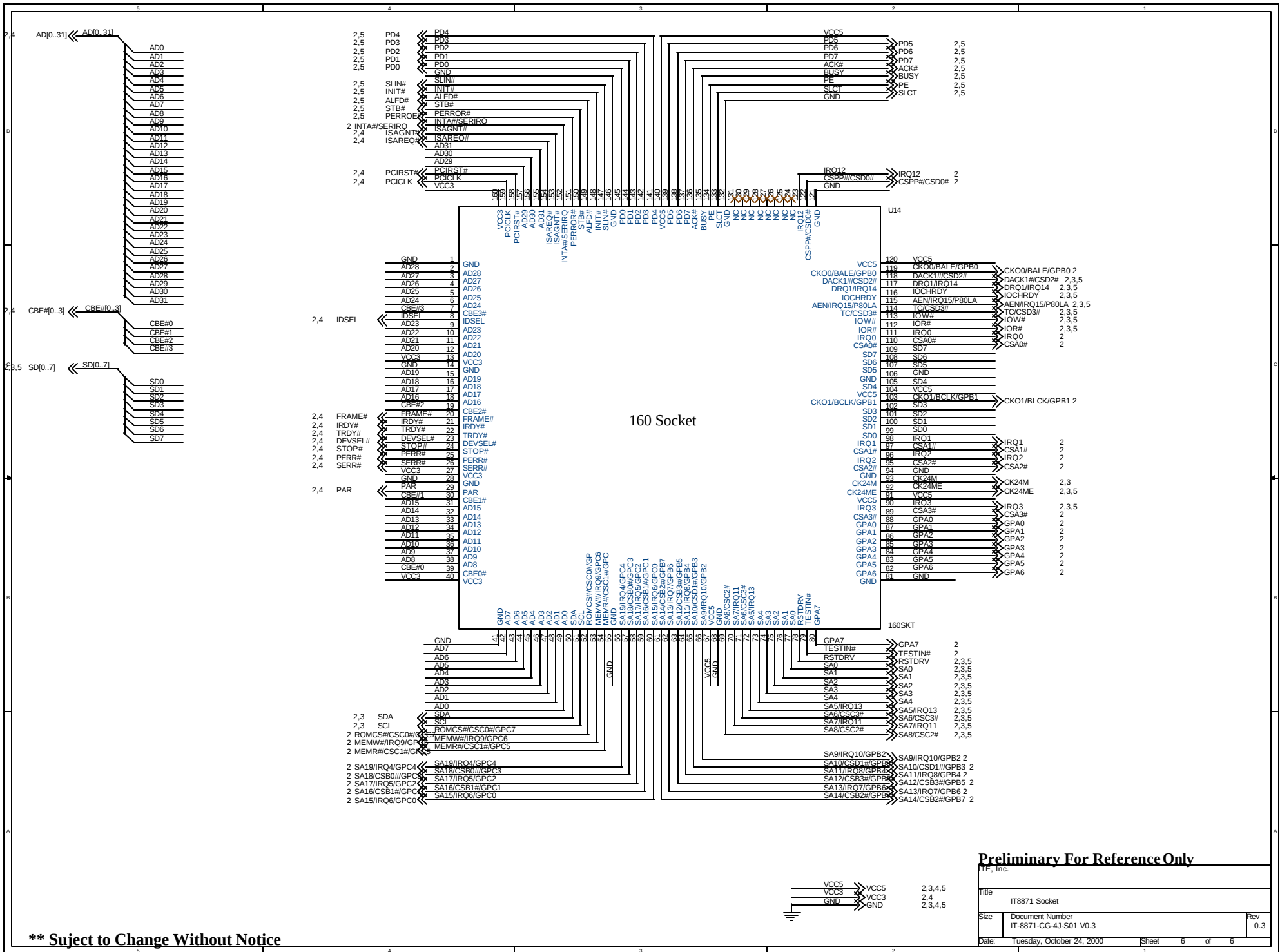


Jumper JP23	2,3,6
SPP	2,3,6
EPP	2,3,6
ECP	2,3,6
Jumper JP24	2,3,6
378h	2,3,6
278h	2,3,6
Disable	2,3,6
268h	2,3,6
26Ch	2,3,6
27Ch	2,3,6
Disable	2,3,6

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