



Digital-to-Analog Selection Table

Generic Part #	Resolution (Bits)	Settling Time (ns typ)	# DACs Per Pkg	Output (I or V)	Data Interface	FS Out Range	REF (Int/Ext)	Power Supply (V nom)	Power Dissipation (mW max)
8-Bit, 10 ns to 100 ns, Sorted by Resolution, Update Rate and # of DACs Per Package									
AD9748	8	11	1	I	Par	Diff (0 to 2mA), Diff (0 to 20mA)	Int/Ext	Range-Single (+2.7 to +3.6), Single (+3), Single (+3.3)	135
AD9708	8	35	1	I	Par	Diff Uni 2mA, Diff Uni 20 mA	Int/Ext	Range-Single (+2.7 to +5.5), Single (+3), Single (+3.3), Single (+5)	180
AD9709	8	35	2	I	Par	Adjustable Uni (2mA) to Uni (20 mA)	Int/Ext	Single (+3 to +5), Range-Single (+2.7 to +5.5)	410
DAC08	8	85	1	I	Par	Diff (0 to 2 mA)	Ext	Dual (+15, -15)	174

Last Updated: 2/2003