

# SM7186

#### **DESCRIPTIONS:**

SM7186 is an Audio Echo Processor using CMOS Technology. It has built-in AD/DA and 48KBits SRAM for time delay. External Resister adjust large range oscillation frequency and control delay time. Many combination to fit your applications.

#### **FEATURE:**

- \* Built-in adjustable OSC.
- \* Built-in power on reset circuit.
- \* Built-in 48KBits SRAM for delay control.
- \* Adjustable delay time setting: 30ms ~ 700ms
- \* Low operating current.

#### **APPLICATIONS:**

- \* VCD
- \* Karaoke
- \* T\/
- \* Audio System
- \* Car Stereo
- \* VCR

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VDD		1	16		OP1-IN
VREF		2	15		OP1-OUT
AGND		3	14		OP2-OUT
DGND		4	13		OP2-IN
OSCO		5	12		DAC-OUT
OSCR		6	11		DAC-IN
DACTL		7	10		ADC-IN
ADCTL		8	9		ADC-OUT
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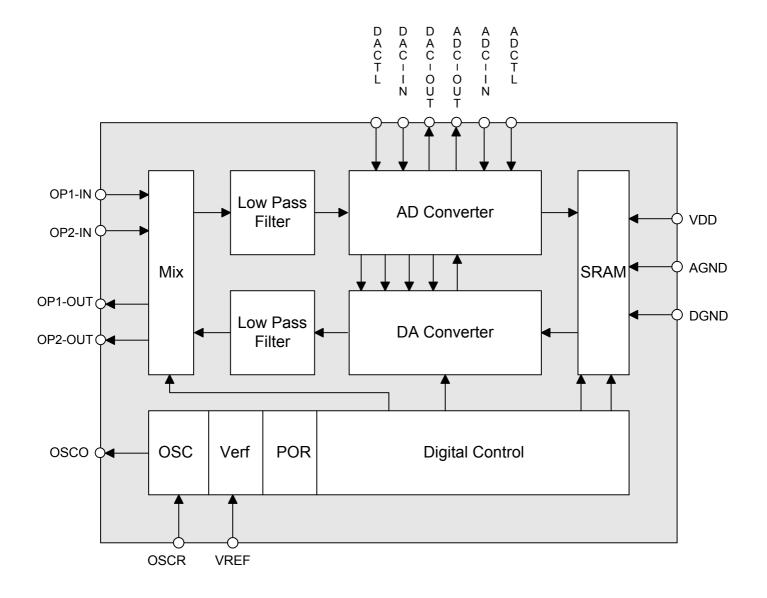


## **PIN DESCRIPTION**

PIN No.	PIN NAME	I/O	FUNCTION	
1	VDD	Power	Positive power supply	
			, ,,,,	
2	VREF	AI/O	Analog reference voltage	
3	AGND	Power	Analog negative power supply	
4	DGND	Power	Digital negative power supply	
5	OSCO	osc	System clock output	
6	OSCR	osc	Add resistor to modify internal OSC	
7	DACTL	AIN	DAC control	
8	ADCTL	AIN	ADC control	
9	ADC-OUT	AOUT	ADC OP output	
10	ADC-IN	AIN	ADC OP input	
11	DAC-IN	AIN	DAC OP input	
12	DAC-OUT	AOUT	DAC OP output	
13	OP2-IN	AIN	OP2 input	
14	OP2-OUT	AOUT	OP2 output	
15	OP1-OUT	AOUT	OP1 output	
16	OP1-IN	AIN	OP1 input	

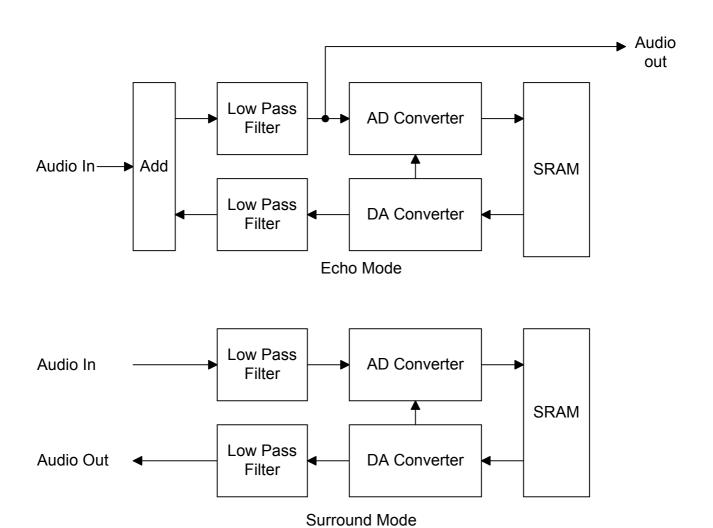


## **BLOCK DIAGRAM**





## **Echo Mode & Surround Mode**



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## **DELAY TIME TABLE**

OSCR	Fosc (MHz)	Delay Time ms
160ΚΩ	1.047	737.28
<b>75</b> ΚΩ	1.989	368.64
33ΚΩ	3.935	184.32
24ΚΩ	4.893	160.00
18ΚΩ	5.974	122.88
11ΚΩ	7.977	92.16
6.8KΩ	9.99	73.73
4.7ΚΩ	11.84	61.44
3.3ΚΩ	13.72	52.66
2.4ΚΩ	15.94	46.08
1.8ΚΩ	18.18	40.96
1.5ΚΩ	19.68	36.86
1.1ΚΩ	22.05	33.51
820 Ω	23.87	30.72

## **AC CHARACTERISTICS**

( Vcc = 5.0V, f  $_{in}$  = 1KHz, Vi = 100mVrms, fck = 4.608MHz, Ta = 25  $^{\circ}$ C, unless otherwise noted )

SYMBOL	PARAMETER	Test condition	Min	Тур	Max	Unit
Icco	Supply current			10	20	mA
Gv	Voltage gain	RL = 47KΩ		-0.5	2.5	dB
Vomax	Maximum output voltage	THD = 10%	0.8	1.4		Vrms
THD	Output distortion			0.38	0.9	%
No	Output noise voltage			-85	-70	dBV
PSRR	Power supply rejection ratio	△ Vcc = -20dBV f = 100Hz		-40	-25	dB
Tmute	Mute time		560	600	640	msec

## **DC CHARACTERISTICS**

Symbol	Parameter		Limits	Unit	
	Farameter	Min	Тур	Max	Offic
Vcc	Supply voltage	4.5	5	5.5	V
Icc	Supply current		10	20	mA
Vıh	"H" input voltage	0.7V			V
VıL	"L" input voltage			0.3V	V



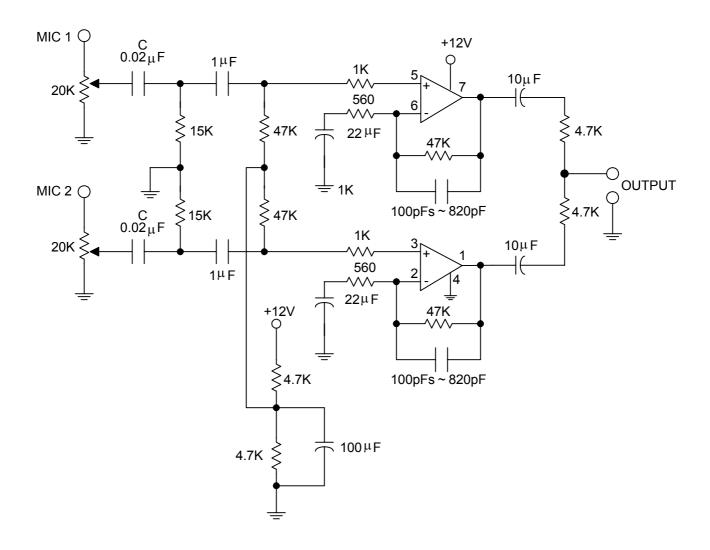
## **ABSOLUTE MAXIMUM RATINGS**

( Ta = 25  $^{\circ}$ C, unless otherwise noted )

Symbol	Decription	Limits	Unit
Vcc	Supply voltage	6.5	V
lcc	Supply current	100	mA
Pd	Power dissipation	1.7	W
Topr	Operation temperature	-20 ~ 75	° C
Tstg	Storage temperature	-25 ~ 125	° C

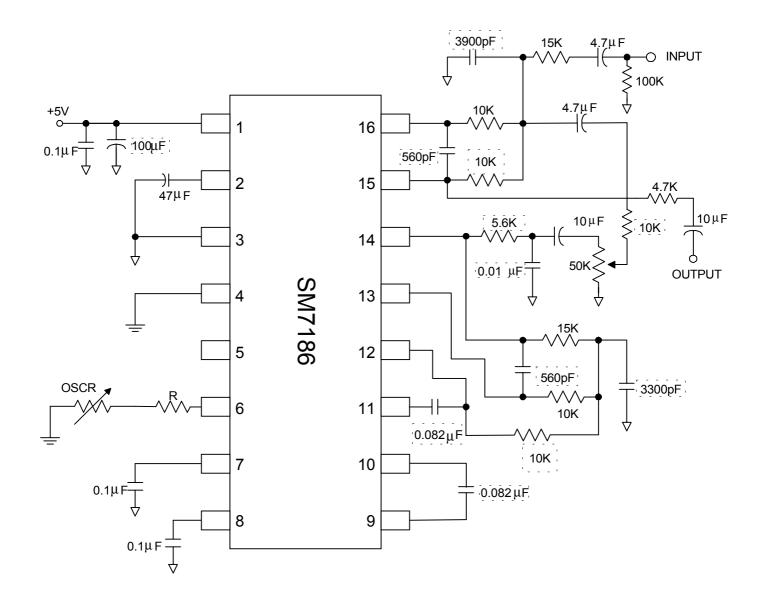


## **MIC PRE-AMP CIRCUIT**





## ECHO MODE APPLICATION CIRCUIT(MODIFIED)



- 1. Pin 6 R is approximate  $1K\sim160k\Omega$  .
- 3.  $\underline{\perp}$  Represent Digital Ground.



## **SURROUND MODE APPLICATION CIRCUIT**

