

Si9945DY*

Dual N-Channel Enhancement Mode MOSFET

General Description

These N-Channel Enhancement Mode MOSFETs are produced using Fairchild Semiconductor's advance process that has been especially tailored to minimize on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

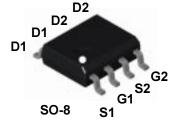
Applications

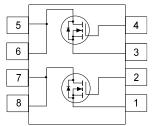
- · Battery switch
- Load switch
- Motor controls

Features

• 3.3 A, 60 V.
$$R_{DS(ON)}$$
 = 0.100 Ω @ V_{GS} = 10 V
$$R_{DS(ON)}$$
 = 0.200 Ω @ V_{GS} = 4.5 V

- · Low gate charge.
- · Fast switching speed.
- · High power and current handling capability.





Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units	
V _{DSS}	Drain-Source Voltage		60	V	
V _{GSS}	Gate-Source Voltage		<u>+</u> 20	V	
I _D	Drain Current - Continuous	(Note 1a)	3.3	Α	
	- Pulsed		10		
P _D	Power Dissipation for Single Operation		2.0	W	
	Power Dissipation for Single Operation	(Note 1a)	1.6		
		(Note 1b)	1		
		(Note 1c)	0.9		
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	∘C	

Thermal Characteristics

$R_{\rm eJA}$	Thermal Resistance, Junction-to-Ambient	62.5	∘C/W
R _{eJC}	Thermal Resistance, Junction-to-Case (Note 1)	40	∘C/W

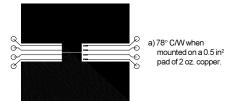
Package Outlines and Ordering Information

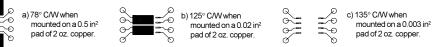
	<u> </u>	<u> </u>				
Device Marking		Device	Reel Size	Tape Width	Quantity	
	9945	SI9945DY	13"	12mm	2500 units	

^{*} Die and manufacturing source subject to change without prior notification.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	ecteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
$\frac{\Delta BVDSS}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 _μ A, Referenced to 25°C		60		mV/∘C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48 V, V _{GS} = 0 V V _{DS} = 48 V, V _{GS} = 0 V, T _J = 55°C			1 25	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Chara	cteristics (Note 2)					
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1			V
$\frac{\Delta^{\text{VGS(th)}}}{\Delta^{\text{T}_J}}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		-4.5		mV/∘C
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 3.3 A V _{GS} = 10 V, I _D = 3.3 A,T _J =125°C V _{GS} = 4.5 V, I _D = 2.5 A		0.076 0.124 0.103	0.100 0.180 0.200	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	10			Α
g _{FS}	Forward Transconductance	V _{DS} = 15 V, I _D = 3.3 A		5.3		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		345		pF
Coss	Output Capacitance	f = 1.0 MHz		110		pF
C_{rss}	Reverse Transfer Capacitance			25		pF
Switchine	Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 30 \text{ V}, I_D = 1 \text{ A}, R_L = 30 \Omega$		5	25	ns
tr	Turn-On Rise Time	$V_{GS} = 10 \text{ V, } R_{GEN} = 6 \Omega$		7.5	30	ns
t _{d(off)}	Turn-Off Delay Time			20	50	ns
t _f	Turn-Off Fall Time			7	40	ns
trr	Drain-Source Reverse Recovery Time	$I_F = 1.7 \text{ A, di/dt} = 100\text{A/}\mu\text{s}$			100	nS
Qg	Total Gate Charge	V _{DS} = 30 V, I _D = 3.3 A,		13	30	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V		1.7		nC
Q_{gd}	Gate-Drain Charge			3.2		nC
Drain-So	urce Diode Characteristic	s and Maximum Ratings				
Is	Maximum Continuous Drain-So	_			1.7	Α
V _{SD}	Drain-Source Diode Forward	V _{GS} = 0 V, I _S = 1.7 A (Note 2)		0.8	1.2	V

^{1.} R_{BJA} is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.









Scale 1 : 1 on letter size paper 2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

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