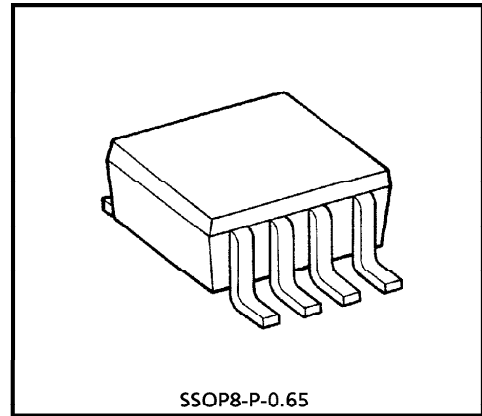


TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA75W393FU

DUAL VOLTAGE COMPARATOR

This device consist of two independent voltage comparators that designed to operate from a single power supply over a wide range of voltage. Normal operation from dual supplies is also to be guaranteed on voltage range from $\pm 1V$ to $\pm 18V$. V_{CC} is necessary at least more 1.5V volts than the input common mode voltage. The output can be connected to other open collector outputs to achieve Wired-OR relation ship.



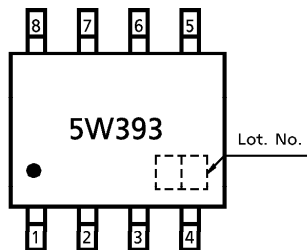
SSOP8-P-0.65

Weight : 0.021g (Typ.)

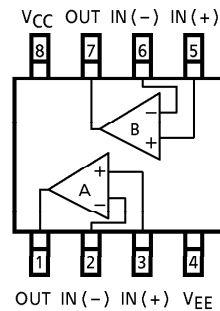
FEATURES

- Compatible to TA75393.
- Single supply voltage range or dual supplies : $2V_{DC}$ to $36V_{DC}$ or $\pm 1V_{DC}$ to $\pm 18V_{DC}$
- Low supply current : 0.8mA (Typ.)
- Low input offset voltage : $\pm 2mV$ (Typ.)
- Wide input common mode voltage range : $0V_{DC}$ to $V_{CC} - 1.5V_{DC}$
- Output compatible with TTL, DTL, MOS and CMOS logic system.
- The output can be connected to achieve Wired-OR relation.

MARKING (TOP VIEW)



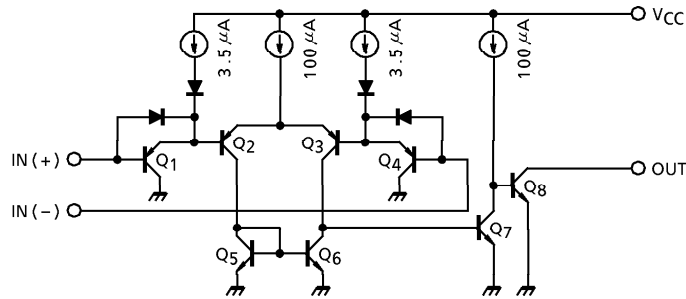
PIN CONNECTION (TOP VIEW)



961001EBA2

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EQUIVALENT CIRCUIT



MAXIMUM RATINGS (Ta = 25°C)

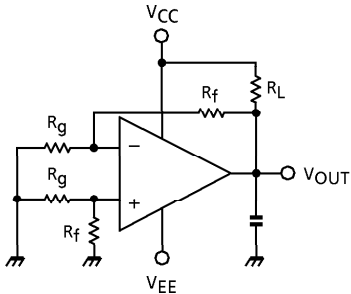
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC} , V _{EE}	± 18 or 36	V
Differential Input Voltage	DV _{IN}	± 36	V
Input Voltage	V _{IN}	- 0.3~V _{CC}	V
Power Dissipation	P _D	250	mW
Operating Temperature	T _{opr}	- 40~85	°C
Storage Temperature	T _{stg}	- 55~125	°C

ELECTRICAL CHARACTERISTICS (V_{CC} = 5V, V_{EE} = GND, Ta = 25°C)

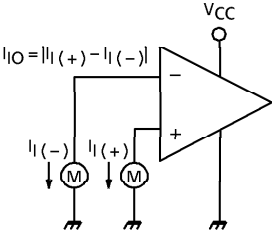
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	1	—	—	2	5	mV
Input Bias Current	I _I	2	—	—	25	250	nA
Input Offset Current	I _{IO}	2	—	—	5	50	nA
Common Mode Input Voltage	CMV _{IN}	—	—	0	—	V _{CC} - 1.5	V
Supply Current	I _{CC}	3	No load	—	0.8	2	mA
Voltage Gain	G _V	—	R _L = 15kΩ	—	200	—	V/mV
Sink Current	I _{sink}	4	IN (+) = 0V _{DC} , IN (-) = 1V _{DC} V _{OL} = 1.5V	6	16	—	nA
Output Voltage ("L" Level)	V _{OL}	5	IN (+) = 0V _{DC} , IN (-) = 1V _{DC} I _{sink} = 3mA	—	0.2	0.4	V
Output Leak Current	I _{LEAK}	—	IN (+) = 1V _{DC} , IN (-) = 0V _{DC} V _O = 5V _{DC}	—	0.1	—	nA
Response Time	t _{rsp}	6	R _L = 5.1kΩ, C _L = 15pF	—	1.3	—	μs

TEST CIRCUIT

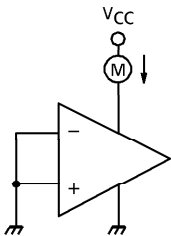
(1) V_{IO}



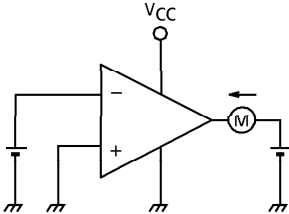
(2) I_I, I_{IO}



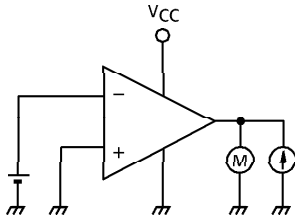
(3) I_{CC}



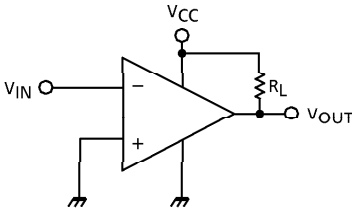
(4) I_{sink}

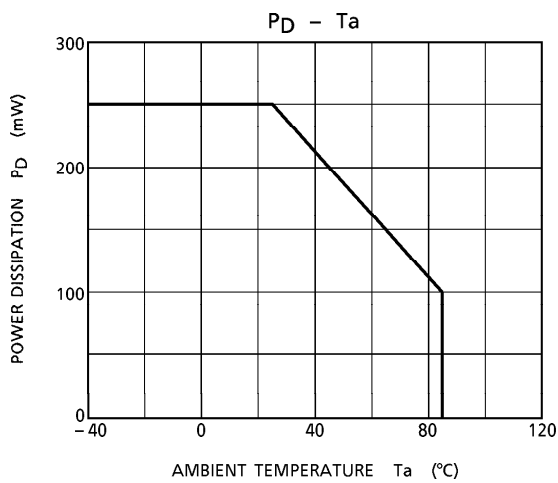
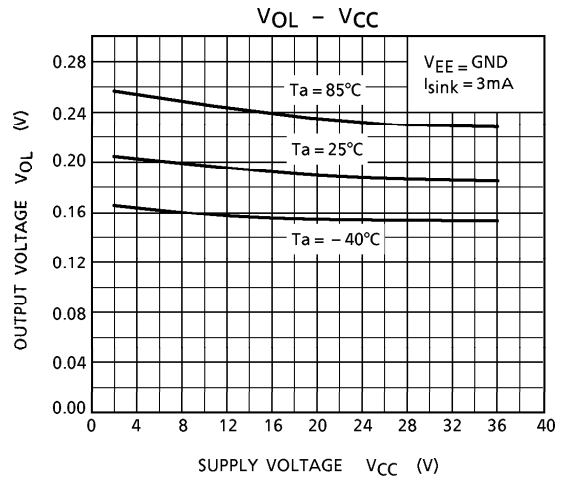
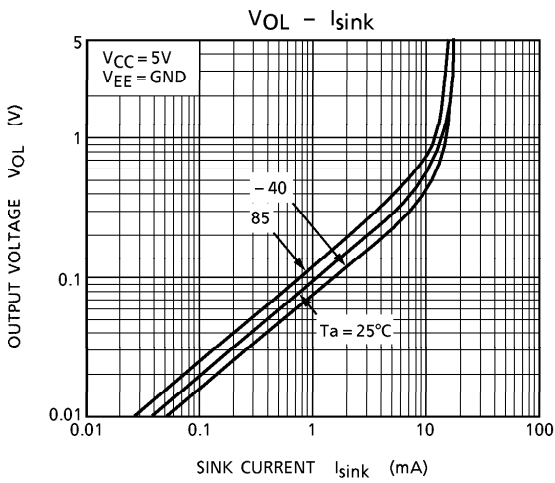
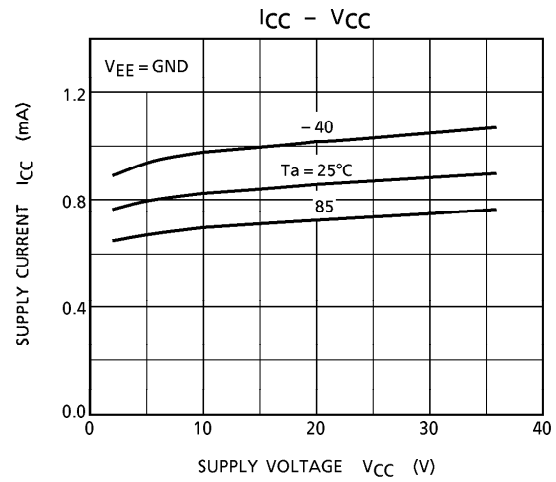
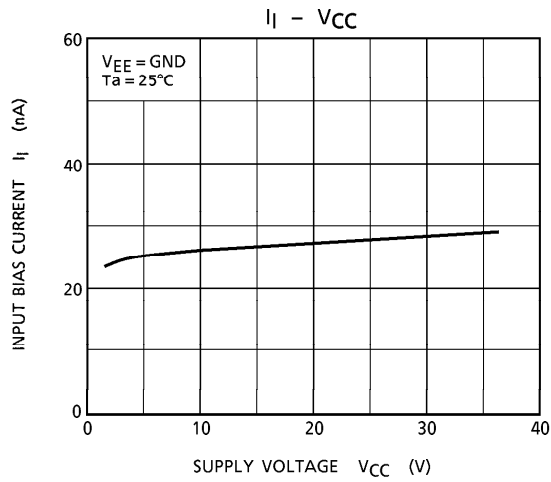


(5) V_{OL}



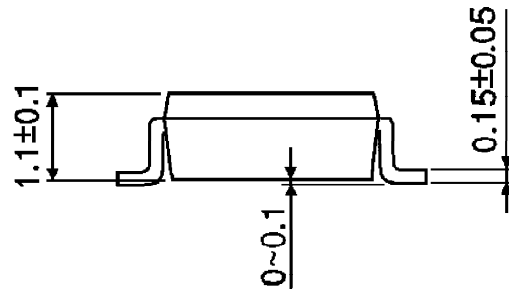
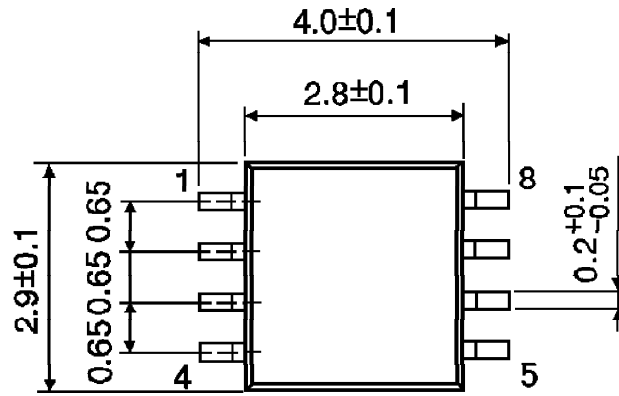
(6) t_{rsp}





OUTLINE DRAWING
SSOP8-P-0.65

Unit : mm



Weight : 0.021g (Typ.)