



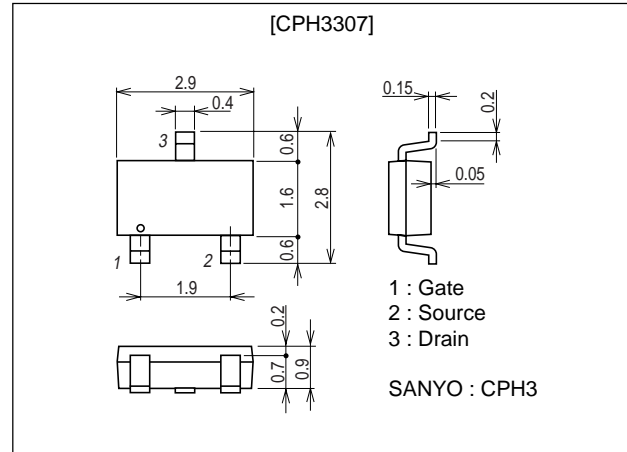
Ultrahigh-Speed Switching Applications

Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensions

unit : mm
2152A



Specifications

Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-20	V
Gate-to-Source Voltage	V_{GSS}		± 10	V
Drain Current (DC)	I_D		-3	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	-12	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm ² X0.8mm)	1.2	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Electrical Characteristics

 at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}$, $V_{GS} = 0$	-20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{V}$, $V_{GS} = 0$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}$, $I_D = -1\text{mA}$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}$, $I_D = -1.5\text{A}$	3.5	5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -1.5\text{A}$, $V_{GS} = -4\text{V}$		70	90	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -1\text{A}$, $V_{GS} = -2.5\text{V}$		95	130	$\text{m}\Omega$

Marking : JG

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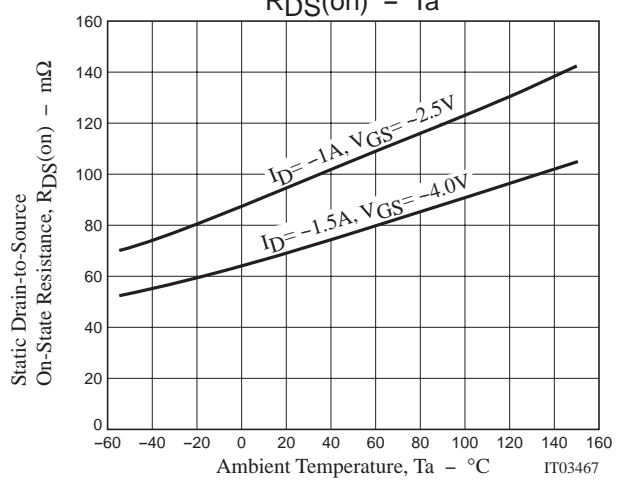
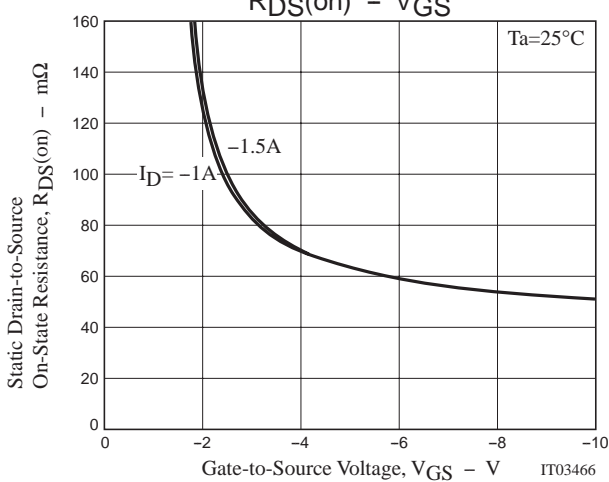
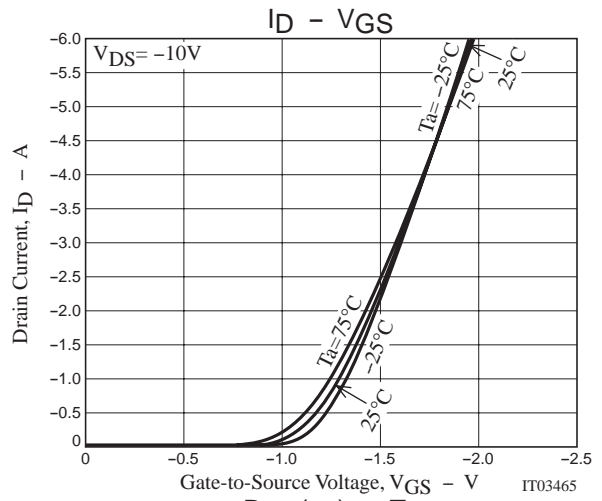
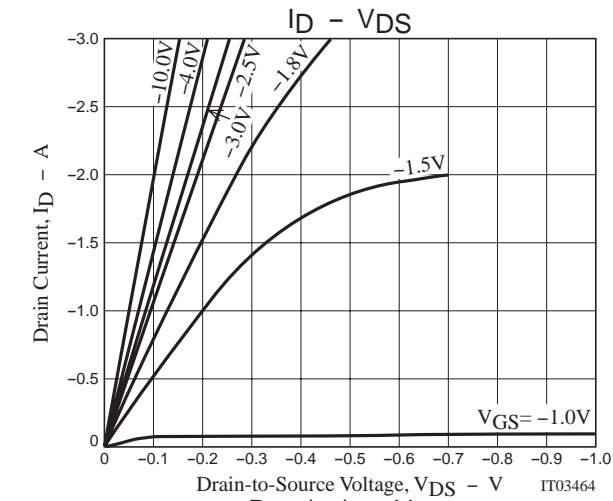
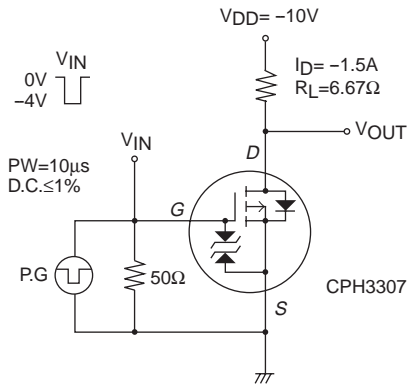
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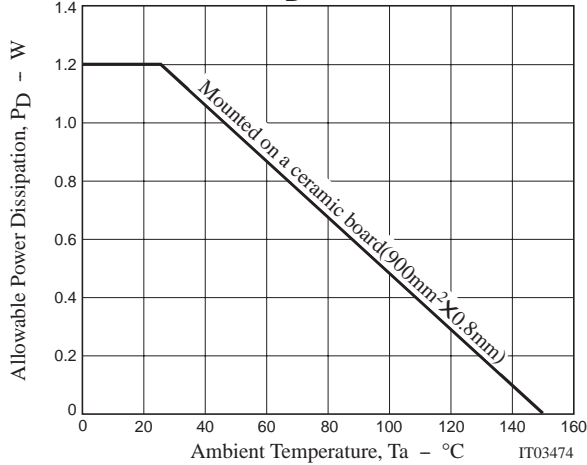
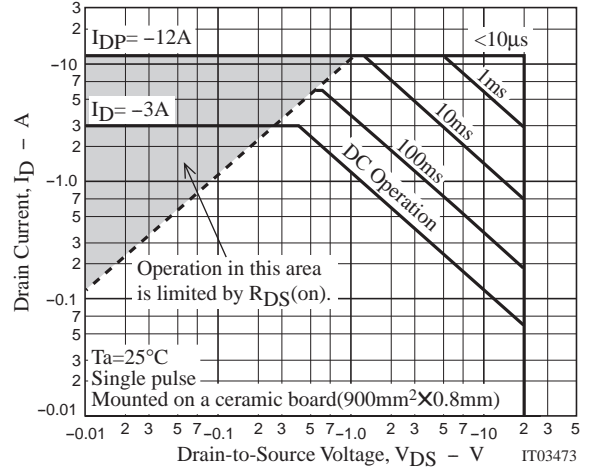
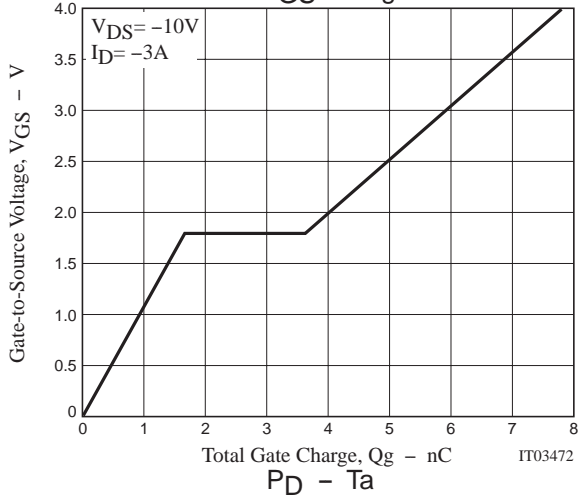
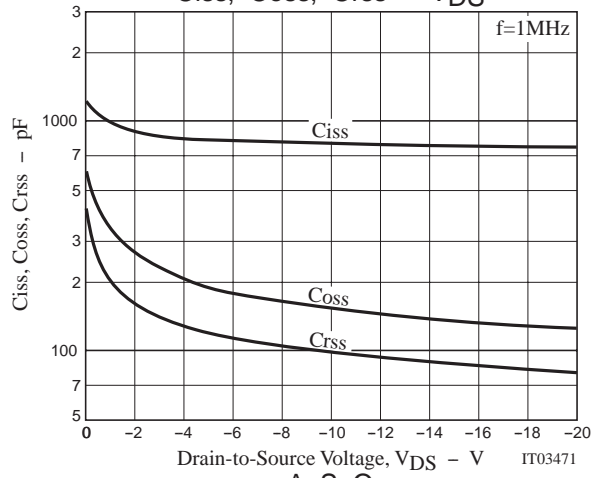
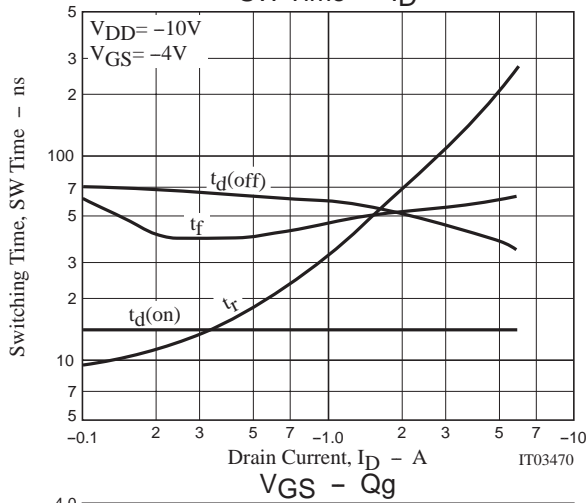
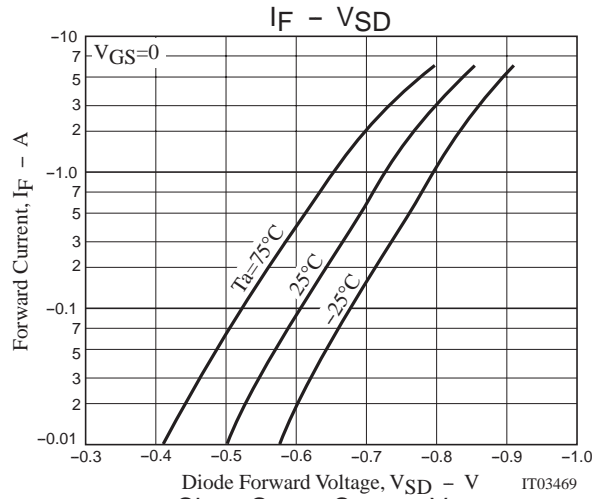
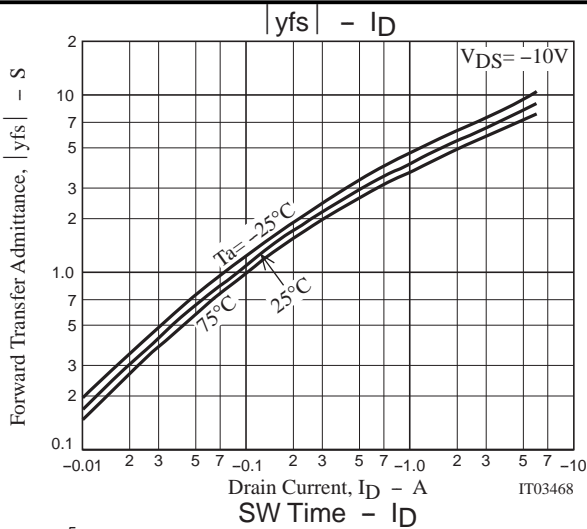
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		800		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		160		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		100		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		14		ns
Rise Time	t _r	See specified Test Circuit		50		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit		56		ns
Fall Time	t _f	See specified Test Circuit		51		ns
Total Gate Charge	Q _g	V _{DS} =-10V, V _{GS} =-4V, I _D =-3A		7.8		nC
Gate-to-Source Charge	Q _{gs}	V _{DS} =-10V, V _{GS} =-4V, I _D =-3A		1.7		nC
Gate-to-Drain "Miller" Charge	Q _{gd}	V _{DS} =-10V, V _{GS} =-4V, I _D =-3A		1.9		nC
Diode Forward Voltage	V _{SD}	I _S =-3A, V _{GS} =0	-0.8		-1.5	V

Switching Time Test Circuit





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