

June 7, 1999

 TEL:805-498-2111 FAX:805-498-3804 WEB:<http://www.semtech.com>

## DESCRIPTION

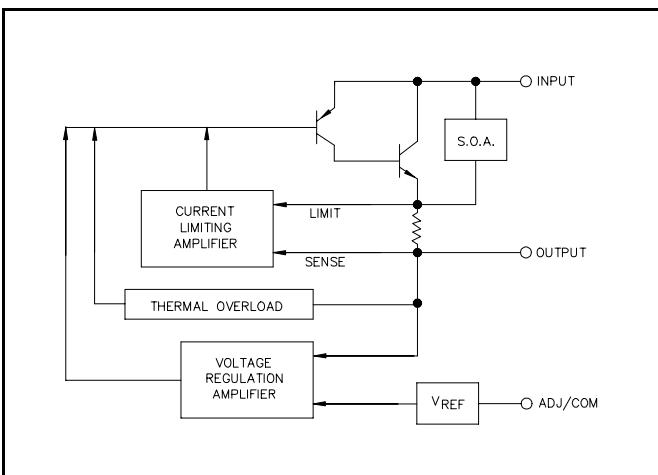
The EZ1083 series of high performance positive voltage regulators are designed for use in applications requiring low dropout performance at full rated current. Additionally, the EZ1083 series provides excellent regulation over variations in line, load and temperature.

Outstanding features include low dropout performance at rated current, fast transient response, internal current limiting and thermal shutdown protection of the output device. The EZ1083 has both fixed and adjustable voltage options, while the EZ1083B is adjustable only for cost sensitive applications. These devices are available in the popular TO-220 package.

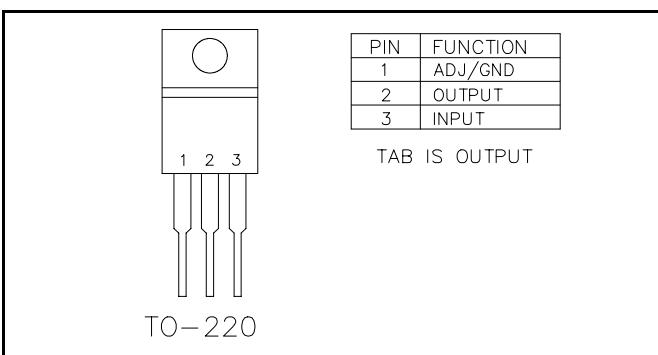
## APPLICATIONS

- High current microprocessor supplies
- Post regulators

## BLOCK DIAGRAM



## PIN CONFIGURATIONS



## FEATURES

- Low dropout performance, 1.3V max. for EZ1083, 1.45V max. for EZ1083B
- Full current rating over line and temperature
- Fast transient response
- $\pm 2\%$  total output regulation over line, load and temperature ( $\pm 2.4\%$  for EZ1083B)
- Adjust pin current max 90 $\mu$ A over temperature
- Fixed/adjustable output voltage (EZ1083)
- Line regulation typically 0.015%
- Load regulation typically 0.05%
- TO-220 package

## ORDERING INFORMATION

DEVICE	PACKAGE	V <sub>OUT</sub> VOLTS
EZ1083CT-X.X EZ1083BCT	TO-220	See Note (1) 1.30 to 5.7

### Note:

(1) Where X.X denotes voltage options. Available voltages are: 1.5V, 2.5V and 3.3V. Leave blank for adjustable version (1.3 to 5.7V). Contact factory for additional voltage options.

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Maximum	Units
Input Supply Voltage	V <sub>IN</sub>	7	V
Power Dissipation	P <sub>D</sub>	Internally Limited	W
Thermal Resistance Junction to Case	$\theta_{JC}$	2	°C/W
Thermal Resistance Junction to Ambient	$\theta_{JA}$	50	°C/W
Operating Junction Temperature Range	T <sub>J</sub>	0 to 125	°C
Storage Temperature Range	T <sub>STG</sub>	-65 to 150	°C
Lead Temperature (Soldering) 10 Sec.	T <sub>LEAD</sub>	260	°C

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## ELECTRICAL CHARACTERISTICS

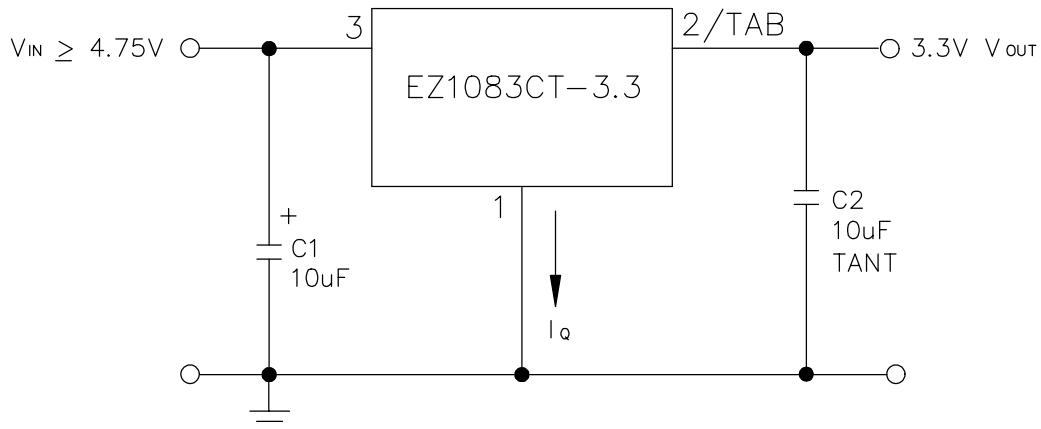
Unless otherwise specified, Adj  $V_{IN}$  = 2.75V to 7.0V and Adj  $I_O$  = 10mA to 7.5A;  
 Fixed  $V_{IN}$  = 4.75 to 7.0V and Fixed  $I_O$  = 0 mA to 7.5A

		Test Conditions			Test Limits			
Parameter	Symbol	$V_{IN}$	$I_O$	$T_J^{(5)}$	Min	Typ	Max	Units
Output Voltage <sup>(1)</sup> EZ1083-X.X	$V_O$	5V	0mA	25°C	0.99 <i> V<sub>O</sub> </i>	$V_O$	1.01 <i> V<sub>O</sub> </i>	V
				O.T.	0.98 <i> V<sub>O</sub> </i>	$V_O$	1.02 <i> V<sub>O</sub> </i>	
Reference Voltage <sup>(1)</sup> EZ1083	$V_{REF}$	5V	10mA	25°C	1.238	1.250	1.262	V
				O.T.	1.225	1.250	1.275	
Reference Voltage <sup>(1)</sup> EZ1083B	$V_{REF}$	5V	10mA	25°C	1.233	1.250	1.267	V
				O.T.	1.220	1.250	1.280	
Line Regulation <sup>(1)</sup>	$REG_{(LINE)}$		10mA	25°C		0.015	0.2	%
				O.T.		0.035	0.2	
Load Regulation <sup>(1)</sup>	$REG_{(LOAD)}$	5V		25°C		0.05	0.3	%
				O.T.		0.2	0.4	
Dropout Voltage <sup>(2)</sup> EZ1083, EZ1083-X.X EZ1083B	$V_D$			25°C		1		V
				O.T.		1.10	1.30	
						1.20	1.45	
Current Limit	$I_{CL}$			O.T.	7.5	9.5		A
Quiescent Current Fixed Voltage Version	$I_Q$	5V		O.T.		12	14	mA
Temperature Coefficient	$T_C$			O.T.		0.005		%/°C
Adjust Pin Current	$I_{ADJ}$			25°C		55		μA
				O.T.			90	
Adjust Pin Current Change	$\Delta I_{ADJ}$			O.T.		0.2	5	μA
Temperature Stability	$T_S$	5V	0.5A	O.T.		0.5		%
Minimum Load Current Adj Voltage Version	$I_O$	5V		O.T.		5	10	mA
RMS Output Noise <sup>(3)</sup>	$V_N$			25°C		0.003		% $V_O$
Ripple Rejection Ratio <sup>(4)</sup>	$R_A$	5V	7.5A	O.T.	60	72		dB

### NOTES:

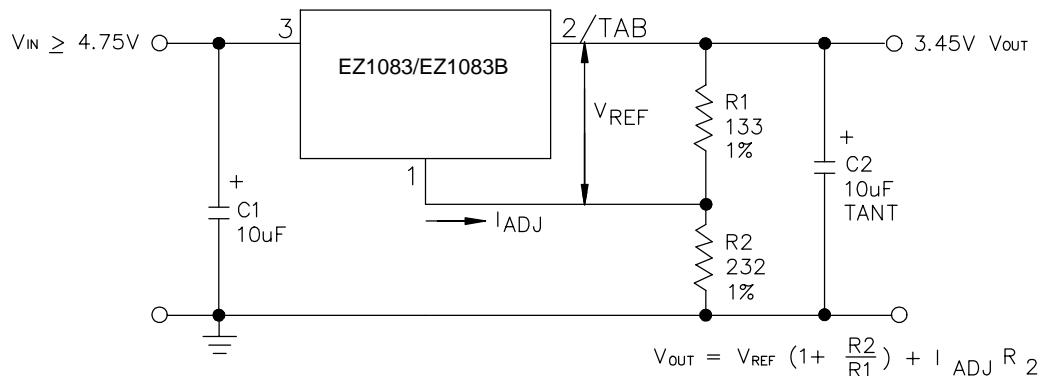
- (1) Low duty cycle pulse testing with Kelvin connections required.
- (2)  $\Delta V_{OUT}$ ,  $\Delta V_{REF} = 1\%$ .
- (3) Bandwidth of 10 Hz to 10 kHz.
- (4) 120 Hz input ripple ( $C_{ADJ}$  for ADJ = 25μF).
- (5) Over Temp. (O.T.) = over specified operating junction temperature range.

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**TYPICAL APPLICATIONS**
**FIXED VOLTAGE REGULATOR<sup>(1)(2)</sup>**


(1) C1 NEEDED IF DEVICE IS FAR FROM FILTER CAPACITORS.

(2) C2 REQUIRED FOR STABILITY.

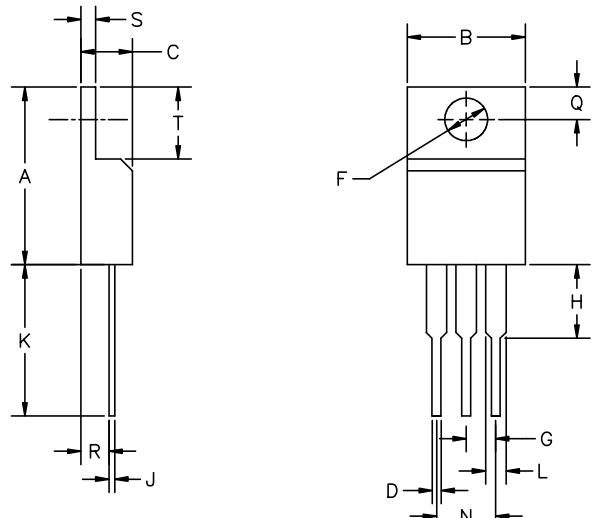
**ADJUSTABLE VOLTAGE REGULATOR<sup>(1)(2)</sup>**


(1) C1 NEEDED IF DEVICE IS FAR FROM FILTER CAPACITORS.

(2) C2 REQUIRED FOR STABILITY.

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**OUTLINE - TO-220**



The diagram shows two views of a TO-220 package. The left view is a top-down cross-section with dimensions A, C, S, T, K, R, and J labeled. The right view is a side profile with dimensions B, Q, F, H, G, L, D, N, and a central circular feature labeled with a question mark.

DIM <sup>N</sup>	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.560	.650	14.23	16.51	
B	.380	.420	9.66	10.66	
C	.140	.190	3.56	4.82	
D	.020	.045	0.51	1.14	
F	.139	.161	3.54	4.08	
G	.090	.110	2.29	2.79	
H	—	.250	—	6.35	
J	.012	.045	.31	1.14	
K	.500	.580	12.70	14.73	
L	.070	.095	1.77	2.41	
N	.190	.210	4.83	5.33	
Q	.100	.135	2.54	3.42	
R	.080	.115	2.04	2.92	
S	.020	.055	.51	1.39	
T	.230	.270	5.85	6.85	

JEDEC TO-220