TOSHIBA TD7101F

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

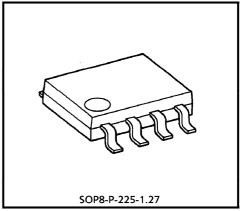
TD7101F

ELC PRESCALER FOR DIGITAL SYNTHESIZED TUNER

TD7101F is a 2 modulus prescaler developed for low operating voltage digital synthesized tuner, and can operate up to 150 MHz.

FEATURES

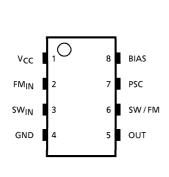
- Operating frequency range is 1.5~35 MHz/50~150 MHz.
- 2 modulus prescaler : $N = 4 \times 15/16$ and N = 15/16
- Input voltage sensitivity is V_{IN} (FM) = 35 m V_{rms} , V_{IN} (SW) $= 40 \, \text{mV}_{rms}$
- 3 V low operating supply voltage.
- The package is SOP-8 pins.

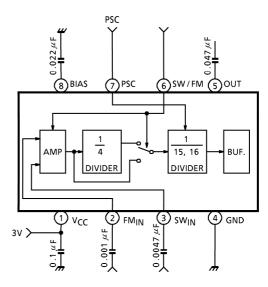


Weight: 0.08 g (Typ.)

PIN CONNECTION

BLOCK DIAGRAM





(Note) This device is vulnerable to surge voltage. Take it into account when using this device in your system.

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PIN FUNCTION

PIN No.	SYMBOL	FUNCTION	REMARKS		
1	Vcc	Power supply terminal.	_		
2	FMIN	Signal input terminal from FM local oscillator.	_		
3	SWIN	Signal input terminal from SW local oscillator.	_		
4	GND	Ground terminal.	_		
5	OUT	Divider signal output terminal.	_		
6	SW / FM	Dividing mode control terminal. "H" level input: SW _{IN} is selected, direct mode. "L" level input: FM _{IN} is selected, 1/4 mode.	_		
7	PSC	2 modulus mode control terminal. "H" level input: 1/16 dividing "L" level input: 1/15 dividing	_		
8	BIAS	Bias capacitor terminal. Bias capacitor is connected.	_		

MAXIMUM RATINGS (Ta = 25°C)

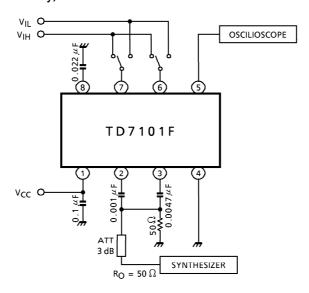
CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	Vcc	6.5	V
Power Dissipation	PD	200	mW
Input Voltage	VIN	-0.3~V _{CC} + 0.3	٧
Operating Temperature	T _{opr}	− 10~60	°C
Storage Temperature	T _{stg}	- 55∼150	°C

ELECTRICAL CHARACTERISTICS

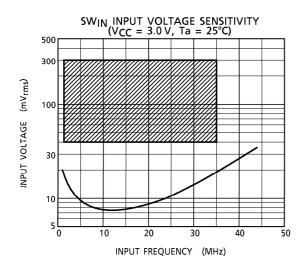
(Unless otherwise specified, $V_{CC}=1.8\sim5.5\,V$, $T_{a}=-10\sim60^{\circ}C$, f_{in} (FM) = $50\sim150\,MHz$, f_{in} (SW) = $1.5\sim35\,MHz$)

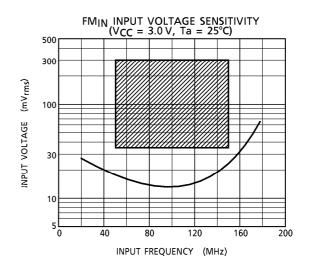
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage		Vcc	 —	_	1.8	3.0	5.5	V
Supply Cuurent		lcc	_	V _{CC} = 3.0 V	_	5.5	9.5	mA
Operating Frequency		f _{IN 1}	_	FMIN	50	_	150	MHz
Range		f _{IN 2}	_	SWIN	1.5	_	35	
Input Voltage Range		V _{IN 1}	_	FMIN	35	_	300	mV _{rms}
		V _{IN 2}	_	sw _{IN}	40	_	300	
Output Amplitude		Vout	_	_	0.5	_	_	V _{p-p}
Input Voltage	"H" Level	VIH	_	PSC, SW/FM	1.6	_	Vcc	V
	"L" Level	V _{IL}	_	PSC, SW/FM	0	_	1.0	
Input Current	"H" Level	"L" Lovel L	_	PSC, SW/FM,	_	_	60	- μΑ
		lін		$V_{CC} = 5.0 \text{ V}, V_{IH} = 4.0 \text{ V}$				
	"L" Level	vol lu		PSC, SW/FM,		l	10	
		"L" Level	'IL		$V_{CC} = 5.0 \text{ V}, V_{IL} = 1.0 \text{ V}$			10

TEST CIRCUIT (Input voltage sensitivity)



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(Note) Operating range ($V_{CC} = 1.8 \sim 5.5 \text{ V}$, Ta = $-10 \sim 60 ^{\circ}\text{C}$)

OUTLINE DRAWING SOP8-P-225-1.27 Unit : mm 0.595TYP 1.27 0.4±0.1 0.25 W 1.27 0.525±0.2

Weight: 0.08 g (Typ.)