

FEATURES

- Fixed and Adjustable Versions Available
- Output Current up to 500mA
- Very Low Quiescent Current
- Reverse Battery Protection
- Input-output Differential less than 0.6V
- Short Circuit Protection
- Internal Thermal Overload Protection
- Load Dump Protection
- Overvoltage Protection
- Mirror Image Insertion Protection
- ON/OFF Pin

APPLICATIONS

- Cellular Telephones
- Portable Consumer Equipment
- Portable (Notebook) Computers
- Battery Powered Systems
- Portable Instrumentation
- Radio Control Systems
- CD/DVD drives
- Automotive

GENERAL DESCRIPTION

The AMS3107 series consists of positive fixed and adjustable voltage regulators ideally suited for use in battery-powered systems. These devices feature very low quiescent current of 0.8mA or less when supplying 50mA loads. This unique characteristic and the extremely low input-output differential required for proper regulation (0.2V for output currents of 100mA) make the AMS3107 ideal to use for standby power systems.

Internal circuitry of AMS3107 is protected from input fault conditions caused by reverse battery installation or input voltages that exceed maximum rated input voltage. During line transients, such as load dump (40V) when the input voltage to the regulator can momentarily exceed the specified maximum operating voltage, the regulator will automatically shut down to protect both internal circuits and the load. The AMS3107 series also includes internal current limiting, thermal shutdown, and is able to withstand temporary power-up with mirror-image insertion.

The AMS3107 is offered in 3 lead TO-92, 8 lead SOIC, SOT-89, SOT-223 and TO-252 packages.

ORDERING INFORMATION

OUTPUT VOLTAGE	PACKAGE TYPE					TEMP. RANGE
	3L TO-92	SOT-89	TO-252	SOT-223	SO-8	
FIXED	AMS3107CN-X	AMS3107CL-X	AMS3107CD-X	AMS3107C-X	AMS3107CS-X	IND
	AMS3107N-X	AMS3107L-X	AMS3107D-X	AMS3107-X	AMS3107S-X	IND
ADJ.					AMS3107S	IND

X = 2.0V, 2.5V, 3.0V, 3.3V, 3.5V, 4.0V, 5.0V.

ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Voltage		Maximum Junction Temperature	+125°C
Operating	25V	Storage Temperature	-65°C to +150°C
Overvoltage Protection	26V to 40V	Lead Temperature (Soldering 10 sec)	230°C
Internal Power Dissipation (Note 4)	Internally Limited	ESD	2000V

ELECTRICAL CHARACTERISTICS

Electrical Characteristics at $V_{IN}=6.3V$, $I_O=5mA$, $T_J=25^\circ C$, $C_2=22\mu F$ unless otherwise specified.

PARAMETER	CONDITIONS	AMS3107C-X			AMS3107-X			Units
		Min.	Typ.	Max.	Min.	Typ.	Max.	
Fixed Output Voltage Versions								
Output Voltage Tolerance		-1.0		+1.0	-3.0		+3.0	%
	$6.3V \leq V_{IN} \leq 25V$, $I_O=100mA$	-3.0		+3.0		± 5.0		%
Line Regulation	$6V \leq V_{IN} \leq 15V$		3	10		5	10	mV
	$15V \leq V_{IN} \leq 25V$		5	10		5	15	mV
Load Regulation	$5mA \leq I_O \leq 100mA$		5	10		5	15	mV
	$5mA \leq I_O \leq 200mA$		10	15		15	20	mV
	$5mA \leq I_O \leq 350mA$		15	20		20	25	mV
	$5mA \leq I_O \leq 500mA$		20	30		20	30	mV
Dropout Voltage	$I_O = 100mA$		0.15	0.20		0.25	0.30	mV
	$I_O = 200mA$		0.30	0.40		0.40	0.50	mV
	$I_O = 350mA$		0.40	0.45		0.45	0.55	mV
	$I_O = 500mA$		0.55	0.70		0.55	0.70	mV
Quiescent Current	$I_O \leq 10mA$,		0.18	0.25		0.2	0.3	mA
	$I_O = 100mA$		2	2.5		2	3	mA
	$I_O \leq 200mA$		5	8		5	8	mA
	$I_O = 350mA$		12			12		mA
Output Noise Voltage	10Hz-100kHz, $C_{OUT} = 100\mu F$		50			50		μV rms
Output Bypass Capacitor	ESR=0.1 to 10 Ω		10			10		μF
Ripple Rejection	$f_o = 120Hz$	55	80			80		dB
Maximum Operational Input Voltage		25	28		25	28		V
Maximum Line Transient	$R_L = 500\Omega$, $V_O \leq 5.5V$ $T = 1ms$, $\tau \leq 100ms$	35	40		35	40		V
Reverse Polarity Input Voltage, D/C		-35	-40		-35	-40		V
Reverse Polarity Input Voltage, Transient	$R_L = 500\Omega$, $T = 1ms$, $\tau \leq 100ms$	-35	-40		-35	-40		V

ELECTRICAL CHARACTERISTICS

Electrical Characteristics at $V_{IN}=6V$, $V_{OUT}=3V$ $I_O=5mA$, $T_J=25^\circ C$, $R1=27k$, $C2=2\mu F$ unless otherwise specified.

PARAMETER	CONDITIONS (Note 2)	Min.	AMS3107 Typ.	Max.	Units
Adjustable Version					
Reference Voltage		1.210	1.235	1.26	V
	$I_O \leq 100$ mA, $-40^\circ C \leq T_J \leq 125^\circ C$, $R1=27k$, Measured from V_{OUT} to Adj. Pin	1.20	1.235	1.270	V
Output Voltage Range		2		24	V
Line Regulation	$6V \leq V_{IN} \leq 25V$.02	1.5	mV/V
Load Regulation	$5mA \leq I_O \leq 100$ mA		0.3	0.5	%
	$5mA \leq I_O \leq 200$ mA		0.5	1	%
	$5mA \leq I_O \leq 350$ mA		1.0	1.5	%
	$5mA \leq I_O \leq 500$ mA		1.0	2.0	%
Dropout Voltage	$I_O = 100$ mA		0.05	0.2	V
	$I_O = 200$ mA		0.3	0.4	V
	$I_O = 350$ mA		0.5	0.6	V
	$I_O = 500$ mA		0.5	0.7	V
Quiescent Current	$I_O = 0$ mA,		0.18	0.3	mA
	$I_O = 100$ mA		2.0	2.5	mA
	$I_O = 200$ mA		5.0	8.0	mA
	$I_O = 350$ mA		12		mA
Output Noise Voltage	10Hz-100kHz		100		$\mu V_{rms}/V$
Output Bypass Capacitor	ESR=0.1 to 10 Ω		10		μF
Long Term Stability	T = 1000hr		0.4		%/1000hr
Ripple Rejection	$f_o = 120Hz$		0.02		dB
Maximum Operational Input Voltage		21	22		V
Maximum Line Transient	$I_O = 10mA$, Reference Voltage $\leq 1.5V$ T = 1ms, $\tau \leq 100ms$	35	40		V
Reverse Polarity Input Voltage, D/C	$R_L = 500\Omega$, $V_O \geq -0.3V$	-35	-40		V
Reverse Polarity Input Voltage, Transient	$R_L = 500\Omega$, T = 1ms, $\tau \leq 100ms$	-35	-40		V
On/Off Threshold Voltage	$V_O = 3V$		1.8	1.5	V
			2.5	2.0	V
On/Off Threshold Current	$V_{OFF} = 2.4V$		35	60	μA

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. For guaranteed performance limits and associated test conditions, see the Electrical Characteristics tables.

Note 2: To ensure constant junction temperature, low duty cycle pulse testing is used.

Note 3: Limits appearing in **boldface** type apply over the entire junction temperature range for operation. Limits appearing in normal type apply for $T_A = T_J = 25^\circ C$.

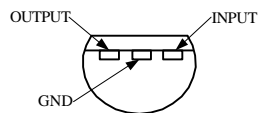
Note 4: The maximum allowable power dissipation is a function of the maximum junction temperature $T_J(MAX)$, the junction-to ambient thermal resistance θ_{JA} and the ambient temperature T_A . The maximum allowable power dissipation at any ambient temperature is calculated using:

$$P(MAX) = \frac{T_J(MAX) - T_A}{\theta_{JA}}$$

Where the value of the junction-to-ambient thermal resistance are as follows: 195 $^\circ C/W$ for TO-92 (N), 160 $^\circ C/W$ for the SO-8 (S), 110 $^\circ C/W$ for SOT-89 (L), 90 $^\circ C/W$ for SOT-223 and 80 $^\circ C/W$ for TO-252.

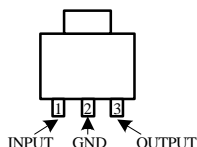
PIN CONNECTIONS

**TO-92
Plastic Package (N)**



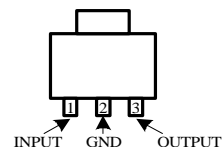
Bottom View

**SOT-89
(L)**



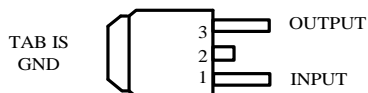
Top View

SOT-223



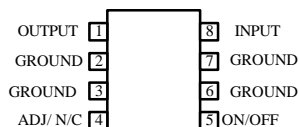
Top View

**TO-252
(D)**



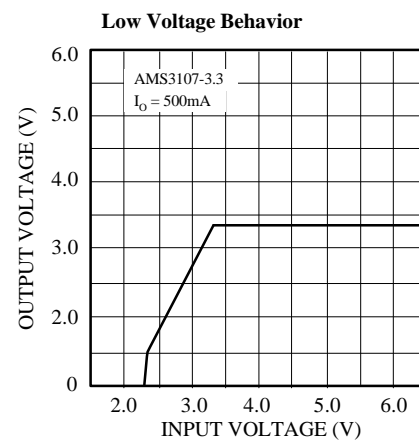
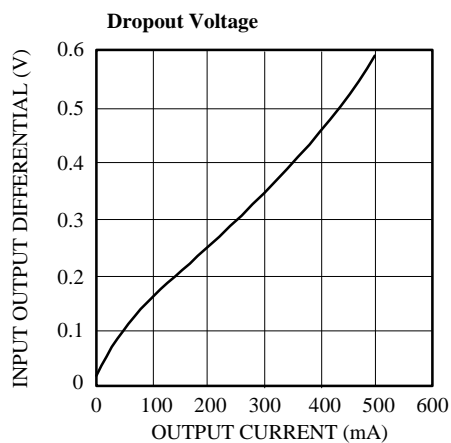
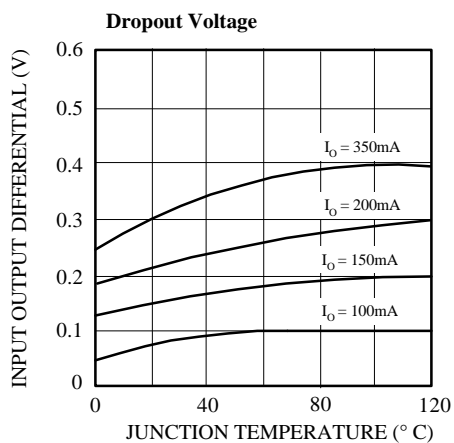
Top View

**8L SOIC
SO-Package (S)**



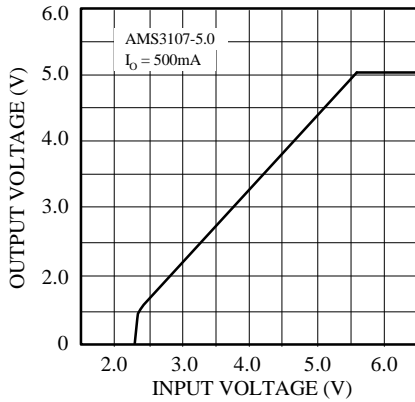
Top View

TYPICAL PERFORMANCE CHARACTERISTICS

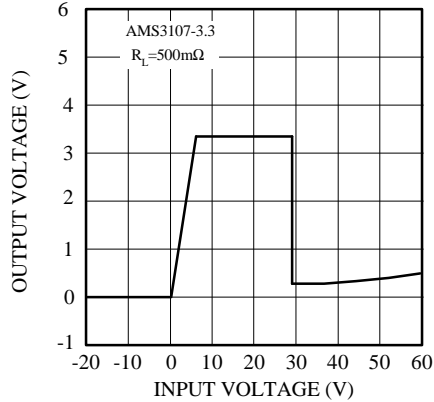


TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

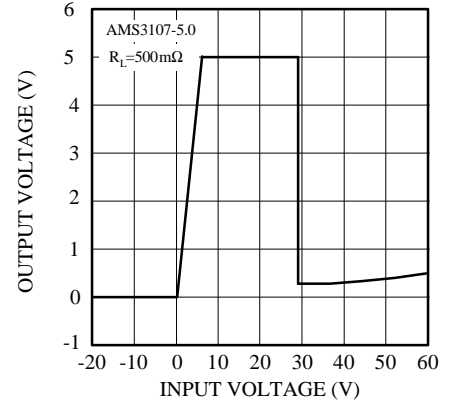
Low Voltage Behavior



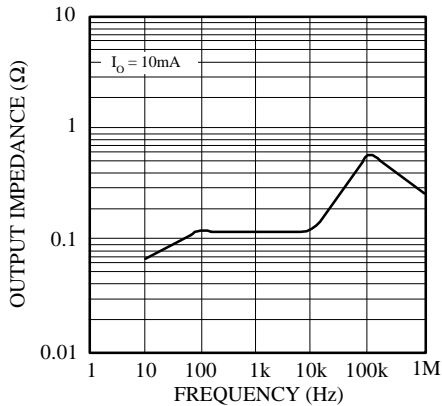
Output at Voltage Extremes



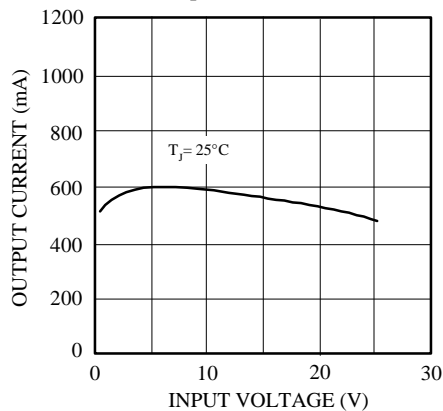
Output at Voltage Extremes



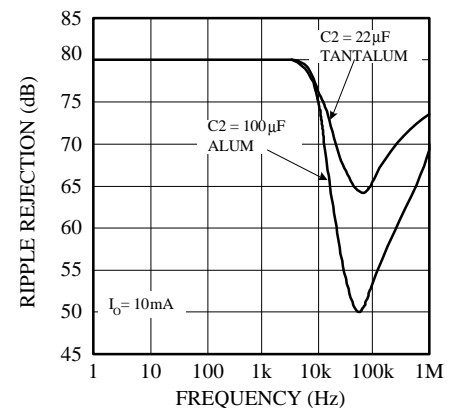
Output Impedance



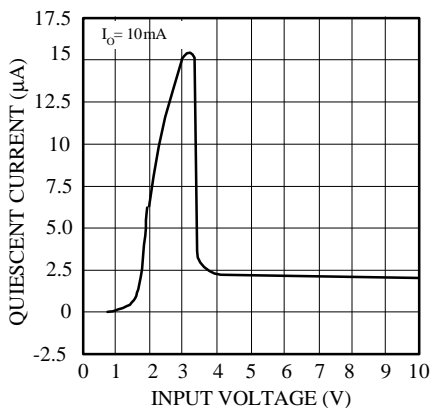
Peak Output Current



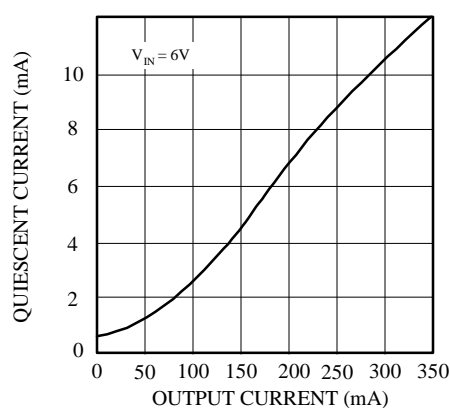
Ripple Rejection



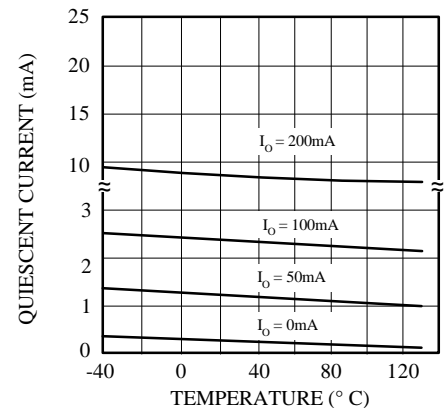
Quiescent Current



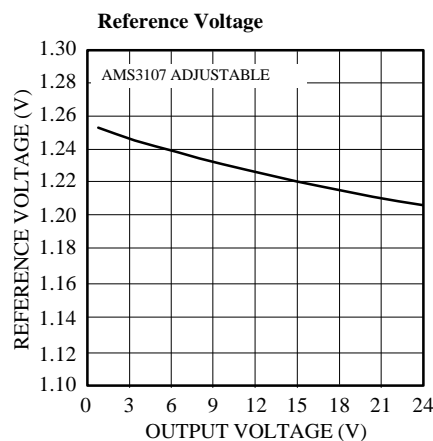
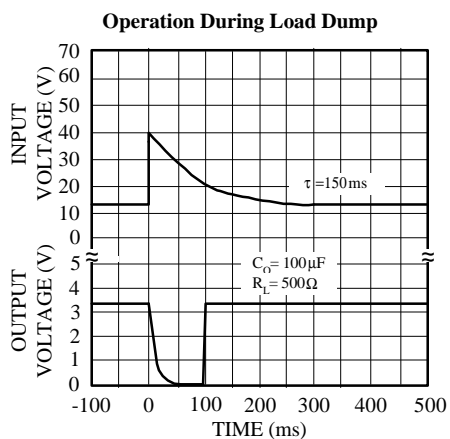
Quiescent Current



Quiescent Current



TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



APPLICATION NOTES

Definition of Terms

Dropout Voltage: The input-output voltage differential at which the circuit stops to regulate against further reduction in input voltage. Measured when the output voltage has dropped 100mV from the nominal voltage obtained at 1V input, dropout voltage is dependent upon load current and junction temperature.

Input Voltage: The DC voltage applied to the input terminal with respect to ground. **Input-Output Differential:** The voltage difference between the unregulated input voltage and the regulated output voltage for which the regulator will regulate.

Line Regulation: The change in output voltage for a change in the input voltage. The line regulation is measured under conditions of low dissipation or by using low duty cycle pulse testing such that the average chip temperature is not significantly affected.

Load Regulation: The change in output voltage for a change in load current at constant chip temperature.

Long term stability: Output voltage stability under accelerated life-test conditions after 1000 hours with maximum rated voltage and junction temperature.

Output Noise Voltage: The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Quiescent Current: That part of the positive input current that does not contribute to the positive load current. The regulator ground lead current.

Ripple Rejection: The ratio of the peak-to-peak input ripple voltage to the peak-to-peak output ripple voltage at specified frequency.

Temperature Stability of V_O : The percentage change in output voltage for a thermal variation from room temperature to either temperature extreme.

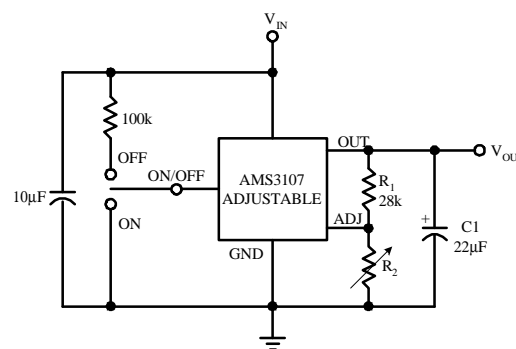
External capacitor

The AMS3107 series require an output capacitor of 10μF or greater to ensure device stability. Without the capacitor the device may oscillate.

Most type of tantalum or electrolytic capacitor can be used in the applications. A critical characteristic of the capacitors is an ESR value of 5Ω or less and a resonant frequency above 500kHz. The value of this capacitor can be increased without limits.

For higher loads, the value of the capacitor should be increased, specially when the output voltage is set for 2.5V or less. The AMS3107 lowest fixed output voltage value is 2.0V.

Typical application circuit (adjustable output)



Minimum Load

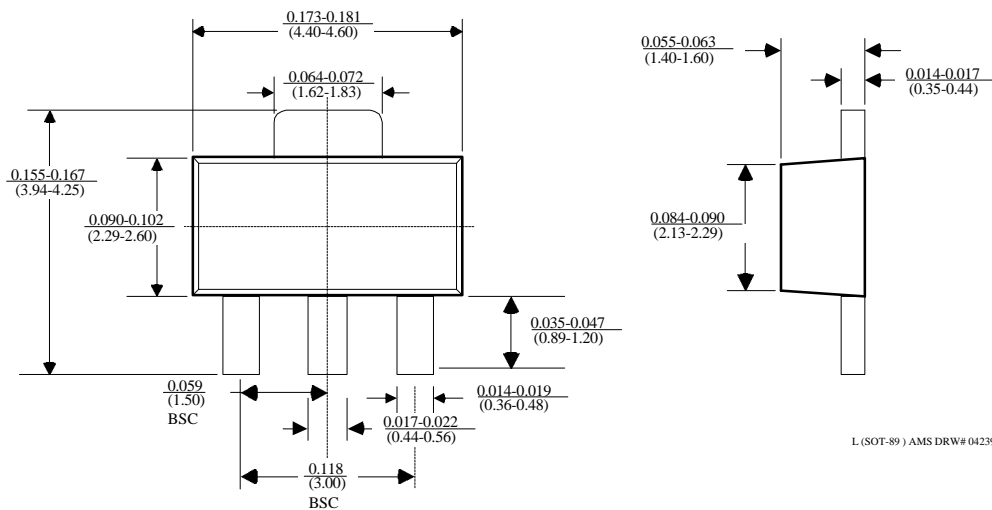
In circuits using the fixed output voltage versions, minimum load is not required. For circuits using the adjustable device, the value of R1 and R2 should be chosen such, that a current of approximately 40μA flows through the network. The reference voltage (1.235V) is measured between the adjust pin and V_{OUT} . The output voltage can be set by the two resistors R1 and R2 using the following equation:

$$V_O = V_{REF} \left(\frac{R_1 + R_2}{R_1} \right)$$

The value of R1 is recommended to be between 25kΩ to 30 kΩ, and the value of R2 will set the output voltage.

PACKAGE DIMENSIONS inches (millimeters) unless otherwise noted (Continued).

SOT-89 PLASTIC PACKAGE (L)



3 LEAD SOT-223 PLASTIC PACKAGE

