



# 6-Pin DIP Optoisolators Darlington Output (No Base Connection)

The MOC8030 and MOC8050 devices consist of gallium arsenide infrared emitting diodes optically coupled to monolithic silicon photodarlington detectors. The chip to Pin 6 base connection has been eliminated to improve output performance in high noise environments.

They are best suited for use in applications susceptible to high EMI levels.

- No Base Connection for Improved Noise Immunity
- High Collector–Emitter Breakdown Voltage — 80 Volts Minimum
- Higher Sensitivity to Low Input Drive Current
- **To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.**

### Applications

- Appliances, Measuring Instruments
- I/O Interfaces for Computers
- Programmable Controllers
- Portable Electronics
- Interfacing and coupling systems of different potentials and impedance
- Solid State Relays

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
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#### INPUT LED

Reverse Voltage	V <sub>R</sub>	3	Volts
Forward Current — Continuous	I <sub>F</sub>	60	mA
LED Power Dissipation @ T <sub>A</sub> = 25°C with Negligible Power in Output Detector Derate above 25°C	P <sub>D</sub>	120	mW
		1.41	mW/°C

#### OUTPUT DETECTOR

Collector–Emitter Voltage	V <sub>CEO</sub>	80	Volts
Collector Current Continuous	I <sub>C</sub>	150	mA
Emitter–Collector Voltage	V <sub>ECO</sub>	5	Volts
Detector Power Dissipation @ T <sub>A</sub> = 25°C with Negligible Power in Input LED Derate above 25°C	P <sub>D</sub>	150	mW
		1.76	mW/°C

#### TOTAL DEVICE

Isolation Surge Voltage <sup>(1)</sup> (Peak ac Voltage, 60 Hz, 1 sec Duration)	V <sub>ISO</sub>	7500	Vac(pk)
Total Device Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	250 2.94	mW mW/°C
Ambient Operating Temperature Range <sup>(2)</sup>	T <sub>A</sub>	–55 to +100	°C
Storage Temperature Range <sup>(2)</sup>	T <sub>stg</sub>	–55 to +150	°C
Soldering Temperature (10 sec, 1/16" from case)	T <sub>L</sub>	260	°C

1. Isolation surge voltage is an internal device dielectric breakdown rating.

For this test, Pins 1 and 2 are common, and Pins 4 and 5 are common.

2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

Preferred devices are Motorola recommended choices for future use and best overall value.

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**MOC8030**

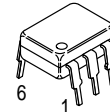
[CTR = 300% Min]

**MOC8050**

[CTR = 500% Min]

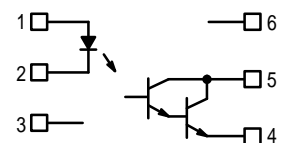
Motorola Preferred Devices

### STYLE 3 PLASTIC



STANDARD THRU HOLE  
CASE 730A–04

### SCHEMATIC



- PIN 1. LED ANODE  
2. LED CATHODE  
3. N.C.  
4. EMITTER  
5. COLLECTOR  
6. N.C.

# MOC8030 MOC8050

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)<sup>(1)</sup>

Characteristic	Symbol	Min	Typ <sup>(1)</sup>	Max	Unit
<b>INPUT LED</b>					
Reverse Leakage Current (V <sub>R</sub> = 3 V)	I <sub>R</sub>	—	0.05	10	μA
Forward Voltage (I <sub>F</sub> = 10 mA)	V <sub>F</sub>	—	1.15	2	Volts
Capacitance (V <sub>R</sub> = 0 V, f = 1 MHz)	C	—	18	—	pF

## PHOTODARLINGTON (T<sub>A</sub> = 25°C and I<sub>F</sub> = 0, unless otherwise noted)

Collector–Emitter Dark Current (V <sub>CE</sub> = 60 V)	I <sub>CEO</sub>	—	—	1	μA
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 1 mA)	V <sub>(BR)CEO</sub>	80	—	—	Volts
Emitter–Collector Breakdown Voltage (I <sub>E</sub> = 100 μA)	V <sub>(BR)ECO</sub>	5	—	—	Volts

## COUPLED (T<sub>A</sub> = 25°C unless otherwise noted)

Collector Output Current (V <sub>CE</sub> = 1.5 V, I <sub>F</sub> = 10 mA)	MOC8030 MOC8050	I <sub>C</sub> (CTR) <sup>(2)</sup>	30 (300) 50 (500)	— —	— —	mA (%)
Isolation Surge Voltage <sup>(3,4)</sup> , 60 Hz Peak ac, 5 Second		V <sub>ISO</sub>	7500	—	—	Vac(pk)
Isolation Resistance <sup>(3)</sup> (V = 500 V)		R <sub>ISO</sub>	—	10 <sup>11</sup>	—	Ohms
Isolation Capacitance <sup>(3)</sup> (V = 0 V, f = 1 MHz)		C <sub>ISO</sub>	—	0.2	—	pF

## SWITCHING

Turn-On Time	V <sub>CC</sub> = 10 V, R <sub>L</sub> = 100 Ω, I <sub>F</sub> = 5 mA <sup>(5)</sup>	t <sub>on</sub>	—	3.5	—	μs
Turn-Off Time		t <sub>off</sub>	—	95	—	
Rise Time		t <sub>r</sub>	—	1	—	
Fall Time		t <sub>f</sub>	—	2	—	

1. Always design to the specified minimum/maximum electrical limits (where applicable).
2. Current Transfer Ratio (CTR) = I<sub>C</sub>/I<sub>F</sub> × 100%.
3. For this test, LED Pins 1 and 2 are common and Phototransistor Pins 4 and 5 are common.
4. Isolation Surge Voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating.
5. For test circuit setup and waveforms, refer to Figure 9.

## TYPICAL CHARACTERISTICS

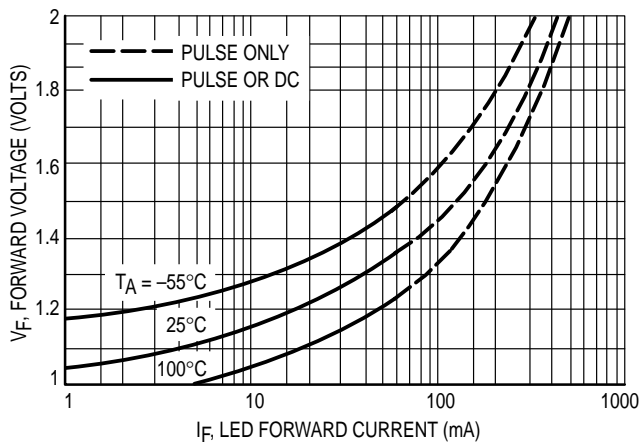


Figure 1. LED Forward Voltage versus Forward Current

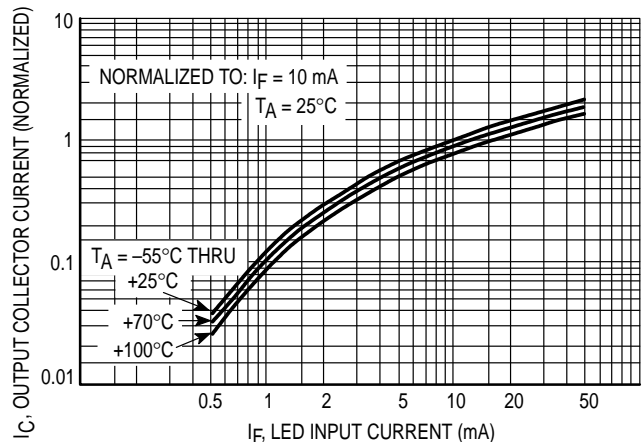


Figure 2. Output Current versus Input Current

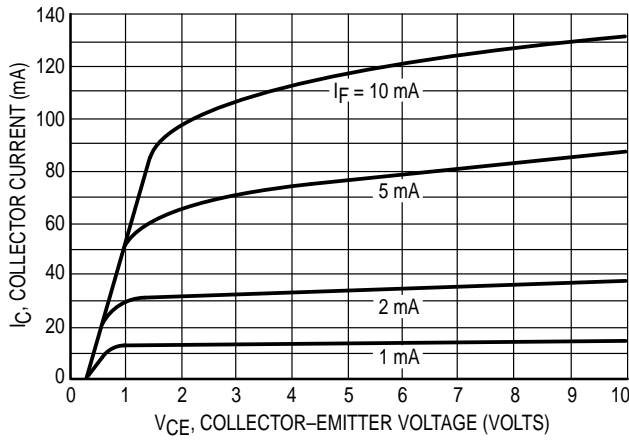


Figure 3. Collector Current versus Collector-Emitter Voltage

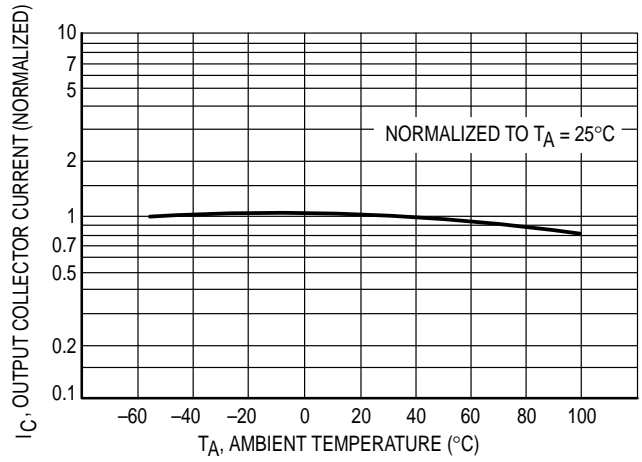


Figure 4. Output Current versus Ambient Temperature

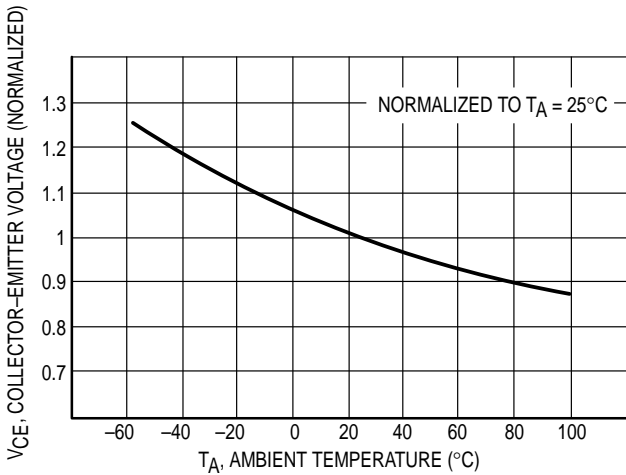


Figure 5. Collector-Emitter Voltage versus Ambient Temperature

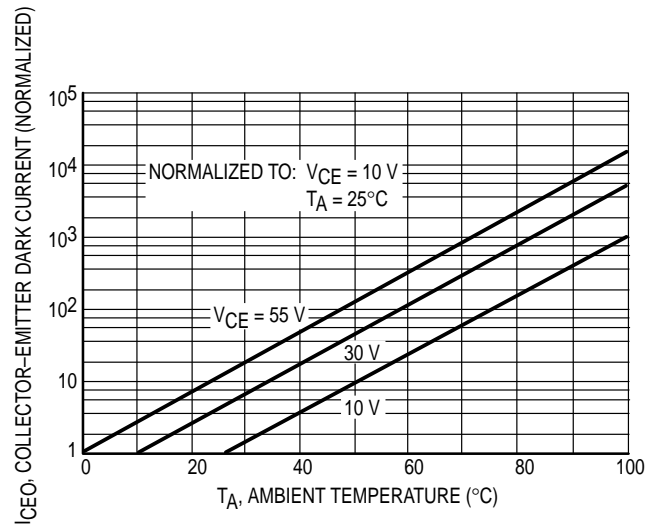


Figure 6. Collector-Emitter Dark Current versus Ambient Temperature

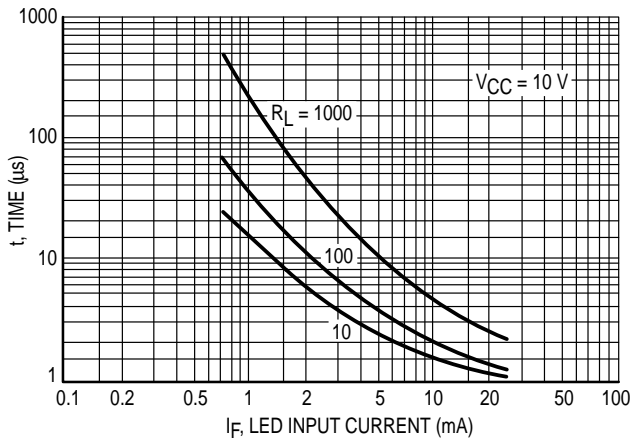


Figure 7. Turn-On Switching Times (Typical Values)

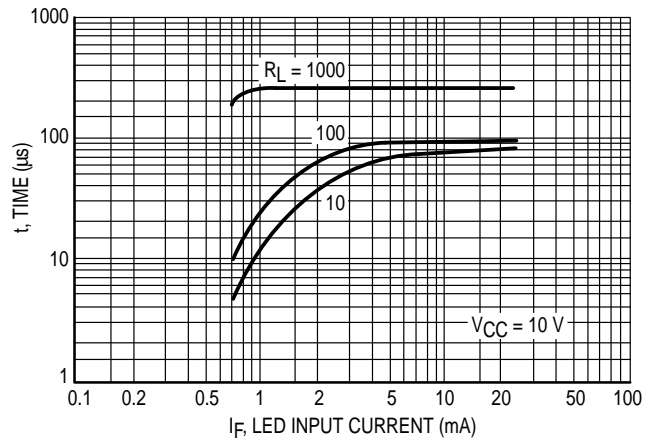


Figure 8. Turn-Off Switching Times (Typical Values)

# MOC8030 MOC8050

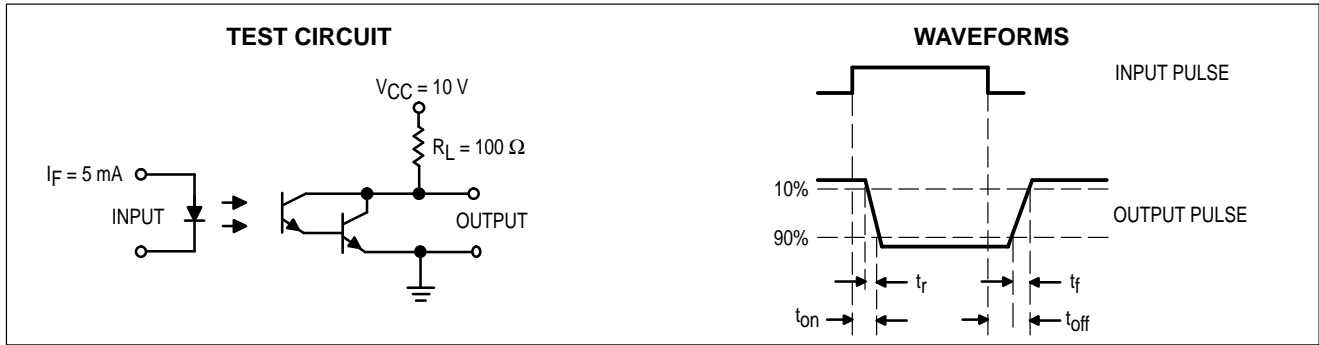
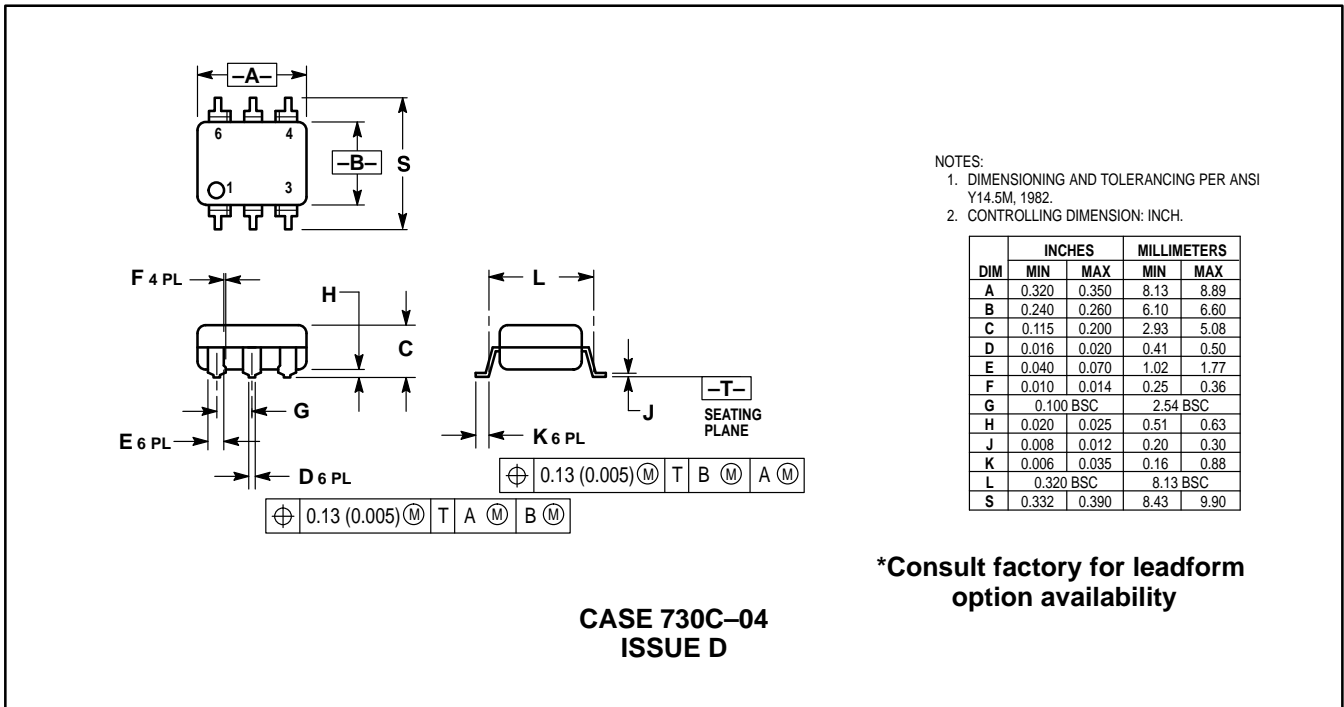
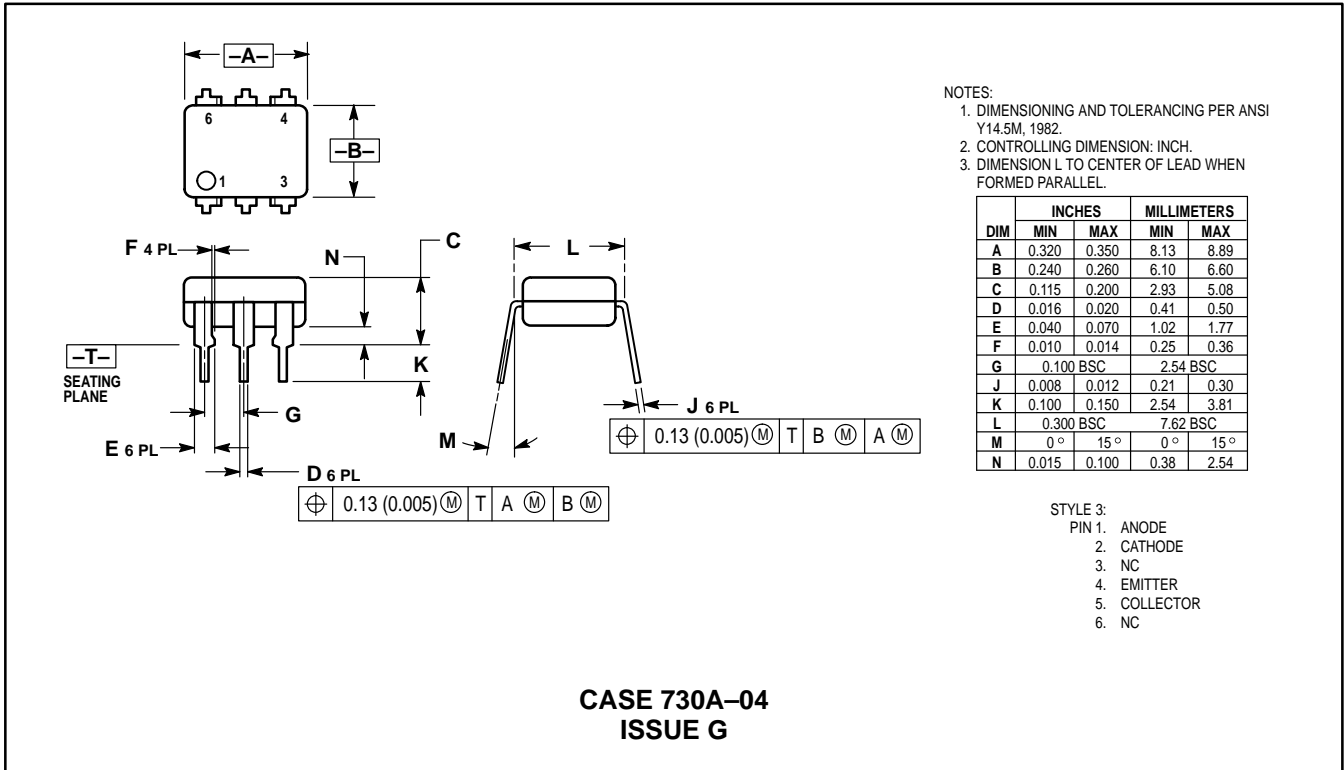
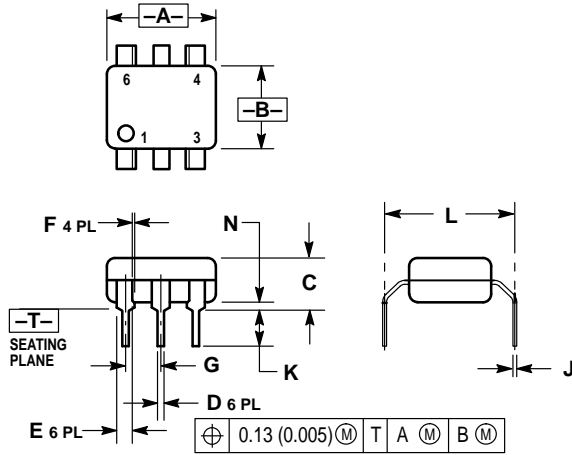


Figure 9. Switching Time Test Circuit and Waveforms

PACKAGE DIMENSIONS



# MOC8030 MOC8050



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.320	0.350	8.13	8.89
B	0.240	0.260	6.10	6.60
C	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
E	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
J	0.008	0.012	0.21	0.30
K	0.100	0.150	2.54	3.81
L	0.400	0.425	10.16	10.80
N	0.015	0.040	0.38	1.02

**\*Consult factory for leadform option availability**

**CASE 730D-05  
ISSUE D**

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